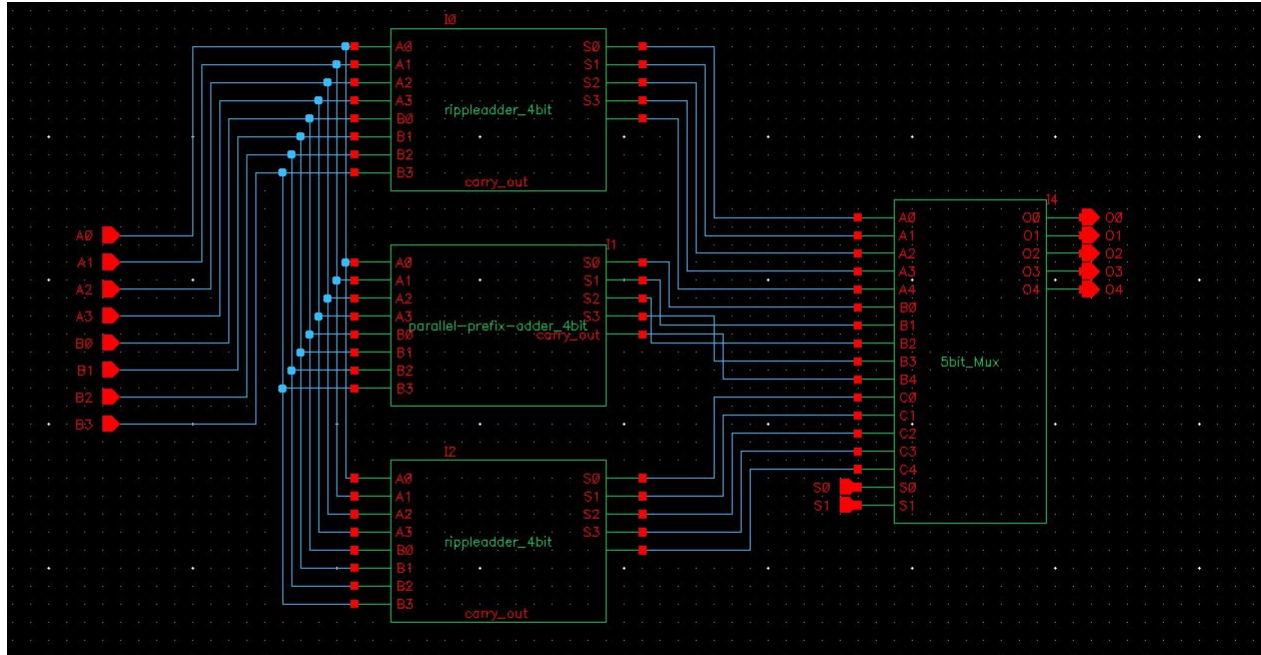


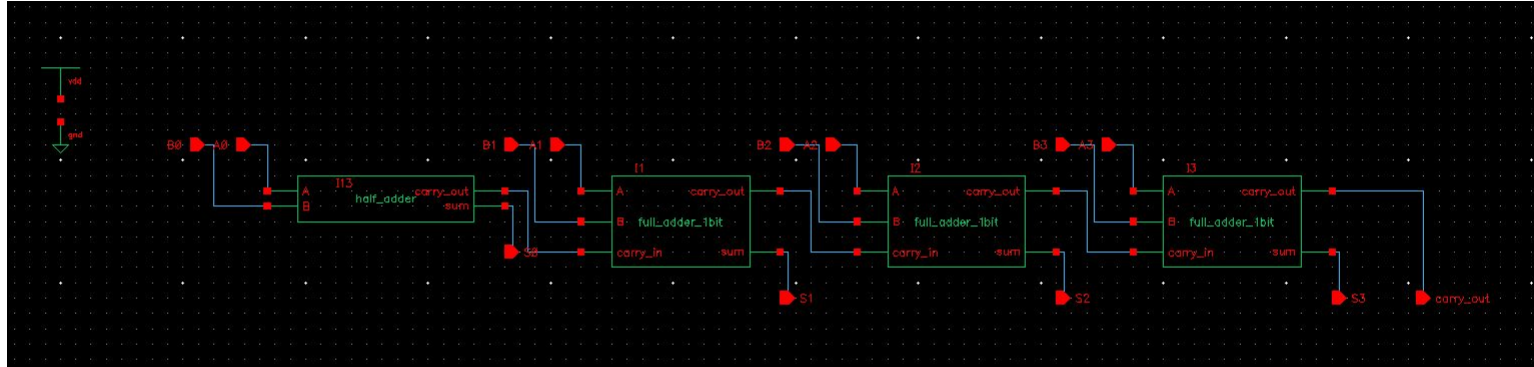
Three 4-bit Adders

Tijmen Van Der Beek

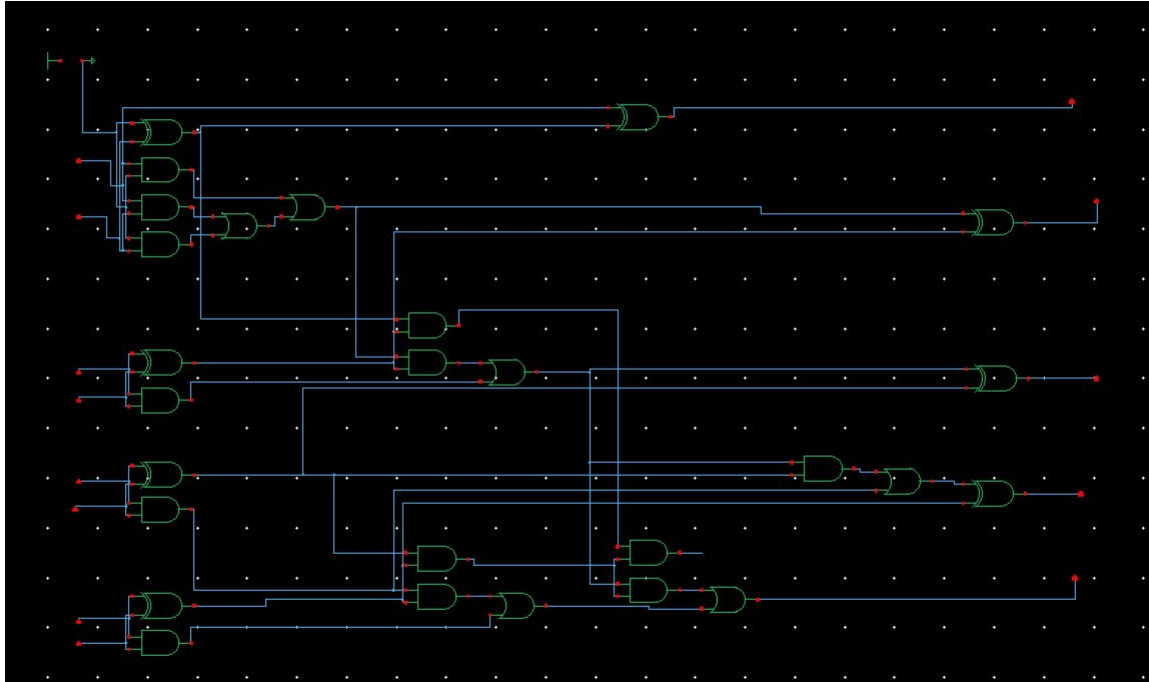
Top Level Design



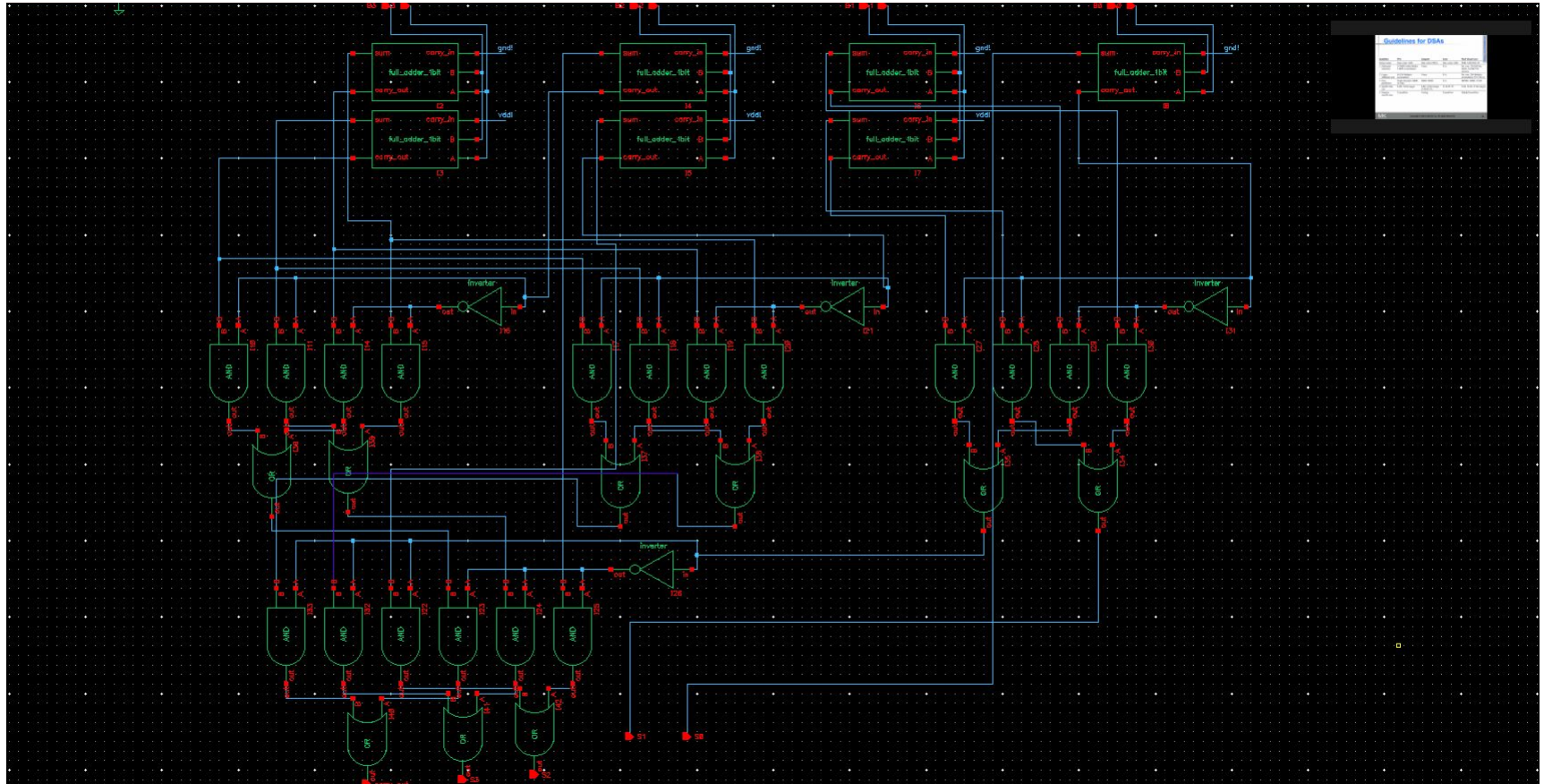
Ripple Adder



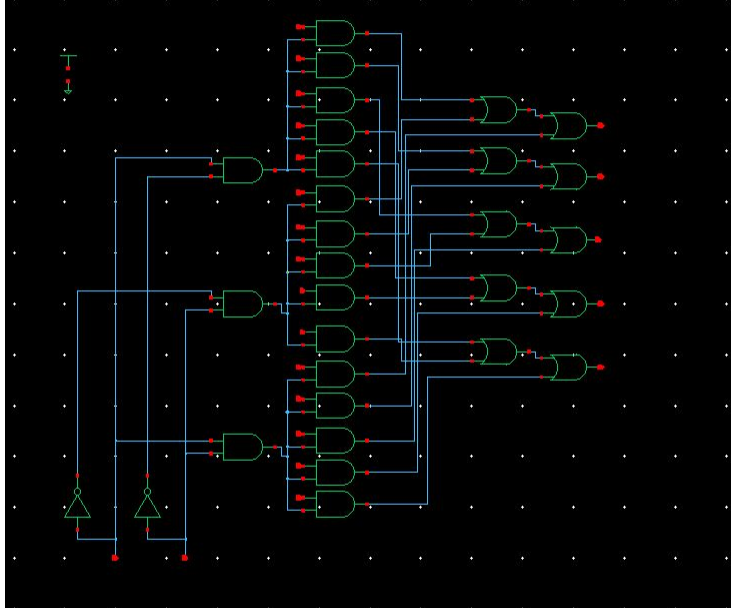
Parallel-Prefix Adder



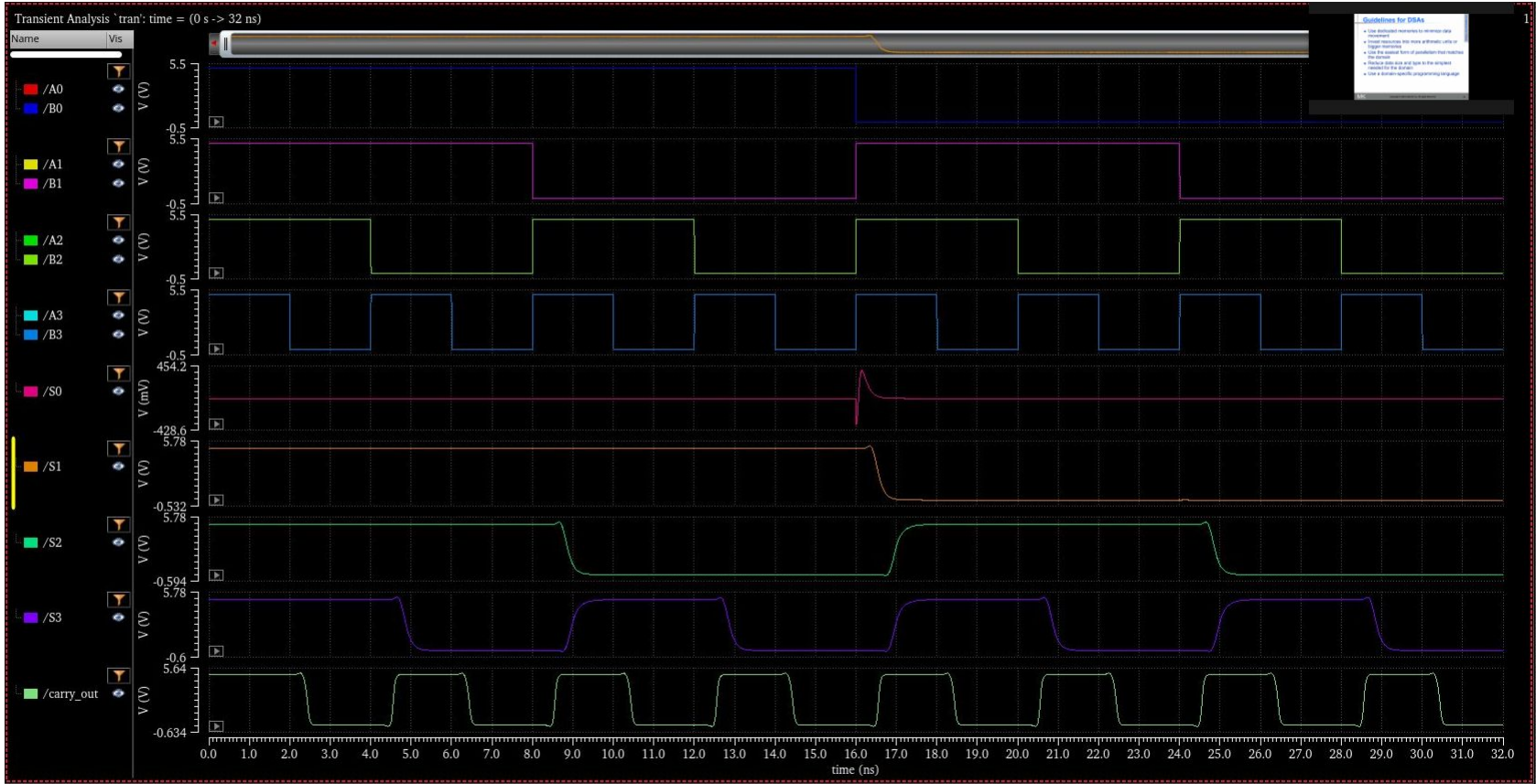
Conditional Sum Adder



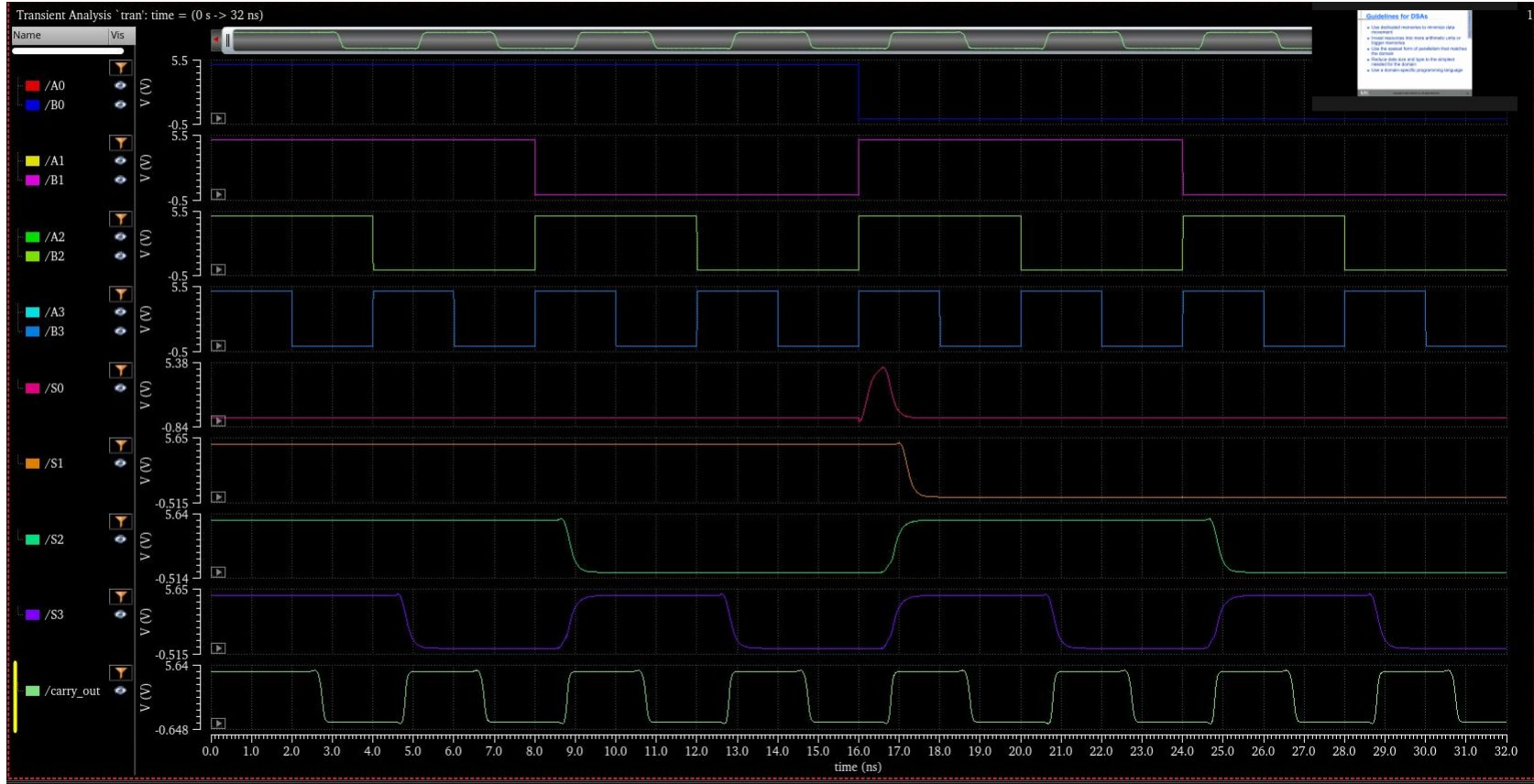
Multiplexer



Ripple Adder Results



Parallel-Prefix Adder Results



Conditional Sum Adder Results

