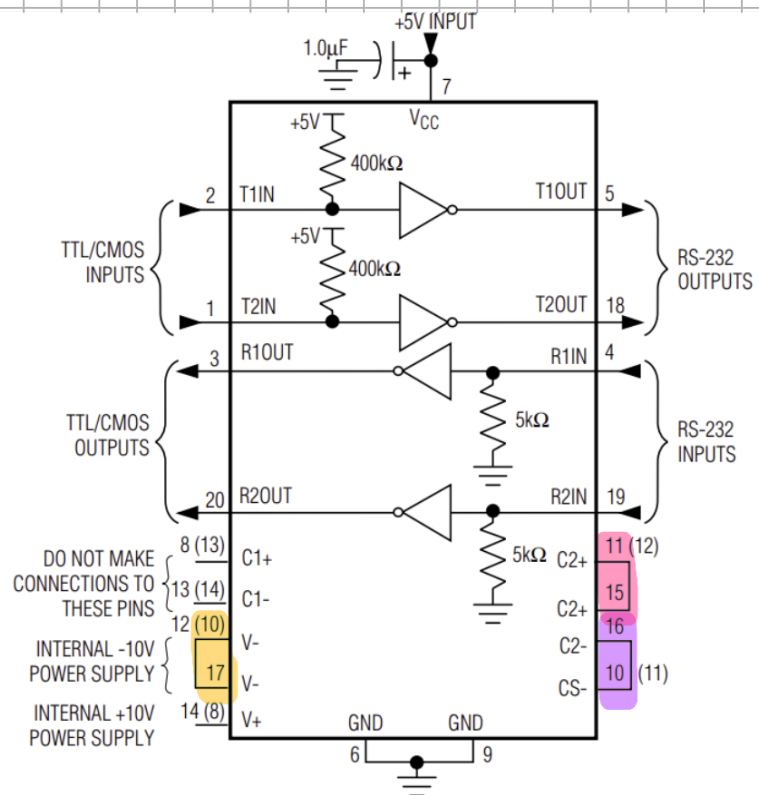
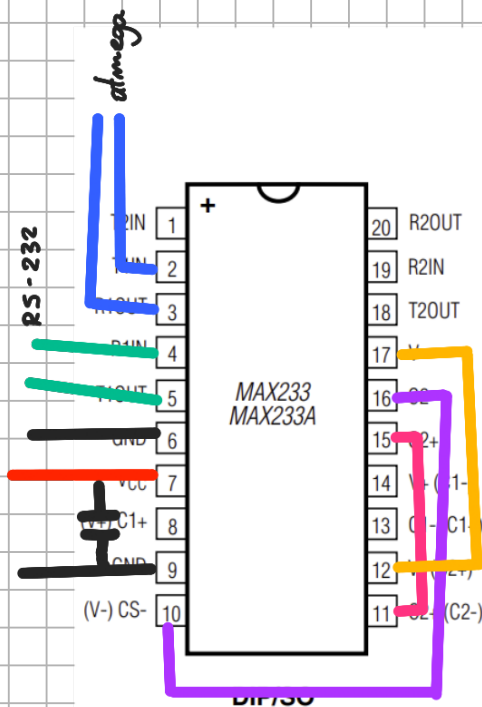
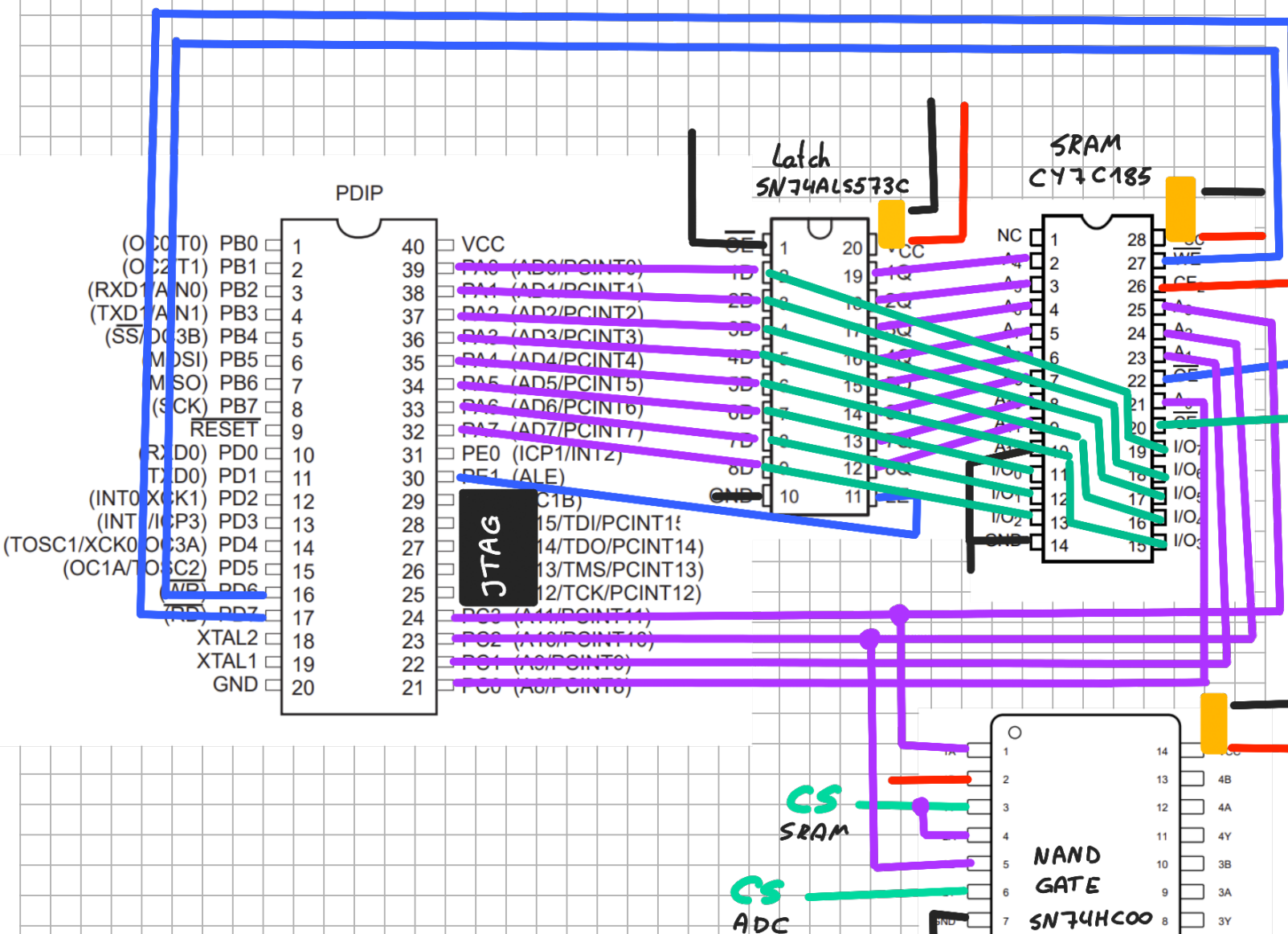


Dag 1



ARE FOR SO PACKAGE.

Dag 2



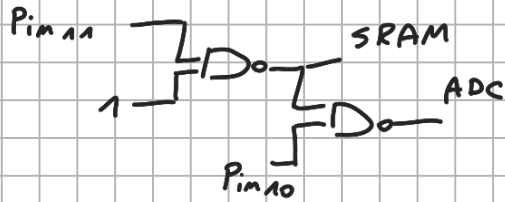
Unit	Hex	Binary	4321	365	4321
OLED	From 0x1000	0001	0000	0000	0000
	To 0x13FF	0001	0011	1111	1111
ADC	From 0x1400	0001	0100	0000	0000
	To 0x17FF	0001	0111	1111	1111
SRAM	From 0x1800	0001	1000	0000	0000
	To 0x1FFF	0001	1111	1111	1111
CS when:		0001	1XXX	XXXX	XXXX

ADC CS when: 01xx xxxx xxxx



A og B : input

Y : output



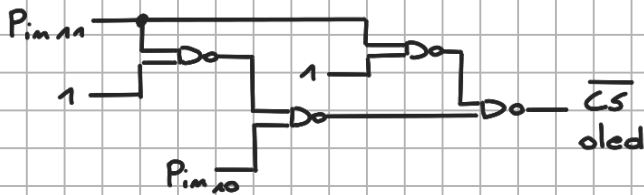
Pim11	Pim10	ADC $\overline{CS}$	SRAM $\overline{CS}$
0	0	1	1
0	1	0	1
1	0	1	0
1	1	1	0

Dag 4 / 5

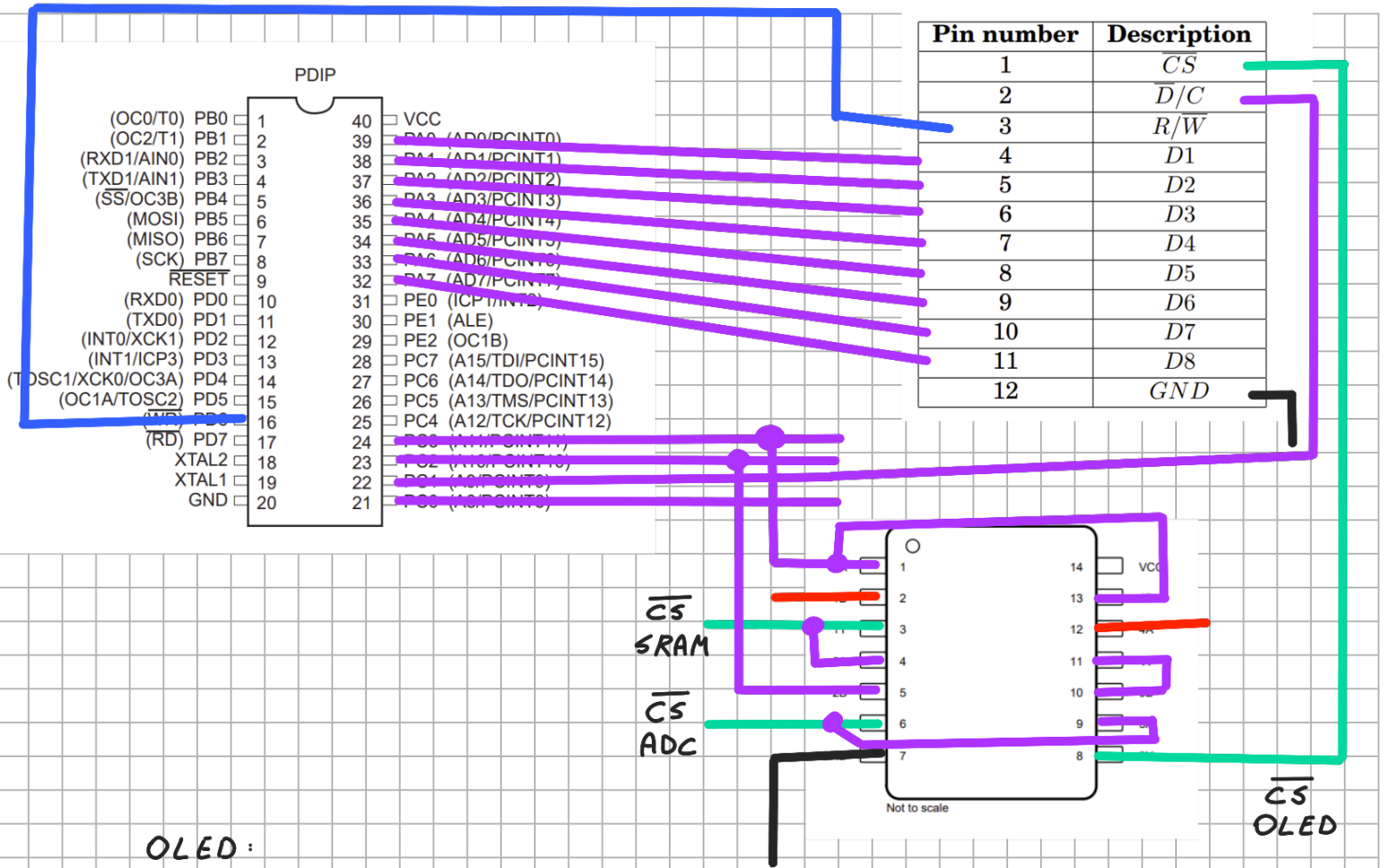
	Hex		Binary
OLED command	From	0x1000	0000100000000000
	To	0x11FF	0000100001111111
OLED data	From	0x1200	0000100100000000
	To	0x13FF	0000100111111111

Pim9	Pim8	D/ $\overline{C}$	high : data low : command
0	0	0	
0	1	0	
1	0	1	
1	1	1	

" Pim9 → D/ $\overline{C}$



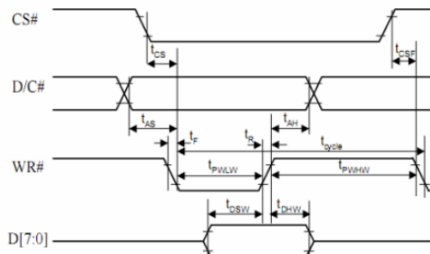
Pim11	Pim10	OLED $\overline{CS}$
0	0	0
0	1	1
1	0	1
1	1	1



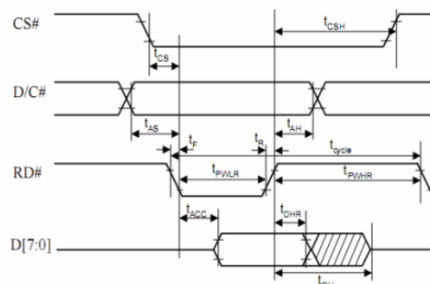
OLED :

#### 8080-series parallel interface characteristics

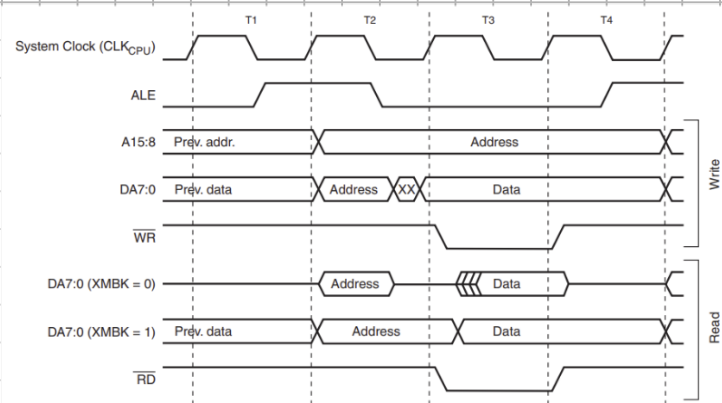
##### Write Cycle



##### Read cycle

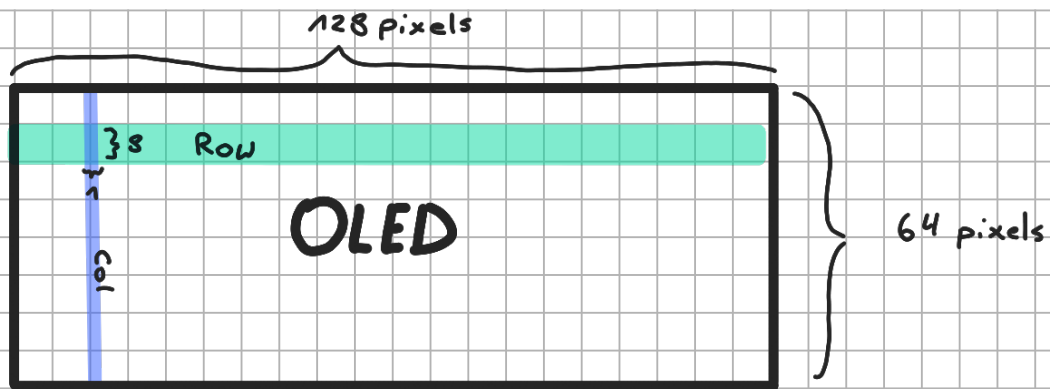


Atmega :



Data er på databussen når WR signalet går fra lav til høy.

Data blir skrevet til oleden på rising edge på WR signalet.



8 rows  
128 columns  
hvert column har 8 pixels

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Dag 6