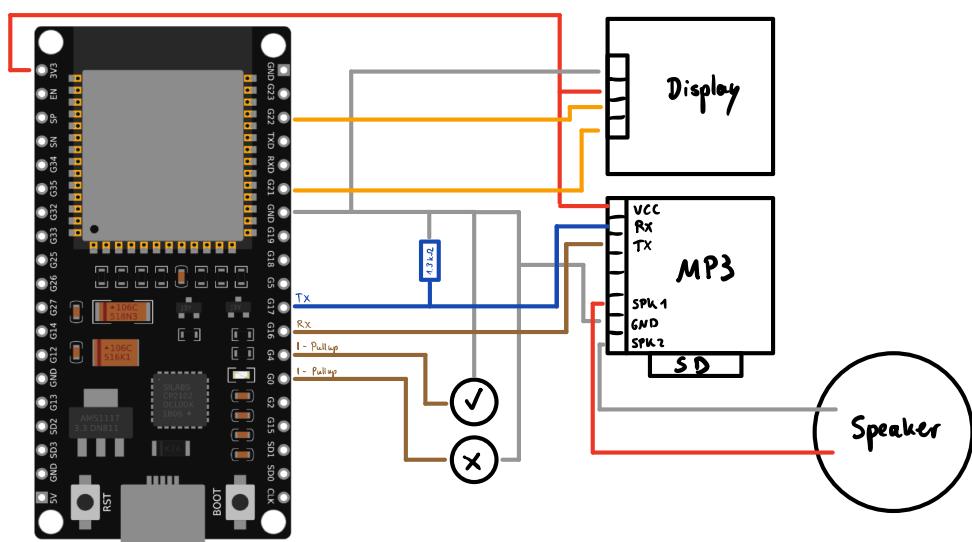
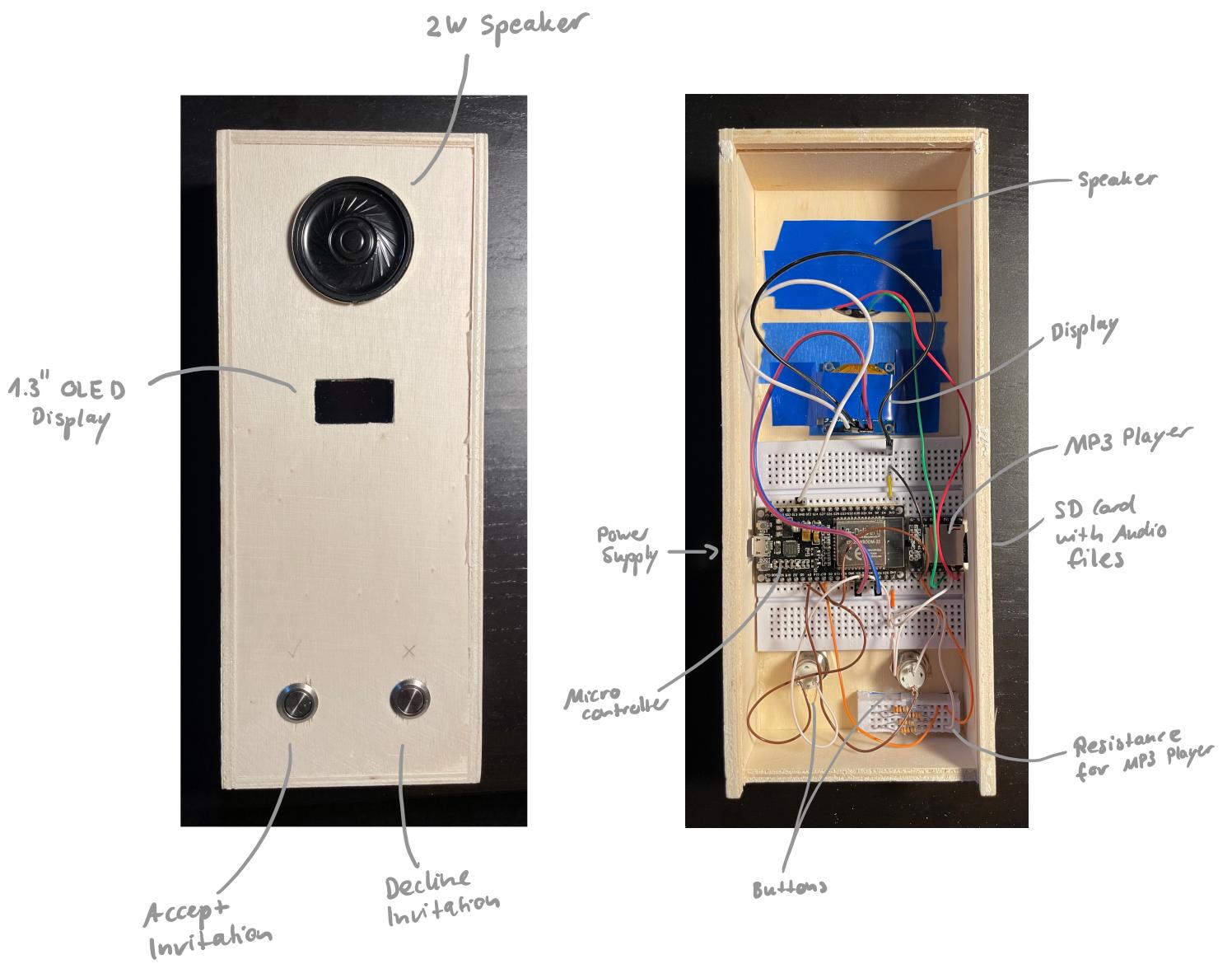


Circuit Design





invite:

- 0 • False
- 1 • Outstanding
- 2 • Accepted
- 3 • Declined
- 0 • Disregarded