	HW 5 4402 4/16/18
CSC 4402 HW5 O(a) (SELECT sid FROM s WHERE rating (= 2) INTERSECT (SELECT sid	4402
CSC 4402 HW5 (SELECT Sid FROM S WHERE rating (= 2) INTERSECT (SELECT Sid	4/16/18
(SELECT sid FROM S WHERE rating <= 2) INTERSECT (SELECT sid	
FROM S WHERE rating <= 2) INTERSECT (SELECT Sid	
WHERE rating <= 2) INTERSECT (SELECT sid	
INTERSECT (SELECT SID	
(SELECT sid	
WHERE bid = 10)	
(b)	
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2	(a) Yes, the given graph is S conflict serializable because there are no directed cycles. (b) TO, T2, T1, T3, T4
3	(a) ToT, and T, To
0	initially 0 0 initially 0 0 after To 0 1 after To 1 0 after To 1 0 Consistency Met: A=O v B=O=TvF=T A=O vB=O=FvT=T
	(b) To read(A) read(B) read(B) if A=0 then B=B+1 Write(B) To To To Tead(B) read(B) read(A) vead(A) vead(A) vead(A)

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(4)	This schedule is S conflict serializable because there is not a cycle. Read(A) and Read(B) cannot be executed until the end.
	(a) TI T2 T3 LX(A) LX(C) LX(B) Read(H) Read(C) Read (B) LX(B) Write(C) Write(B) Read(B) LX(A) LX(C) Write(A) Read(H) Read(C) Write(B) Write(H) Write(C)