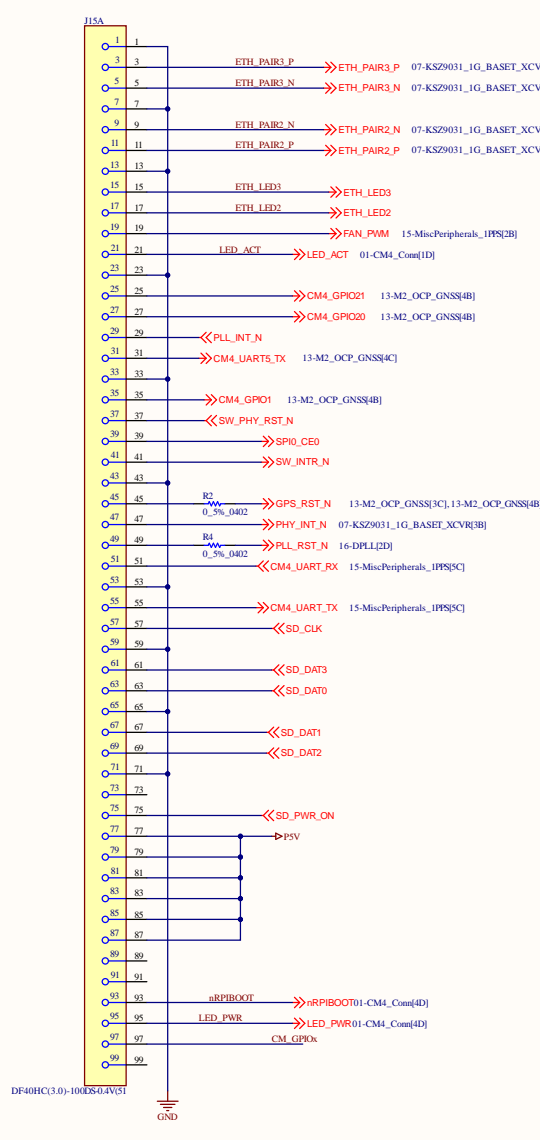
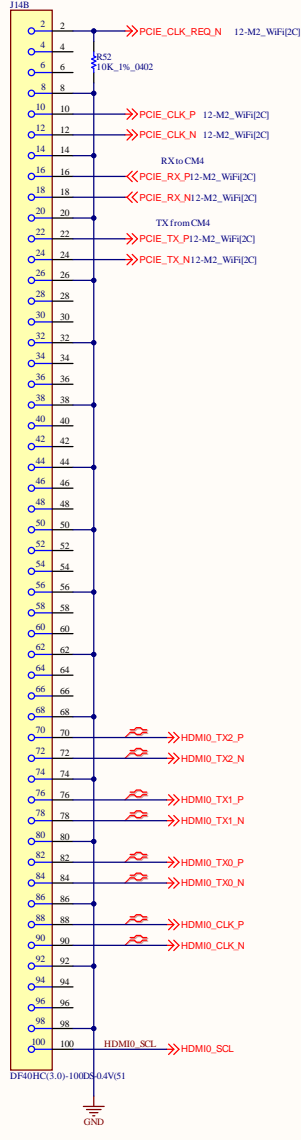
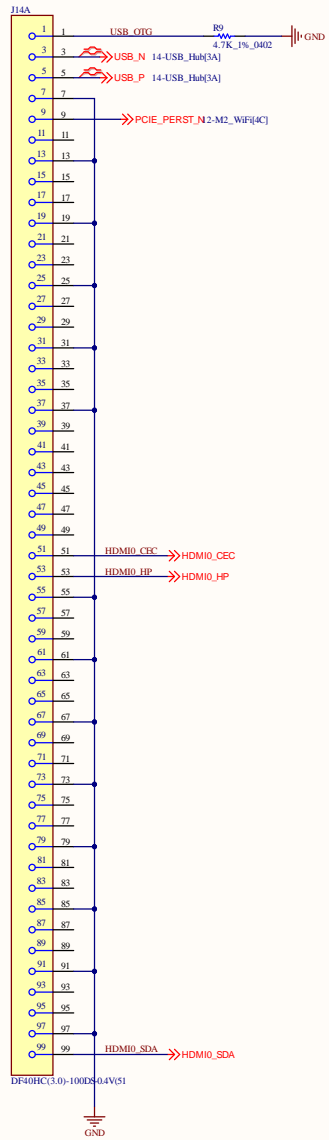
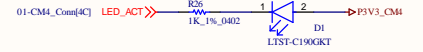
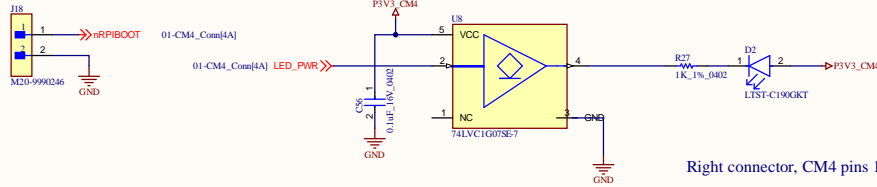
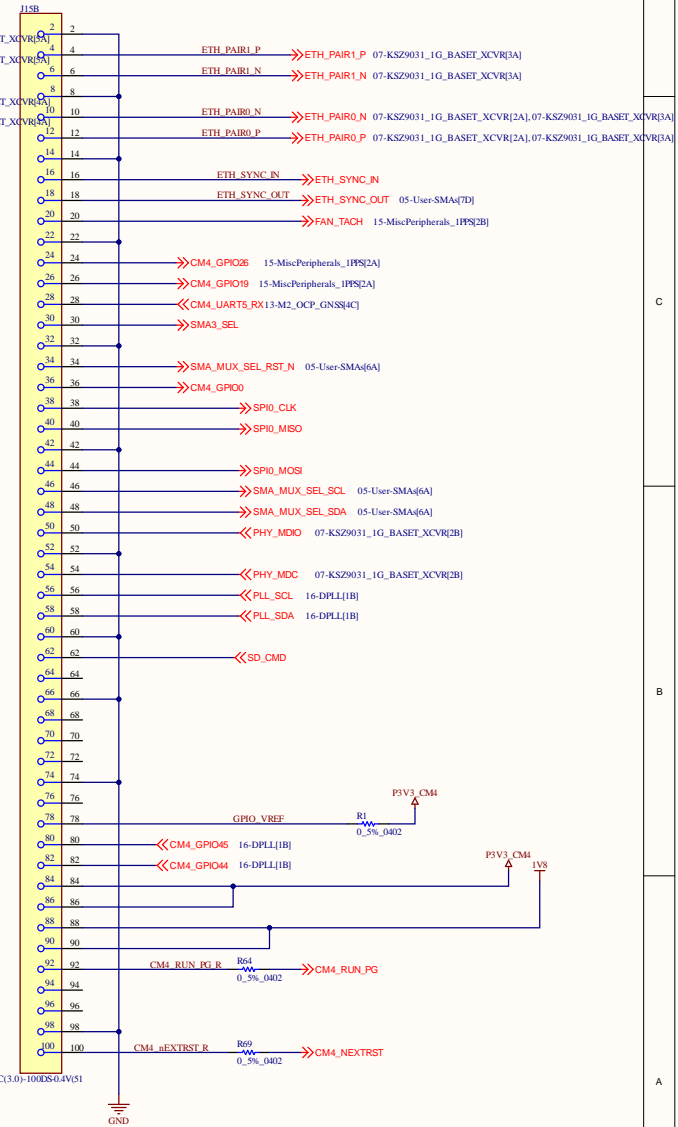
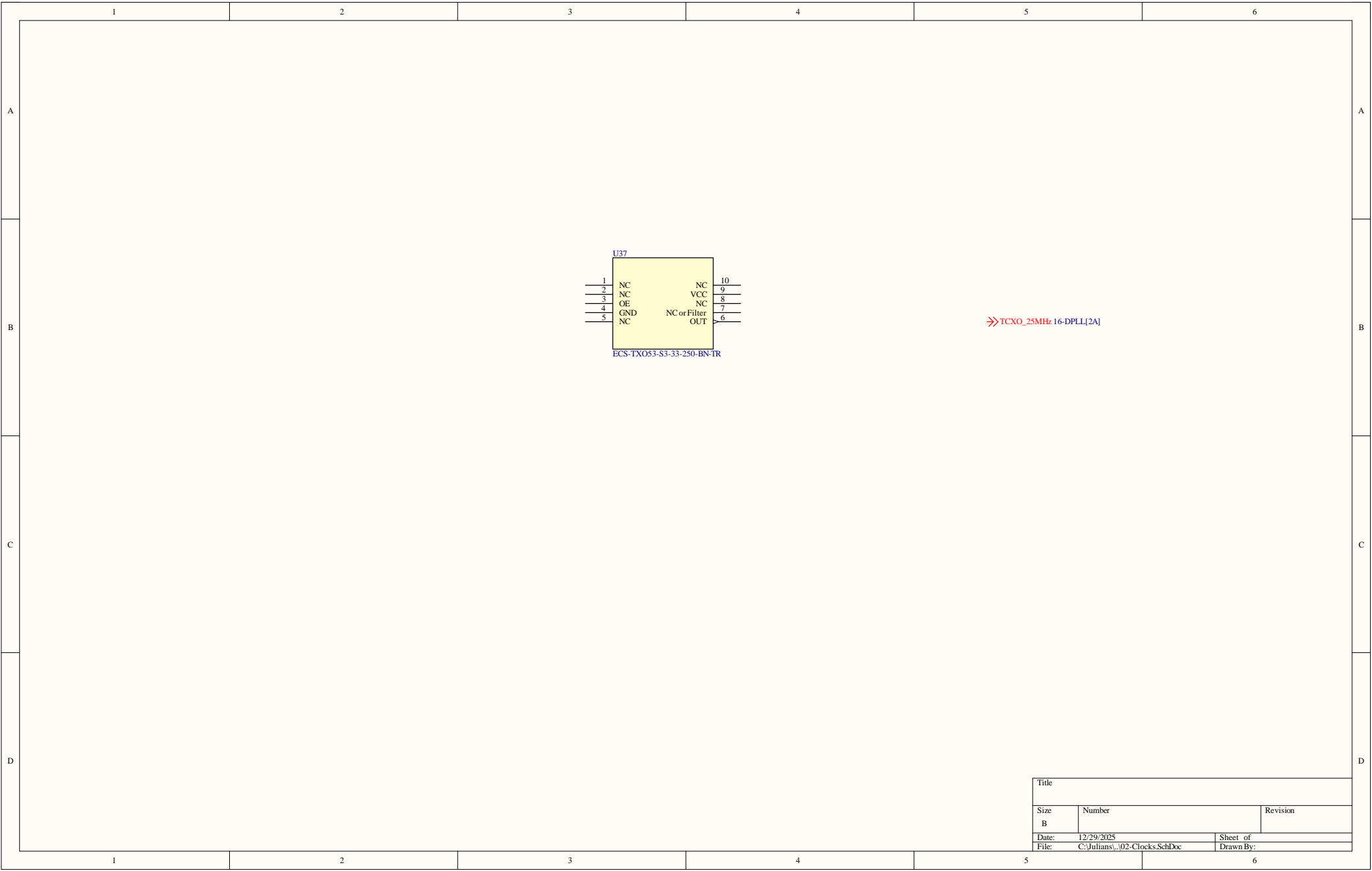


Left connector, CM4 pins 101-200

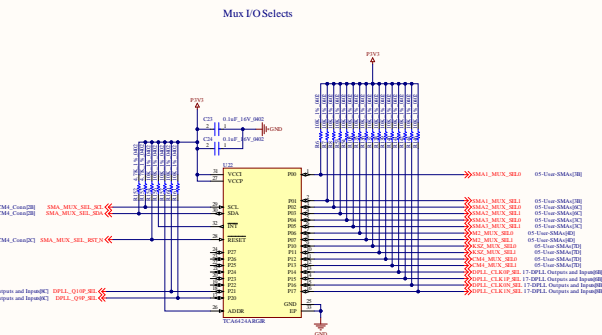


Right connector, CM4 pins 1-100

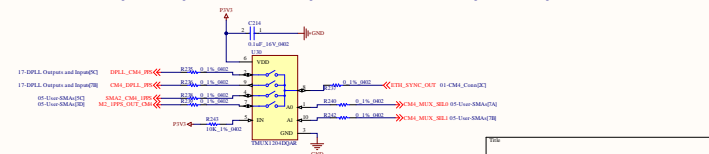




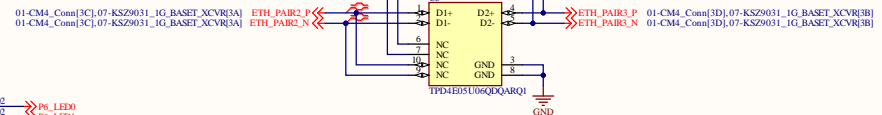
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Size B	Number	Revision
Date:	12/29/2025	Sheet of
File:	C:\Julians\...02-Clocks.SchDoc	Drawn By:



SMA2 -> CM4 1PPS Raw or DPLL Input or DPLL Output



Title		
Size D	Number	Revision
Date 12-29-2025	Sheet of	
File C:\Jalisco_05-User-SMA.schDoc	Drawn By:	



# M.2 E-Key for WiFi

<https://developer.embeddedartists.com/docs-m2/integration/pinning/>

A

B

C

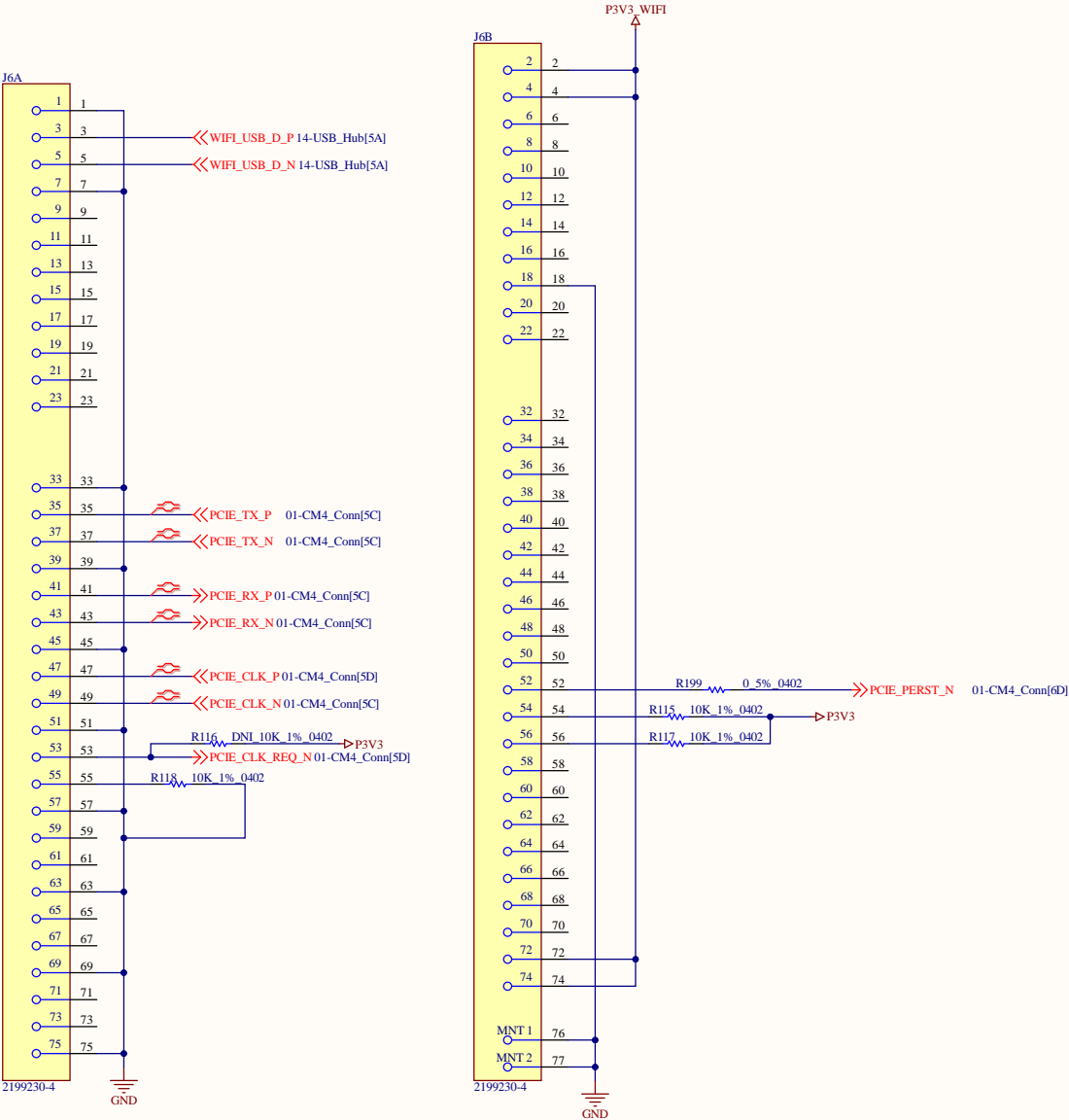
D

A

B

C

D

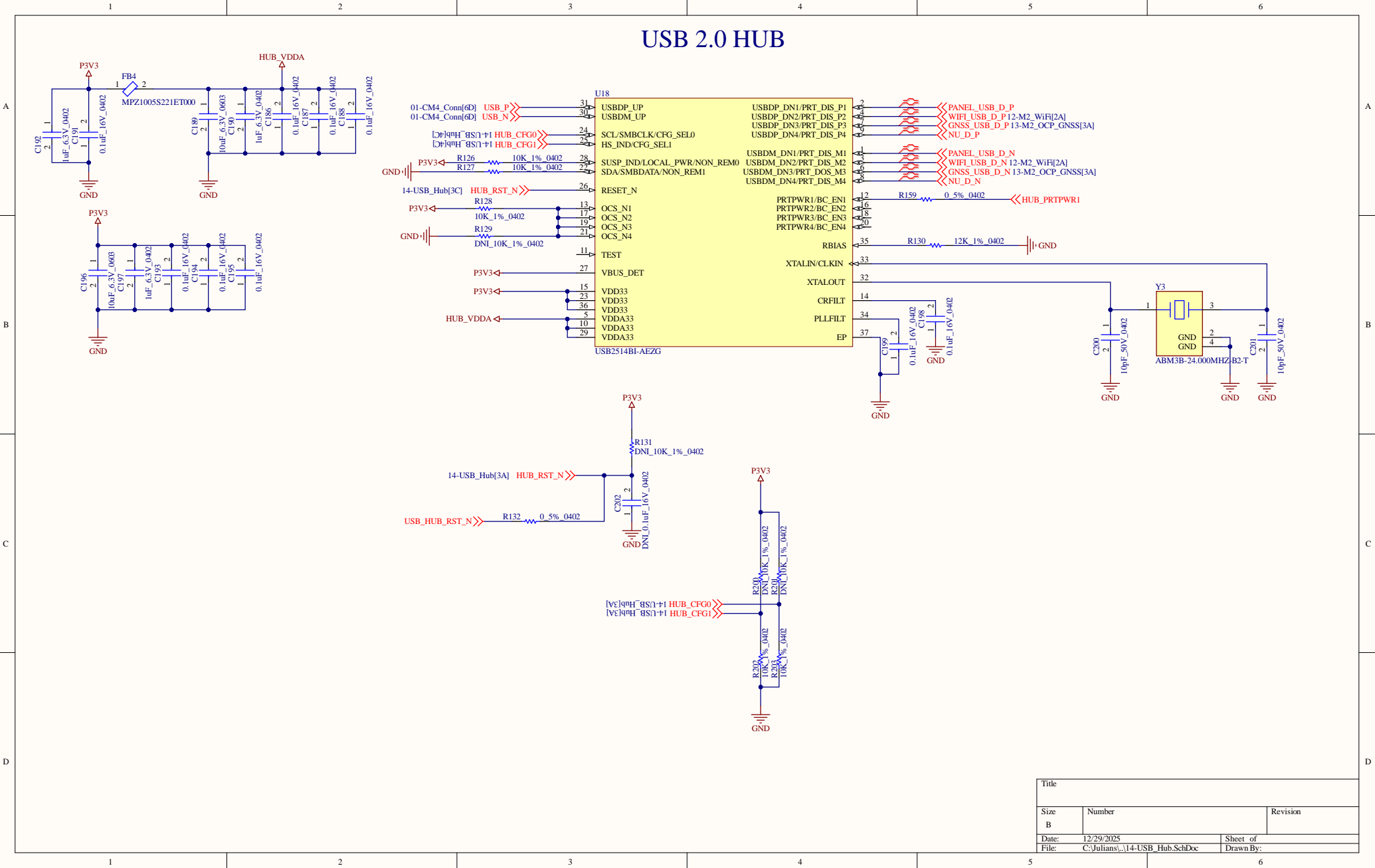


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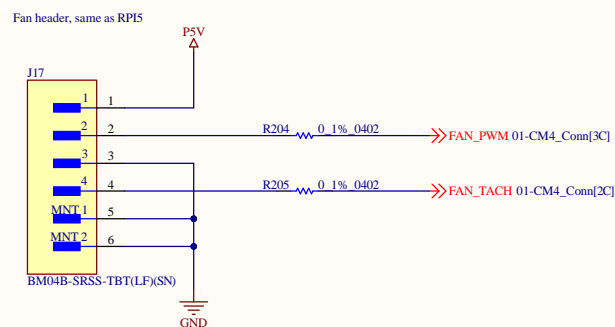
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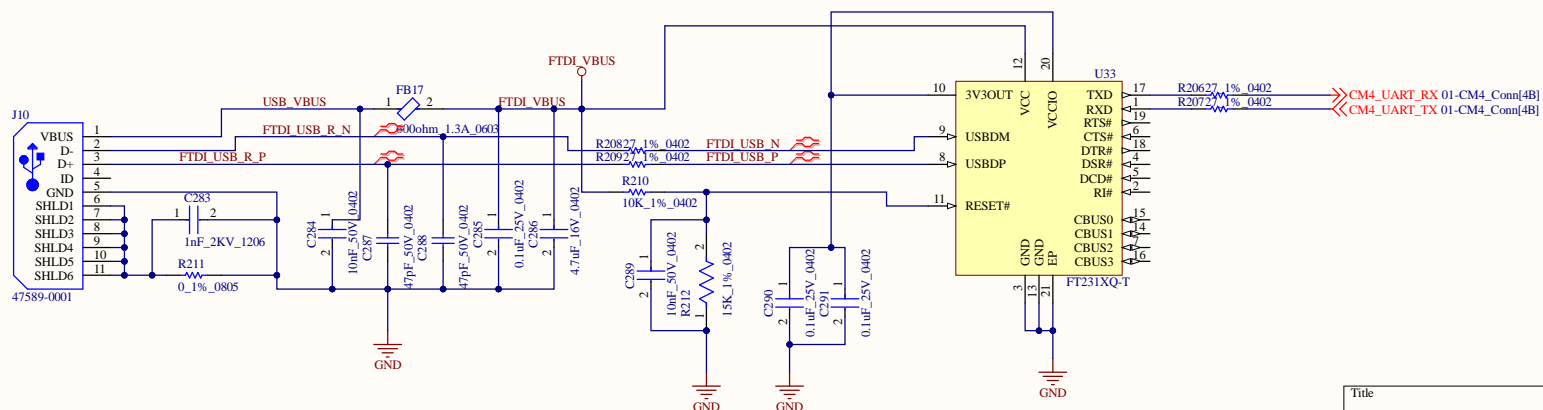
USB 2.0 HUB



Title		
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## USB to UART



Title		
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File: C:\Julians\15-MiscPeripherals_1PPS	Drawn By:	



