

0.1 From Ethernet to Æthernet

It would be a mistake to assume conventional network concepts and terminology that you already know and love will remain unscathed in this project. While we have no intention of reinventing the wheel, some new concepts and terminology will be necessary in order to escape the quagmire of incrementalism of the last five decades.

0.1.1 Ethernet was Born

The Original concepts of Ethernet, developed 50 years ago, help us understand the thinking, theoretical concepts, and guide us to practical implementations. It is important to understand the design philosophy, so we can learn the most from the intuition that created the Ethernet revolution. It is instructive to trace the initial intellectual and conceptual steps when Ethernet was first developed, and make sure we are not missing some invaluable intuition.

The original Ethernet was a single coax cable (photon cavity) Half-Duplex ‘Bus’: alternating between listening and transmitting in ‘slots’ on the bus.

These temporal ‘slots’ were time *intervals*: controlled and measured against a local oscillator (often a crystal), which had their own drift and stability characteristics. In a half-duplex world, such as on a single coax cable, this meant that the Transceiver (Transmitter + Receiver) would transmit for half the ‘time’ and listen for the other half the ‘time’, and be able to detect collisions (the receiver monitors what it itself is transmitting and compares it to what it is receiving to see.

The transmitting station is immediately provided with electrical (signal) feedback which lets it see (a) that it’s transmitter was working, and (b) what other receivers might be seeing. From a Shannon perspective, we call this *Initial Information-Feedback (IIF)* and reserve the definition of *Perfect Information Feedback (PIF)* for the case where the SerDes at the other end is reflecting what it sees. See section XX for the full theory behind the Dual Back-to-Back (DB2B) Shannon model works.

0.1.2 Ethernet Evolves

Ethernet rapidly evolved to a full duplex situation where there are two separate connections to the media. One in the transmit direction, and one in the receive direction. However, if one direction is working, and the other direction is not, we could always (in Æthernet) revert back to communicating independently on each cable. This may not have

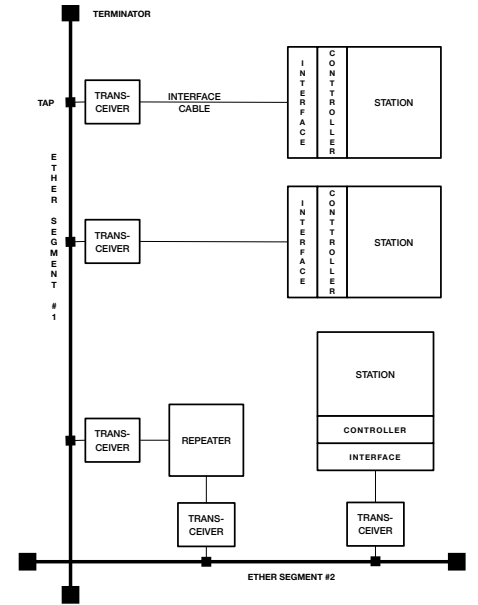


Figure 1: Original Ethernet Concepts

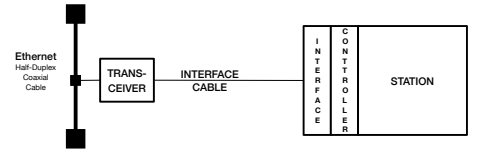


Figure 2: Ethernet Components

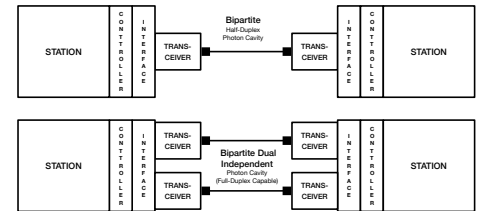


Figure 3: Chiplet Æthernet: HDX/FDX

the ideal performance characteristics, but remains a valuable redundancy tool to locally diagnose (and report) unidirectional errors, and flakey fiber connections. This is effectively a dual redundant ‘communication’ verification tool that can be algorithmically self diagnosing, perhaps in combination to Link training in Modern Ethernet.

0.2 AEthernet Configuration

We begin with two cells, and extend to three. This is the minimum irreducible graph for healing around a broken link. Links can be broken in both directions, or one direction at a time (unidirectional failures)

0.3 Chiplet XPU's

0.4 Configured Links

We go from Chiplet Servers to something more generalized - XPU's

This 3×3 Tile is the basic fault-tolerant Tile.

Unactivated Links go from being dead to being alive by exchanging configuration packets. These establish the *direction* of the links from

0.5 From Repeaters to Switches to Routers, and Back to Repeaters

Bob Metcalfe's concept of a repeater was like what Heaviside discovered – a device to take a fading signal and boost it to recreate a strong signal so as to propagate further. That was the simplistic ‘engineers’ view of how it worked. The ‘physicist’s view’ was far more interesting – they took a transmission line model, where characteristic impedance $Z_0 = \sqrt{\frac{L_0}{C_0}}$.

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0.6 Minimum Triangle

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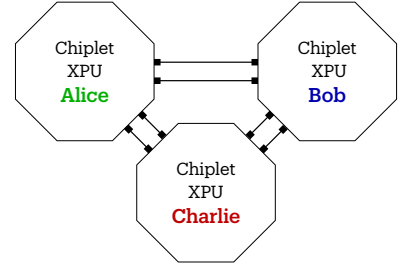


Figure 4: Uninitialized XPU Links

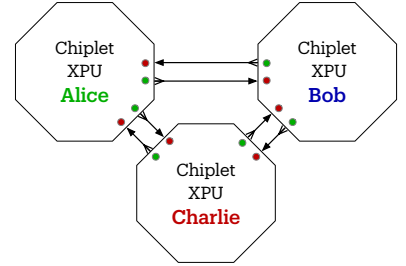


Figure 5: Configured Links for TX/RX

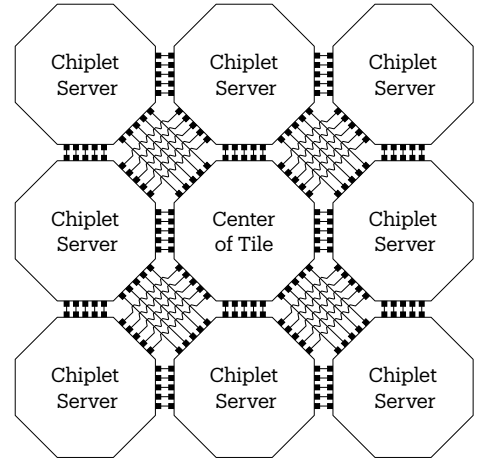


Figure 6: 3x3-wire-hop-chiplets

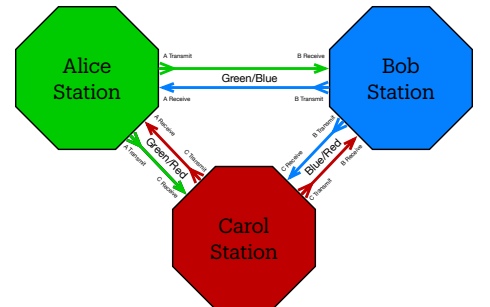
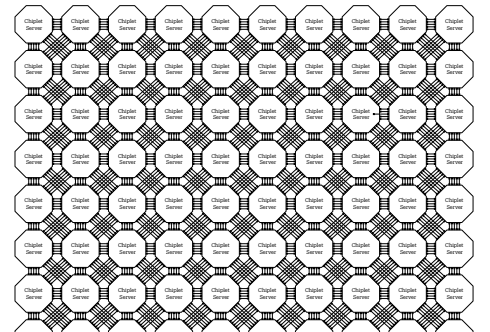


Figure 7: Minimum-Triangle.pdf



0.7 Big Flexible Chiplet

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0.8 Links are primary communication resources

The link between Alice and Bob is the resource to be shared. What goes over this link is controlled entirely by Alice and Bob.

The GAME the protocol plays ensures *fairness* (50/50 sharing of the link resources). Whatever is left over is offered as a resource to other cells further away.

This removes the need for POLICY – Each link has it's own policy, and can use as much of the bandwidth they need to perform their computation.

Whatever is left over can be offered (advertised) to the rest of the system as a utilization path.

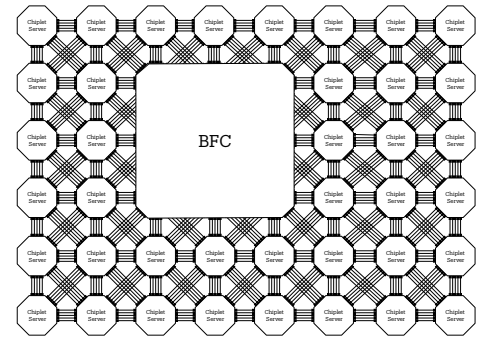
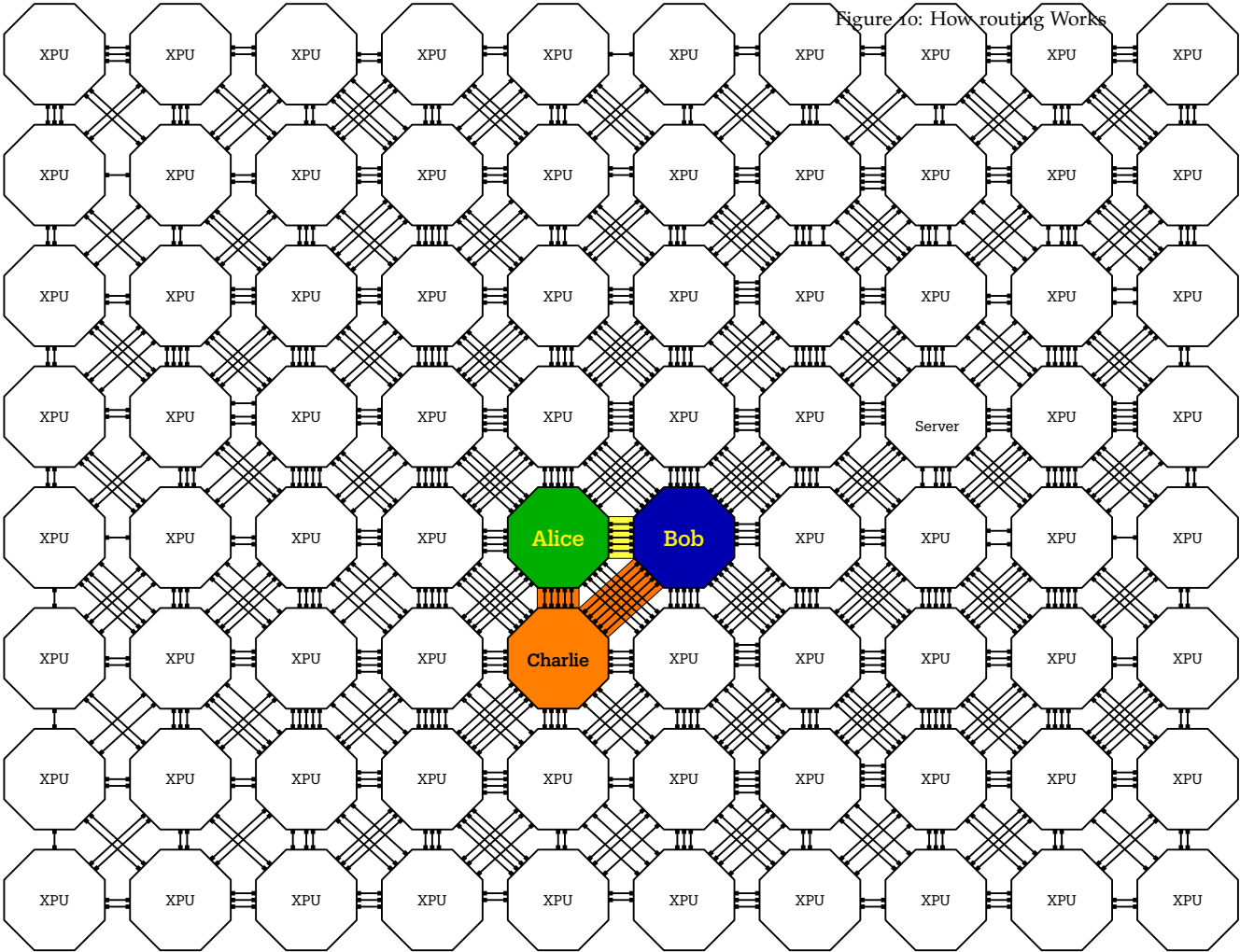


Figure 9: Big-Flexible-Chiplet.pdf



0.9 Colored Big Picture

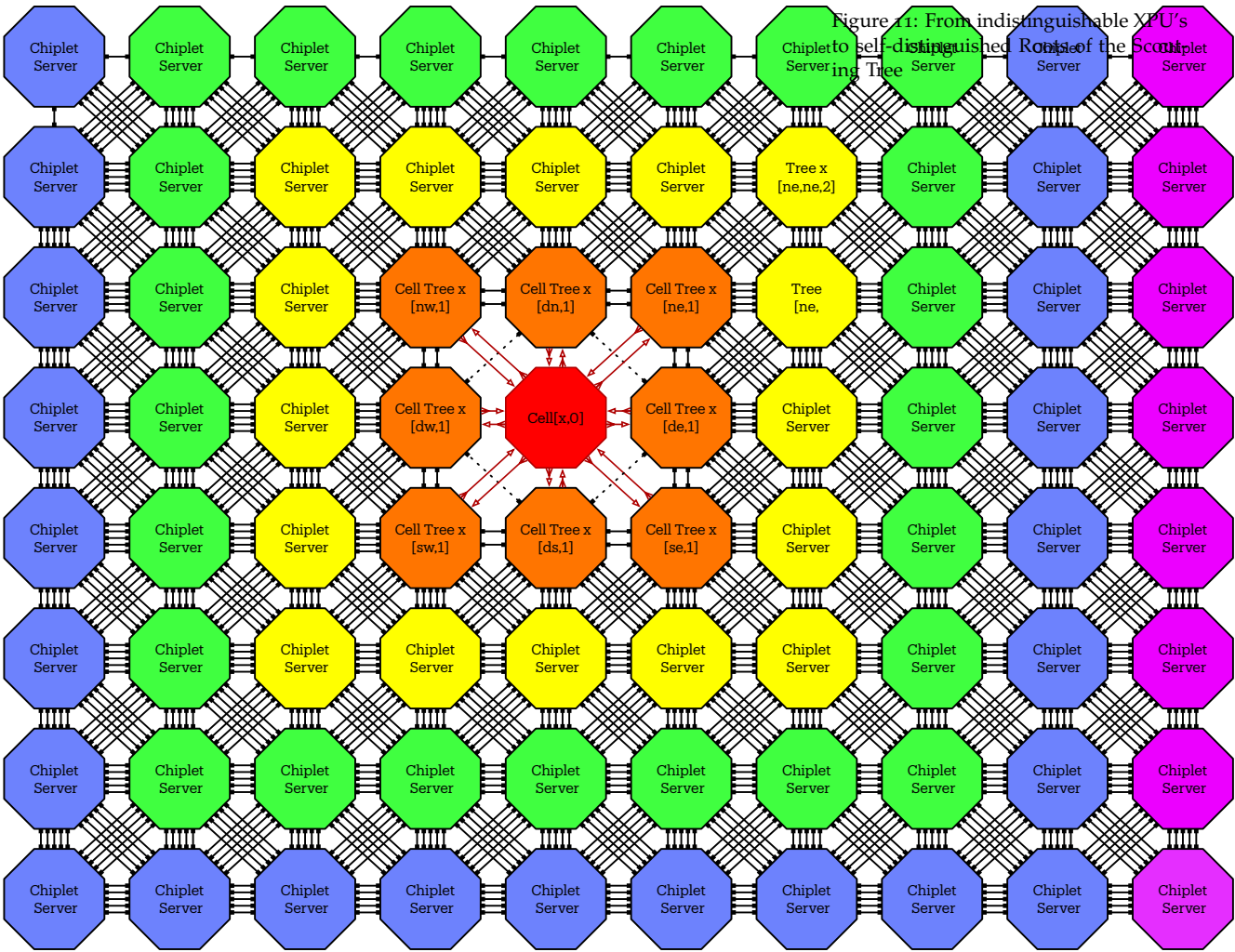


Figure 11: From indistinguishable XPU's to self-distinguished Roots of the Scout

0.10 From Physical Cells (XPU) to Logical Tiles

0.11 Consensus Tiles

1

0.12 Logical Tiles

Logical Tiles are built on top of physical tiles. They have the same 3x3 failure independence characteristics, but help define failure boundaries (the shared fate) of the system. These are discovered, not configured.

Because these

0.13 Tiles -2

0.14 Consensus Tiles 3

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0.15 Logical Overlays

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0.15.1 Unfolded Clos

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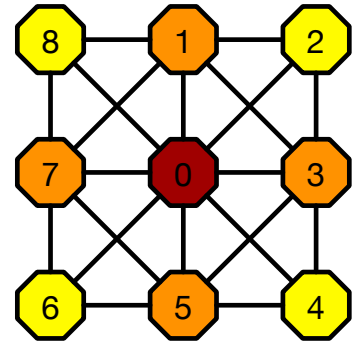


Figure 12: Consensus Tiles

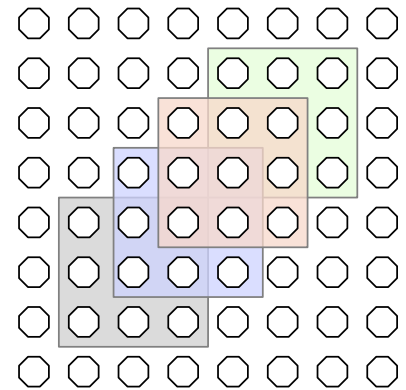


Figure 13: 3 x 3 tile

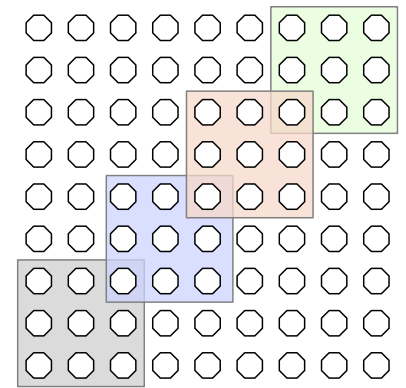


Figure 14: 9 x 9 Logical Mesh

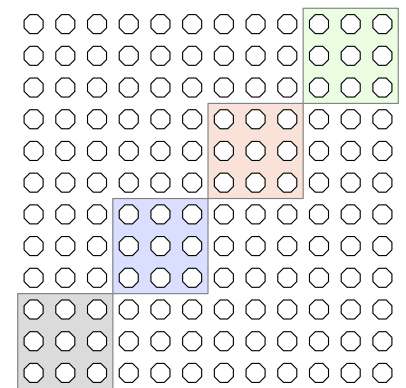


Figure 15: 9 x 9 Logical Mesh

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0.15.2 Virtual Unfolded Clos

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0.16 Physical Overlays

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0.17 Dragonfly

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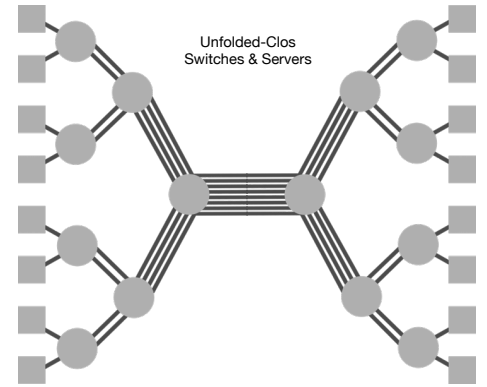


Figure 16: Unfolded Clos

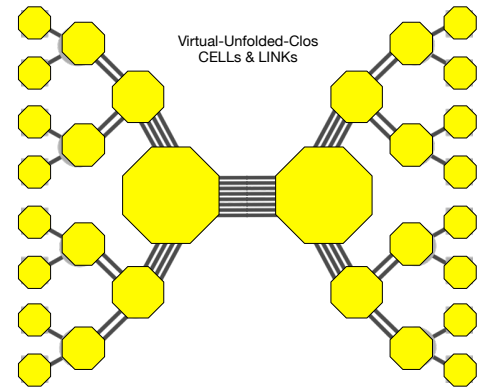


Figure 17: Virtual Unfolded Clos. Fish-Eye View of the LINK from any arbitrary LINK

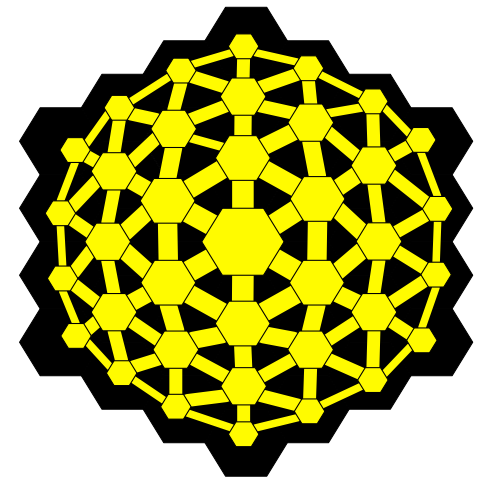


Figure 18: Fish-Eye Lens View of the network from an arbitrary Cell