

Faculty of Electrical and Computer Engineering, Examination Office

RECORD OF GRADES

As per: 19.04.2021

Mr Timo Nicolai,

born on 03 August 1994 in Flensburg,

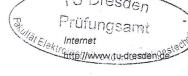
has been enrolled in the Diplom programme Informationssystemtechnik and obtained the following academic results:

	CP*	grade	examiner	date
Required modules in upper-division studies	102			02.07.2018
Communication Engineering	3	1,0	Fettweis	anerk.
Introduction to academic and professional English 1	3	1,3		23.02.2015
written exam Introduction to academic and professional English		1,3	Lehniger	23.02.2015
Introduction to academic and professional English 2	3	1,0		23.07.2015
presentation Introduction to academic and professional English		1,0	Lehniger	23.07.2015
Seminar Paper	12	1,0		
Project work Seminar paper Informations Systems Engineering		1,0	Castrillón-Mazo	
Colloquium Seminar paper Informations Systems Engineering		1,0	Castrillón-Mazo	
General Qualifications	4	2,3		anerk.
Integrated Circuit Design	7	1,3		30.09.2017
Project work Integrated Circuit Design		1,3	Mayr	30.09.2017
Operating Systems/Security	7	1,3		21.02.2018
Written exam Operating Systems and Security		1,3	Härtig	21.02.2018
Databases/Computer Networks	10	1,5		08.02.2018
Written exam Databases		1,7	Lehner	08.02.2018
Written exam Computer Networks		1,3	Schill	06.02.2018
Theory and Applications of Formal Systems	10	1,0		20.07.2017
Written exam Formal Systems		1,0	Krötzsch	14.02.2017
Written exam Compiler Construction		1,0	Castrillón-Mazo	20.07.2017
Research Project	6	1,3		02.07.2018
Project work Research Project Informations Systems Engineering		1,3	Kumar	02.07.2018
Protokoll Forschungspraktikum Informationssystemtechnik		++	Spallek	02.07.2018

Postal address

Technische Universität Dresden Prüfungsamt Elektrotechnik und Informationstechnik 01062 Dresden Visitors

Helmholtzstraße 10 Room Barkhausen-Bau 175-177





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	CP*	grade	examiner	date
Practical Engineering Intership	30	++		
Internship report Practical Engineering Internship Information		++		
Signal Processing and Information Theory	7	1,0		24.07.2017
Written exam Signal Processing		1,0	Birkholz	20.02.2017
Written exam Information Theory		1,0	Jorswieck	24.07.2017
Specialization area Electrical circuits and systems	21			17.09.2018
Digital Information Processing	7	1,0		31.03.2018
Oral exam or written exam Digital Signal Processing and Hardware Implementation		1,0	Fettweis	03.08.2017
Written exam Digital Signal Processing		1,0	Fettweis	31.03.2018
VLSI Processor Design	7	1,0		17.09.2018
Project work VLSI Processor Design		1,0	Mayr	17.09.2018
Presentation VLSI Processor Design		1,0	Mayr	
Neuromorphic VLSI Systems	7	1,2	Mayr	30.09.2017
Assigned paper Neuromorphic VLSI Systems		1,3	Mayr	30.09.2017
Presentation Neuromorphic VLSI Systems		1,0	Mayr	30.09.2017
Specialization area Technical computer science	27			08.08.2018
Artificial Intelligence I	12	1,0		08.08.2018
Oral exam Artificial Intelligence I		1,0		08.08.2018
Artificial Intelligence II	15	1,0		
Oral exam Artificial Intelligence II		1,0		
Specialization area Architecture of				

distributed systems or software engineering

abbreviations and symbols: * - CP - credit points

explanation of grades: grades 1.5 and better = very good grades ranging from 1.5 to 2.5 = good grades ranging from 2.5 to 3.5 = average grades ranging from 3.5 to 4.0 = pass grades 4.1 and worse = fail

Dresden, 19 April 2021

Bearbeiterin Prüfungsamt

TU Dresden
Prüfungsamt

(läi Elektrolechnik und Informationste

^{&#}x27;++' - exam passed, no grade given



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has been enrolled in the Diplom programme Informationssystemtechnik and obtained the following academic results:

	CP*	grade	examiner	date
Required modules in lower-division studies	120			31.03.2018
Introduction to Analysis and Algebra	11	1,0	Wensch	anerk.
Calculus for Functions with Several Variables	9	1,0		anerk.
Complex Function Theory	4	1,0		anerk.
Partial Differential Equations and Probability Theory	4	1,0		anerk.
Algebra	6	1,3		30.09.2017
attestation Algebra		++	Baumann	30.09.2017
written exam algebra		1,3	Baumann	11.07.2017
Basic Electrical Engineering	6	1,0	Tetzlaff	anerk.
Electric and Magnetic Fields	6	1,7		anerk.
Dynamical Electrical Networks	8	2,1		anerk.
Electronic Circuits	7	1,0		anerk.
Electronic Devices	3	1,3	Bartha	anerk.
System Theory and Automation Engineering	10	1,7		04.08.2016
written exam System Theory		2,0	Jorswieck	anerk.
written exam System Theorie and Automation Engineering		1,0	Janschek	anerk.
Algorithms and Data Structures	5	1,0		06.02.2017
written exam Algorithms and Data Structures		1,0	Vogler	06.02.2017
Introduction project	4	1,0		31.03.2018
project work introduction project Computer Science/Media Computer Science		1,0	Fetzer	31.03.2018
Programming	6	1,0		21.07.2017
written exam Programming		1,0	Vogler	21.07.2017
Software Technology	6	1,0		17.07.2017
written exam Software Technology		1,0	Aßmann	17.07.2017
Software Technology - project	6	++		31.03.2018
project work Software Technology		++	Aßmann	31.03.2018

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Technische Universität Dresden Prüfungsamt Elektrotechnik und Informationstechnik 01062 Dresden

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	CP*	grade	examiner	date
PresentationSoftware Technology		++	Aßmann	31.03.2018
Computer Architecture	10	2,3		31.07.2017
written exam Computer Architecture		2,3	Spallek	31.07.2017
Technical Foundations of Computer Science and Hardware Laboratory	9	1,7		30.09.2017
written exam Technical Foundations of Computer Science and Hardware Laboratory		1,7	Spallek	02.03.2017
lab course reports Hardware Laboratory Final grade preliminary Diplom procedure	120	++ 1,3	Göhringer	30.09.2017 31.03.2018

abbreviations and symbols:

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Dresden, 19 April 2021

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