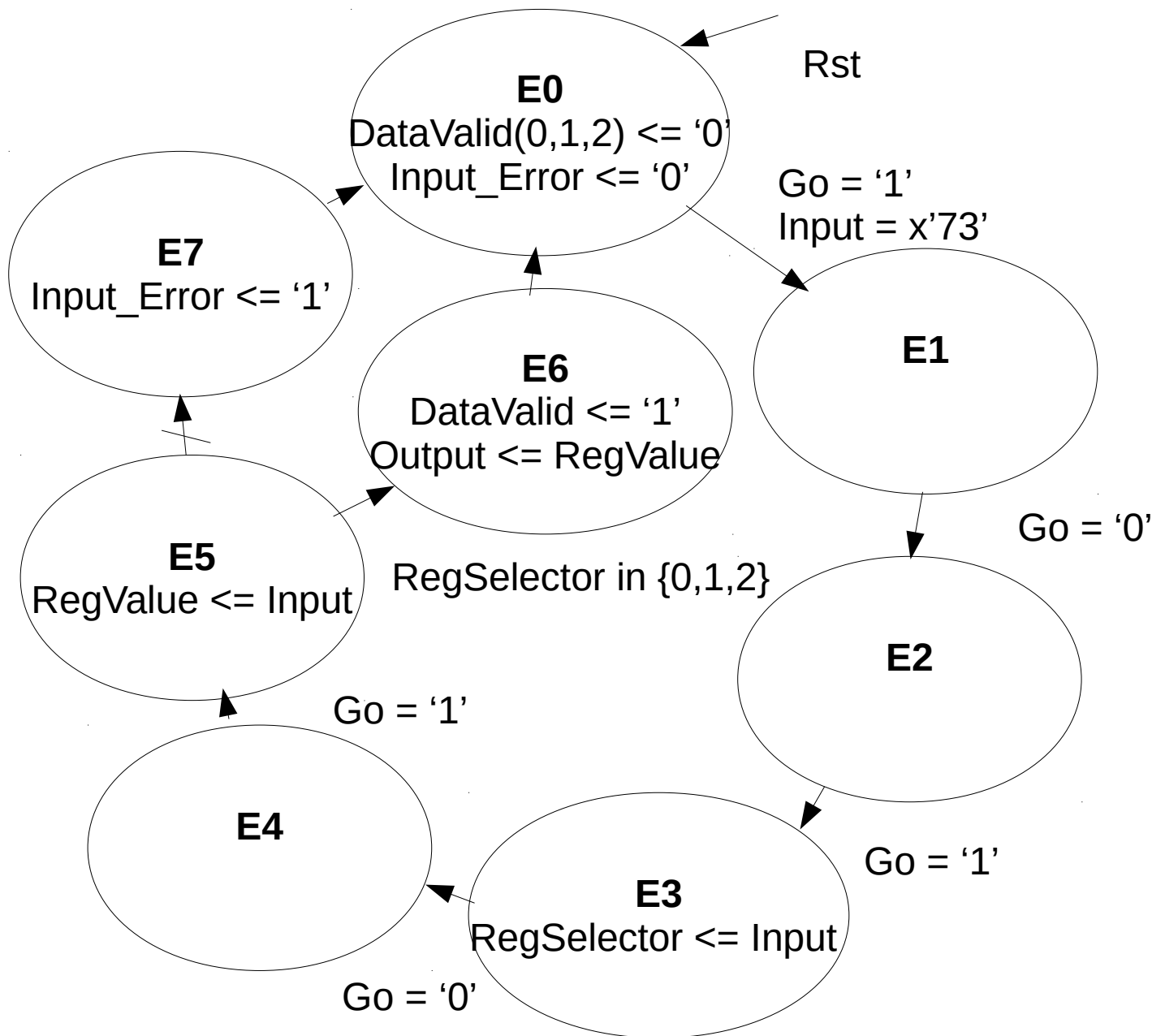


MAE de la partie Servo



Entrées :

- Input
- Clk
- Go
- Rst

Signaux :

- Signal regSelector std_logic_vector (7 downto 0)
- Signal regValue std_logic_vector (7 downto 0)

Sorties :

- Output(0,1,2)
- DataValid(0,1,2)
- Input_Error