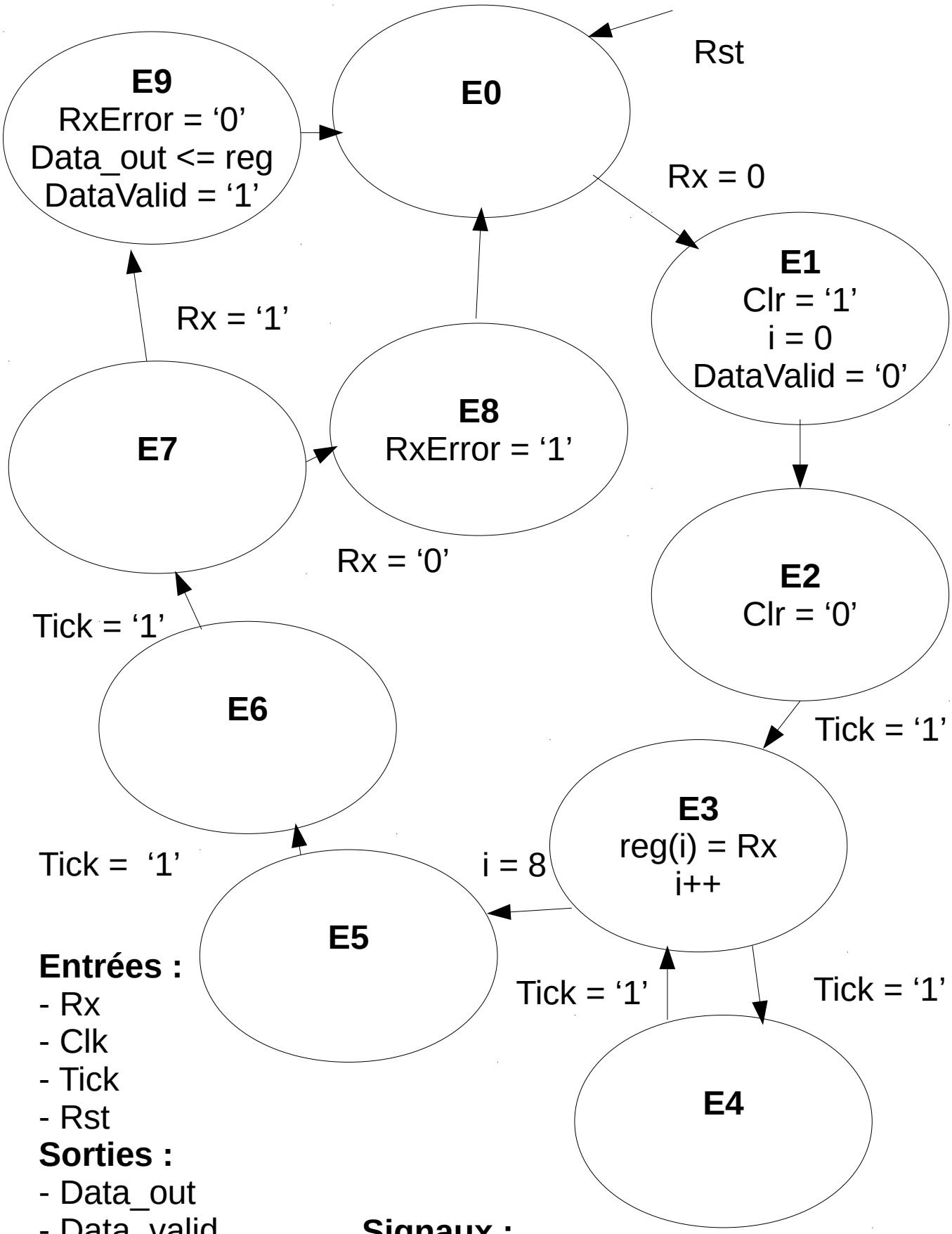
MAE de la partie Réception



Entrées :
- Rx
- Clk
- Tick
- Rst

Sorties :
- Data_out
- Data_valid
- RxError
- Clr

Signaux :
- Signal reg : std_logic_vector(7 downto 0)
- Signal i : natural := ‘0’