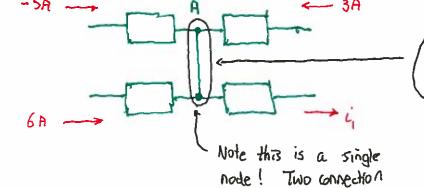
From our previous example at node A

$$i_1 + i_2 - i_3 = 0$$
entoring leaving node A node A

so i3 = i, tiz (common-sense interpretation: what goes in must come out!)

Example: Determine i,



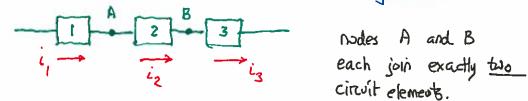
(collapsed into a single connection (Inca

KCL at node A: $-5+6+3-i_1=0$ entening leaving

Serios circuits:

This is a very common and important circuit configuration.

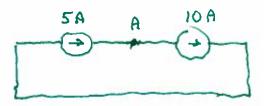
points joined by a conductor.



circuit elements.

kch at node A: $i_1 - i_2 = 0$, $i_1 = i_2$ node B: $i_2 - i_3 = 0$, $i_2 = i_8$ Circuit elements in series all have the same current $i_1 = i_2 = i_3$

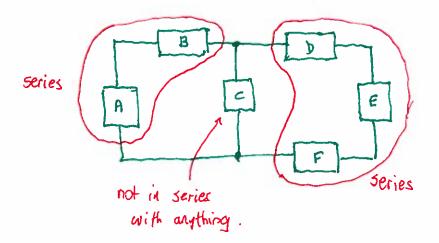
Circuits that violate KCL



Must have the same current when in series. At Node A:

5-10 # 0

Example: What's in series here?

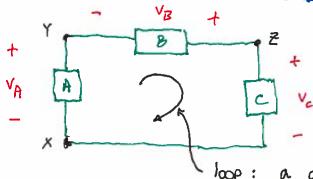


Examine all connection points (nodes), and identify ones at which only two circuit elements are juried.

Kirchoff's voltage Law (KVL)

Derives from conservation of energy.

Consider circuit elements in a closed loop

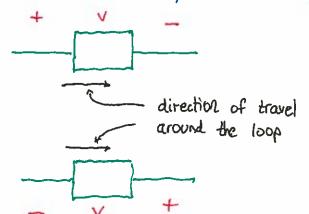


a closed path starting at a node and finishing back at the same node.

 $X \rightarrow Y \rightarrow Z \rightarrow X$

KVL states: Algebraic sum of all voltages around a loop must be zero

Choose a consistent way to sum voltages. By convention:

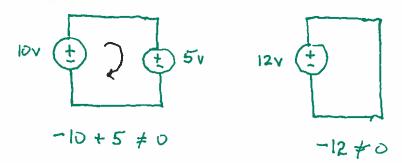


Subtract V

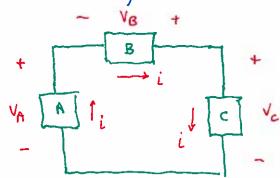
Add V

Therefore, KVL around our above loop gives - VA - VB + Vc = 0

Circuits that violate KVL.



kVL and power and energy



There must be at all times energy balance (generated must equal absorbed). Hence at any time, net power must be zero.

Circuit element power

A -Vai

B -Vai

C Vci

Teference convention $-V_{A}i - V_{B}i + V_{C}i = 0$ or $i(-V_{A} - V_{B} + V_{C}) = 0$ Assuming $i \neq 0$, $-V_{A} - V_{B} + V_{C} = 0$ kvl