

SENG 301

Software

Analysis and

Design

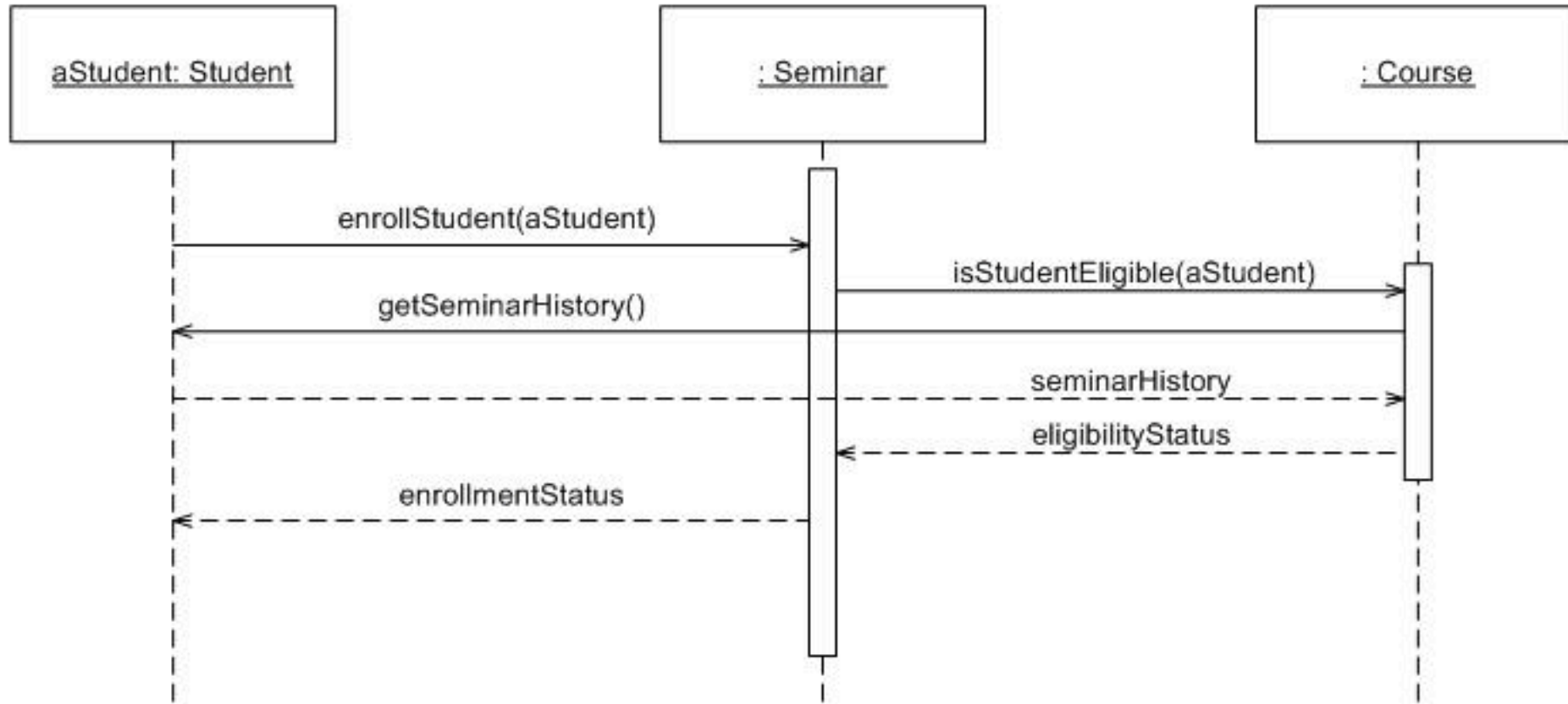
Lab 3 & Lab 4

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Lab 3 – Sequence Diagram

- In this lab, you will be:
 - interpreting the meaning of sequence diagrams
 - creating simple sequence diagrams
 - finding problems and errors in sequence diagrams

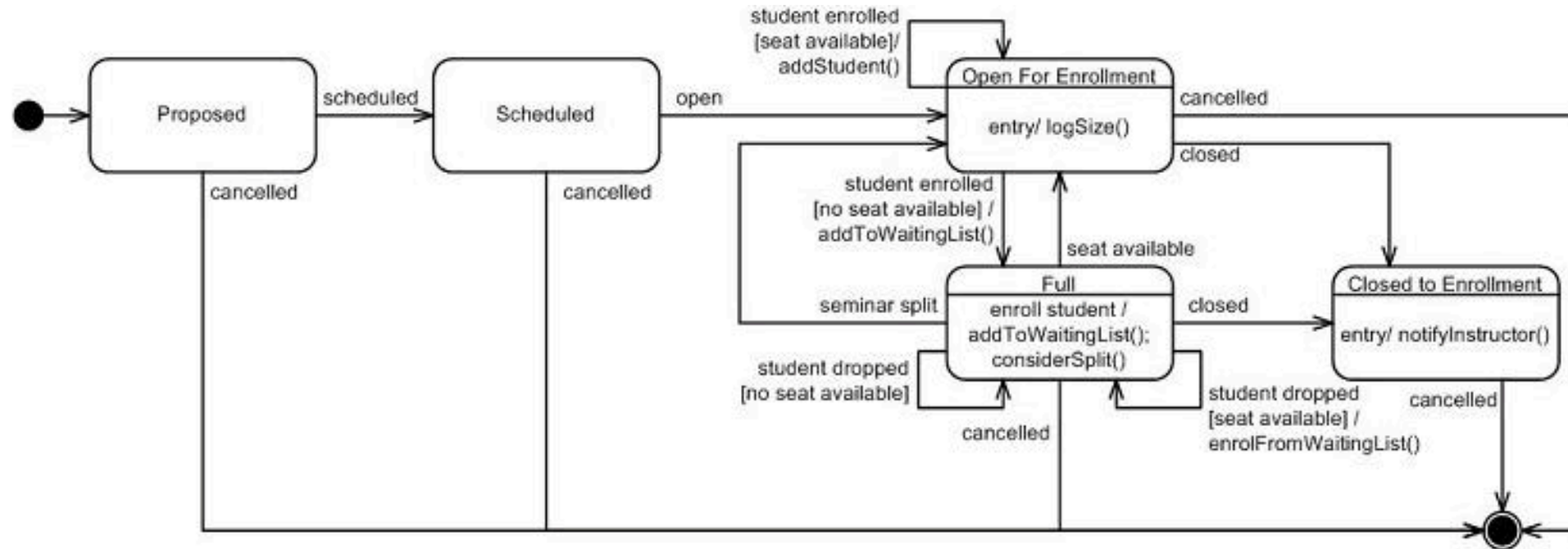
Sequence Diagram



Lab 4 – State Diagram

- In this lab, you will be considering options for modelling the behavior of ArrayList with a state machine diagram.
 - Consider the class ArrayList. Its behaviour varies depending on its capacity and its size: when the size reaches the capacity, ArrayList increases the capacity of its internal array. This reaction is only visible if you examine an ArrayList instance in the debugger.

State Diagram



Start Lab