

```

1
2  /**
3   *
4   * @author Timothy Dakis
5   *
6   */
7  public class InstructionReader {
8
9      //these are all variables that parts of the machine code represent
10     static String operation;
11     static int sourceRegister1;
12     static int sourceRegister2;
13     static int destRegister;
14     static int shiftAmount;
15     static int constant;
16
17     public static void main (String args[]) {
18
19         decodeInstruction("00000010001100100100000000100000");
20         decodeInstruction("001000100010100000000000000000101");
21         decodeInstruction("00000010010100111000100000100010");
22         decodeInstruction("10001110010100010000000001100100");
23         decodeInstruction("00000001010010111001000000100101");
24         decodeInstruction("00000001001010101001100000100100");
25         decodeInstruction("00000001010010110100100000101010");
26         decodeInstruction("10101110001100110000000001100100");
27         decodeInstruction("00010010001100100000000001100100");
28
29     }
30
31     /**
32     * Determines whether an instruction is R-format, or a desired I-Format instruction.
33     * If either are present it then configures variables then prints
34     *
35     * @param machineCode this is the instruction code to be decoded
36     */
37     public static void decodeInstruction (String machineCode) {
38         //this sets opCode to the decimal equivalent of the first 6 bits of the machine code
39         int opCode = Integer.parseInt(machineCode.substring(0, 6), 2);
40
41         //this block of if statements check if its R format or if its a wanted I-Format instruction or neither
42         if(opCode == 0) {
43             //if the opCode is 0, it is R-format then it configures the variables to print the correct value
44             configureRFormat(machineCode);
45             printRFormat(machineCode);
46         }
47         else if (opCode == 8 || opCode == 35 || opCode == 43 || opCode == 4) {
48             //if the opCode is any of these values its a desired I-Format instruction
49             configureIFormat(machineCode, opCode);
50             printIFormat(machineCode, opCode);
51         }
52         else
53             //if neither condition is true, the code was invalid
54             System.out.println("Invalid Operation Code");
55
56     }
57
58     /**
59     * Configures the variables to the corresponding bits of the machine code to print out the correct values
60     *
61     * @param instruction this is the machine code of the instruction
62     */
63     public static void configureRFormat(String instruction) {
64         //stores the decimal equivalent of bits 31->26 into functCode
65         int functCode = Integer.parseInt(instruction.substring(26, 32), 2);
66         //takes this functCode value and sends it to another function to determine the operation of the instruction
67         operation = returnRFormatOperation(functCode);
68         //these just store the decimal equivalent of the corresponding bit ranges too
69         sourceRegister1 = Integer.parseInt(instruction.substring(6, 11), 2);
70         sourceRegister2 = Integer.parseInt(instruction.substring(11, 16), 2);
71         destRegister = Integer.parseInt(instruction.substring(16, 21), 2);
72         shiftAmount = Integer.parseInt(instruction.substring(21, 26), 2);
73     }
74

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75  /**
76  *
77  * @param funcCode the decimal representation of bits 31->26, the function code of an R-Format instruction
78  * @return the corresponding operation code, or that it is invalid
79  */
80  public static String returnRFormatOperation (int funcCode) {
81
82      //this block of if statements checks what the funcCode contains and returns certain specific operations
83      if (funcCode == 32)
84          return "add";
85      else if (funcCode == 34)
86          return "sub";
87      else if (funcCode == 36)
88          return "and";
89      else if (funcCode == 37)
90          return "or";
91      else if (funcCode == 42)
92          return "slt";
93      else
94          return "Invalid Function Code";
95
96  }
97
98  /**
99  * This just prints R-Format instructions
100  *
101  * @param instruction the machine code of the instruction
102  */
103  public static void printRFormat(String instruction) {
104      System.out.println("Input:\n" + instruction + "\n");
105      System.out.println("Outputs:");
106      System.out.println("Instruction Format: R");
107      System.out.println("Operation: " + operation);
108      System.out.println("Source Registers: " + sourceRegister1 + ", " + sourceRegister2);
109      System.out.println("Destination Register: " + destRegister);
110      System.out.println("Shift Amount: " + shiftAmount);
111      System.out.println("Constant/Offset: none\n");
112
113  }
114
115  /**
116  * Configures the variables to the corresponding bits of the machine code to print out the correct values
117  *
118  * @param instruction the instruction to be decoded
119  * @param operationCode the opCode of the I-Format instruction
120  */
121  public static void configureIFormat(String instruction, int operationCode) {
122      //sets operation to the return value of the following function to determine operation of instruction
123      operation = returnIFormatOperation(operationCode);
124      sourceRegister1 = Integer.parseInt(instruction.substring(6, 11), 2);
125      // ensures that for beq and sw instructions, that it configures the second source register, not dest. reg.
126      if(operationCode == 4 || operationCode == 43)
127          sourceRegister2 = Integer.parseInt(instruction.substring(11, 16), 2);
128      else
129          destRegister = Integer.parseInt(instruction.substring(11, 16), 2);
130      constant = Integer.parseInt(instruction.substring(16, 32), 2);
131  }

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132
133  /**
134   * Determines the I-Format operation used
135   *
136   * @param operationCode opCode of the machine code
137   * @return the operation of the instruction, or that the opCode was invalid
138   */
139  public static String returnIFormatOperation(int operationCode) {
140      if(operationCode == 8)
141          return "addi";
142      else if(operationCode == 35)
143          return "lw";
144      else if(operationCode == 43)
145          return "sw";
146      else if(operationCode == 4)
147          return "beq";
148      else
149          return "Invalid I-Format OpCode";
150  }
151
152  /**
153   * This just prints out I-Format instructions
154   *
155   * @param instruction the machine code of the instruction
156   * @param operationCode the opCode of the instruction
157   */
158  public static void printIFormat (String instruction, int operationCode) {
159
160      System.out.println("Input:\n" + instruction + "\n");
161      System.out.println("Outputs:");
162      System.out.println("Instruction Format: I");
163      System.out.println("Operation: " + operation);
164      //this chain of if statements changes what is printed based on if the instruction is beq or sw, or neither
165      if(operationCode == 43 || operationCode == 4) {
166          System.out.println("Source Registers: " + sourceRegister1 + ", " + sourceRegister2);
167          System.out.println("Destination Register: none");
168      }
169      else {
170          System.out.println("Source Registers: " + sourceRegister1);
171          System.out.println("Destination Register: " + destRegister);
172      }
173      System.out.println("Shift Amount: none");
174      System.out.println("Constant/Offset: " + constant + "\n");
175
176  }
177
178
179
180 }
181

```

Input:
00000010001100100100000000100000

Outputs:
Instruction Format: R
Operation: add
Source Registers: 17, 18
Destination Register: 8
Shift Amount: 0
Constant/Offset: none

Input:
0010001000101000000000000000101

Outputs:
Instruction Format: I
Operation: addi
Source Registers: 17
Destination Register: 8
Shift Amount: none
Constant/Offset: 5

Input:
00000010010100111000100000100010

Outputs:
Instruction Format: R
Operation: sub
Source Registers: 18, 19
Destination Register: 17
Shift Amount: 0
Constant/Offset: none

Input:
10001110010100010000000001100100

Outputs:
Instruction Format: I
Operation: lw
Source Registers: 18
Destination Register: 17
Shift Amount: none
Constant/Offset: 100

Input:
00000001010010111001000000100101

Outputs:
Instruction Format: R
Operation: or
Source Registers: 10, 11
Destination Register: 18
Shift Amount: 0
Constant/Offset: none

Input:
00000001001010101001100000100100

Outputs:
Instruction Format: R
Operation: and
Source Registers: 9, 10
Destination Register: 19
Shift Amount: 0
Constant/Offset: none

Input:

00000001010010110100100000101010

Outputs:

Instruction Format: R

Operation: slt

Source Registers: 10, 11

Destination Register: 9

Shift Amount: 0

Constant/Offset: none

Input:

10101110001100110000000001100100

Outputs:

Instruction Format: I

Operation: sw

Source Registers: 17, 19

Destination Register: none

Shift Amount: none

Constant/Offset: 100

Input:

00010010001100100000000001100100

Outputs:

Instruction Format: I

Operation: beq

Source Registers: 17, 18

Destination Register: none

Shift Amount: none

Constant/Offset: 100