

EDUCATION

Imperial College London	United Kingdom	Oct 2020 – Jun 2024 (Expected)
<ul style="list-style-type: none">• MEng in Electrical and Electronic Engineering Year 1: 74.31%, Year 2: 72.77%, Year 3: 73.64%• Thesis (Ongoing): Investigating chopper stabilisation in reducing flicker noise in Ion-Selective Field Effect Transistors (ISFET)<ul style="list-style-type: none">– Created python code to interface with test equipment to prove concept using an existing chip– Fabricating a test chip on TSMC 65nm silicon in February to integrate readout electronics on silicon with the sensor– Designing ISFET using metal planes as floating gates and optimising dimensions to minimise capacitive attenuation of signal– Designing switched capacitor programmable gain amplifier for sensor reading out with emphasis on low noise– SPI and digital clock generator synthesised, placed and routed using Cadence Genus and Innovus– Creating a PCB in Altium Designer for mounting test chip, oscillator IC and ADC. This will be interfaced with a Xilinx FPGA• Relevant Modules: Analog Integrated Circuits, Digital Systems Design, Digital Signal Processing, Instrumentation, Electromagnetics, Digital Electronics and Computer Architecture, Machine Learning, Power Electronics, Optoelectronics, Full-Custom Integrated Circuits, Sensors, Hardware and Software Verification		
Anglo Chinese School (Independent)	Singapore	Feb 2017 – Nov 2019
<ul style="list-style-type: none">• International Bacclaireate Diploma Program, 41/45 points overall, HL Math 7, HL Physics 7, HL Chemistry 7		

EMPLOYMENT

RF Design Verification Intern	Mediatek, Kent, United Kingdom	Apr 2023 – Sep 2023
<ul style="list-style-type: none">• Worked on an intermediate frequency chip designed for a 5G mobile communication platform• Modelled circuit behaviour with Verilog-AMS, ran cosimulations & transistor level simulations and analysed results with scripts• Took responsibility for chip power estimation and current measurement final test sequences for post-silicon testing• Presented results in daily verification meetings and coordinated with multiple RFIC designers		
Front-end Developer Intern	Bühler UK, London, United Kingdom	Jul 2022 – Sep 2022
<ul style="list-style-type: none">• Developed desktop GUI using ElectronJS, ReactJS and TailwindUI for querying an internal data management platform• Setup CI/CD pipeline on Azure DevOps with test, build and publish stages to distribute application for user testing• Presented applications to both technical and non-technical users to gain feedback which was then incorporated into later versions		
Undergraduate Teaching Assistant	Imperial College London	
<ul style="list-style-type: none">• In 2021-2022, conducted one-to-one help sessions for students taking the Topics in Electrical Engineering module• In 2022-2023, assisted in software defined radio lab in the Communications module		

LANGUAGES AND TECHNOLOGIES

- **Programming:** Python, Javascript, C++, Matlab, Verilog, SQL, TCL, Dafny, Isabelle
- **Tools:** Quartus, LTSpice, KICAD, LabView, Simulink, Git, IBM Cloud, Fusion 360, Azure DevOps, STM32CubeIDE, Cadence Virtuoso, Cadence Genus & Innovus, Ansys HFSS

TECHNICAL EXPERIENCE

2023

- **Inductive Distance Sensor** Designed an inductive distance sensor using a PCB stretched coil and inductance measurement sensor. First simulated the coil in Ansys HFSS and after fabrication, characterised the coil impedance using a VNA, then calibrated the sensor using linear interpolation of measured values.
- **Design of DAC** Designed and laid out a 12-bit R-2R DAC in GPD45 for the full-custom integrated circuits module.
- **Formal Verification** Completed hardware and software formal verification tasks using Dafny, Isabelle and SymbiOsYS.
- **Impedance measurement meter** Designed and built PCB with auto-balancing bridge to measure impedance. Programmed a STM32 microcontroller to generate sinewaves up to 100kHz and implemented Geortzel algorithm to process measured voltages.
- **Function accelerator using FPGA (Altera DE1)** Implemented a hardware block in verilog using the Cordic algorithm to calculate an equation. The block was then invoked in the NIOS2 processor using a custom instruction.

2022

- **Autonomous Mars Rover (Top 2nd year group project)** Worked in a 7 member group to create a 2-wheel rover designed to autonomously navigate through a simulated Martian surface with obstacles. I contributed towards the drive subsystem by interfacing an optical flow sensor with the ESP32 and tuned an algorithm for precise rotation and linear motion. I also created an analog readout circuit for the doppler radar module.

2021

- **Analogue Music Synthesizer (Top 1st year group project in category)** Designed and simulated a 88-key analog music synthesizer in LTSpice. Considered the product design specifications, costs, power consumption and waveform quality. Created a Python program to transcribe frequencies from a CSV file to piece-wise voltage level directives in LTSpice for testing.
- **Personal Website** Created website with GatsbyJS for the front-end and Strapi.io for the content management system (CMS). Optimized layout and images for different screen sizes and setup continuous deployment for static content on Netlify from Github repository. Configured Linux VPS, Postgres database and Nginx server to host CMS.
- **Optiver TraderCraft 2021 (2nd placed team)** Created a Python delta hedging trading algorithm and competed with other teams to make highest profit. I was selected to attend the Insights Days program.

2019

- **European Organization for Nuclear Research (CERN)** Selected among 30 students for a weeklong study trip. Attended lectures on topics including particle physics, computing and medical applications. Visited data centre, CMS detector and anti-matter factory.
- **Singapore Astronomy Olympiad 2019** Silver medalist and best star-gazer in olympiad focussed on astronomy and astrophysics

ACTIVITIES AND INTERESTS

- Violinist at IC Sinfonietta Orchestra and pianist with ABRSM grade 7 Piano
- **President of IC AstroSoc** I collaborated with my committee to organise stargazings, lectures and trips and to maintain our telescopes. Increased membership by 10% and organised new trips to South Downs and Silwood Park.
- **IC Space Society** Constructed a high-power rocket to participate in the National Rocketry Championship. As part of the electronics team, I designed a PCB flight computer to record and transmit sensor values using a 915MHz LoRa radio.