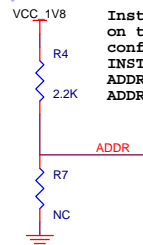


Reset(EN) can be implemented with passive components as shown or active circuitry. In case of using passive components, the values of the RC circuitry need to be adjusted to make sure the low to high transition occurs after the Vcc supply has reached the minimum recommended operating voltage. For this reason, it is recommended to USE ACTIVE CIRCUITRY for better control of the RESET/EN timing.



Install PU or PD depending on the I2C Target addr configuration. DO NOT INSTALL BOTH.
ADDR = 1, Slave Addr = 0x5A
ADDR = 0, Slave Addr = 0x58