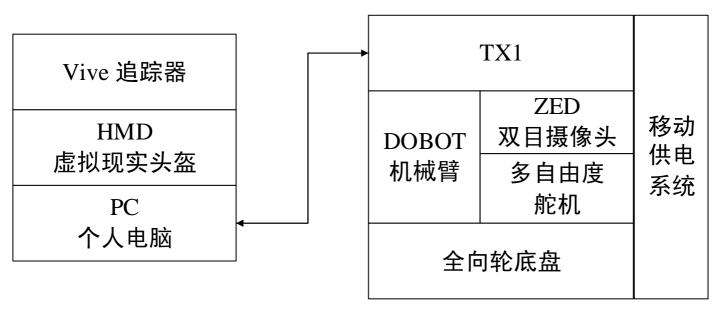
项目硬件架构概念图



控制端

执行端