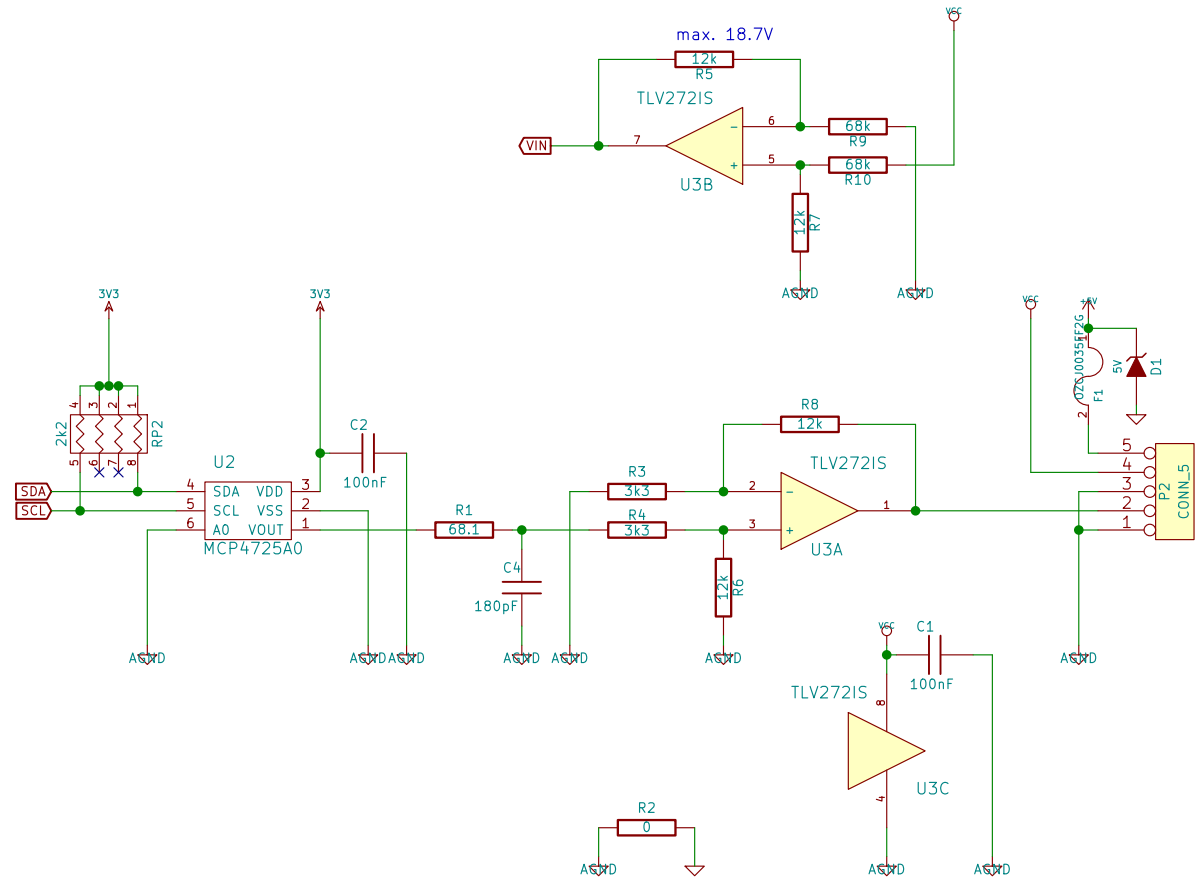
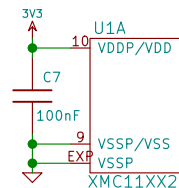
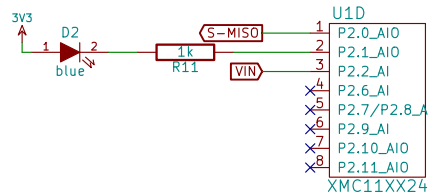
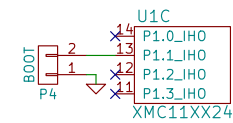
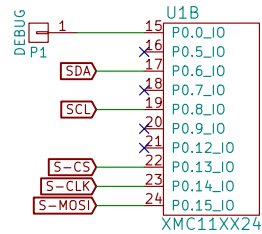


I2C Master/CH1
P0.6 : USIC0_CH1-DOUT0/DX0C : SDA
P0.8 : USIC0_CH1-SCLKOUT/DX1B : SCL

SPI Slave/CH0
P0.13 : USIC0_CH0-DX2F : SEL
P0.14 : USIC0_CH0-DX1A : CLK
P0.15 : USIC0_CH0-DX0B : MOSI
P2.0 : USIC0_CH0-DOUT0 : MISO



max. Input Voltage: 12V
Amplifier theoretical max voltage: $12k/3k \times 3.3V = 12.0V$

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Sheet: /
File: analog-out-v3.sch

Title: Analog Out Bricklet

Size: A4 Date: 2018-03-01
KiCad E.D.A. kicad 4.0.2+dfsg1-stable

Rev: 3.0
Id: 1/1

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