

The schematic shows the power supply circuit for the LMS9DS1 module. A voltage divider consisting of resistors R1 and R2 provides a reference voltage to the INT pin of the module. The VCC pin is connected to a 7.0V source through capacitor C1 and to ground through capacitor C2. The module's VDD pin is connected to the same 7.0V source through capacitor C3 and to ground through capacitor C4. The module's GND pin is connected to the common ground. The module's pins are labeled as follows:

- VCC: 7.0V
- GND: 0.0V
- VDD: 7.0V
- GND: 0.0V
- SCL: 7.0V
- SDA: 7.0V
- CS\_AG: 7.0V
- CAP: 7.0V
- SDO\_M: 7.0V
- DRDY\_M: 7.0V
- INT1\_AG: 7.0V
- INT2\_AG: 7.0V
- GND: 0.0V