

The diagram shows a 3.3V voltage divider circuit. The input is connected to a 1.8uF capacitor (C2) and a 9.1uF capacitor (C1) in parallel to ground. The input voltage is UCC+. The divider consists of two 9.1k resistors (R1, R2) connected in series between UCC+ and ground. The output of the divider is connected to the VDD pin (pin 6) of the BMA26B IC. The IC is also connected to ground at pin 1. The IC pins are labeled: 1 (GND), 2 (VDD), 3 (NC), 4 (VDD), 5 (VDD), 6 (VDD), 7 (GND), 8 (GND), 9 (GND), 10 (GND), 11 (GND), 12 (GND). A 5-pin header J1 is shown on the right with pins 1 through 5 labeled.

Original Arduino Uno Design by Team Arduino
LilyPad Arduino Design by Leah Buechley and Sparkfun Electronics
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