Castruction Manual



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A Columbia Pictures Industries Company

Q*BERT (GAME GV-103) INSTRUCTION MANUAL

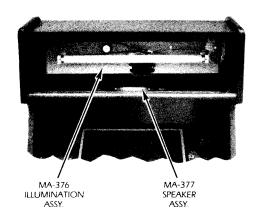
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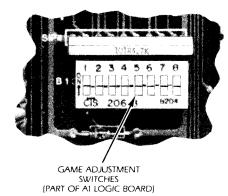
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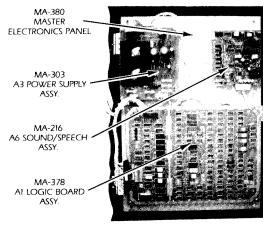
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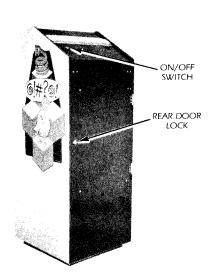
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I. INSTALLATION

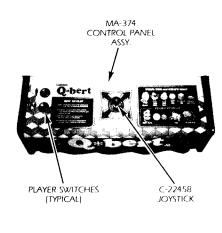


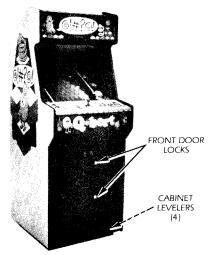


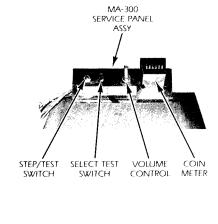


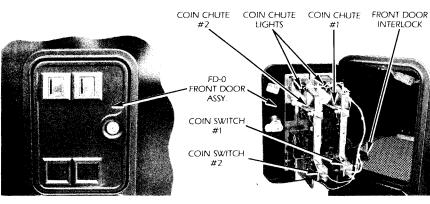


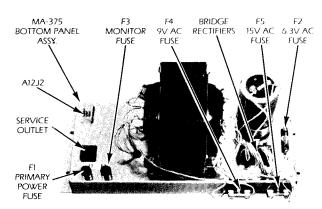








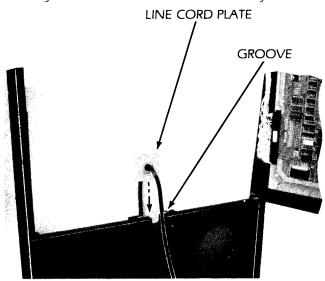




I. INSTALLATION

A. SET-UP

- Carefully inspect the exterior of the game for any damage which might have occurred during shipment.
- Remove the two screws from the back door, that were used for extra security during shipping.
- 3. Unlock and open the rear cabinet door.
- 4. Check that all plug in connectors are seated firmly. The connectors are keyed so they will only go in one way.
- Remove the binding strap from the line cord, and install the line cord plate in the groove provided (see photo).
- 6. Adjust the cabinet levelers as necessary.



B. CHECK-OUT

- 1. Check that all cables are free of moving parts.
- 2. Check for any loose wires.
- 3. Check for loose solder or foreign matter on switches and power supply assemblies.
- Be certain all fuses are seated firmly.
- 5. Be sure transformer wiring corresponds to the supply voltage.
- Refer to section VI to make all the necessary game adjustments.
- 7. Reassemble the game.
- 8. Plug the line cord into a properly grounded 3-wire receptacle ONLY!!

C. CONTROL PANEL REMOVAL

- 1. Unplug the game.
- 2. Unlock and open the coin chute door.
- Reach in through the coin chute door and remove the wing nut and flat washer from each of the two carriage bolts which secure the control panel to the game. Remove the carriage bolts.
- Pull the control panel back, grasp it at the front edge as far back as it will go (approximately ¼").
- Raise the front of the control panel approximately one inch above its supports and lift the entire assembly high enough to disconnect plug A9J2/A9P2.
- 6. Remove the entire control panel assembly from the game.
- 7. The Joystick and leaf-switches are now accessible for removal or cleaning

D. MONITOR REMOVAL

- 1. Unplug the game.
- 2. Perform the control panel assembly removal procedure (Section C).
- 3. Remove the outside shield, glass and monitor mask and put them aside in a secure place.
- 4. Unlock and open the rear cabinet door.
- NOTE: The color monitor contains HIGH VOLTAGES delivering LETHAL quantities of energy. Do not attempt to service the monitor until you have shorted the anode plug on the picture tube to ground.
- Disconnect the video plug A17J1, the monitor power supply plug A12J3/A12P3 and the ground wire from the monitor chassis.
- From the rear of the game, remove the one nut and two washers from each of the four carriage bolts used to secure the monitor to the platform.
- 8. From the front of the game, remove the monitor, being careful to clear all cables from the CRT neck.
- 9. For reassembly, reverse the above procedure.

II. INITIALIZATION, III. GAME OPERATION

II. INITIALIZATION

TURN GAME ON

Immediately, all of the illumination lamps turn on. These include the control panel lamps, the coin chute lamps and the speaker marquee lamp.

AFTER A TEN SECOND DELAY

- A. The attract mode appears on the screen.
- B. The playing field cycles through the following:

- 1. High Game to Date screen
- 2. Instruction Set
- 3. Game Play Cycle

DEPRESSING ANY CONTROL BUTTON

The instruction set will now appear on the playing field and follow through game play and strategies.

III. GAME OPERATION

A. GAME START

- 1. Insert coins into coin chute.
 - a. Coin chute tune is played.
 - b. Total credits are displayed on screen.
- 2. Press one or two player button to start game.
 - a. Demonstration scene displayed on screen.
 - b. Total Credits are decreased by one.
 - c. Game initializes.

B. FIRST PLAYER

- 1. The first player's score displays a zero.
- 2. The other player's display will be blank.

C. SECOND PLAYER

 Additional player is indicated by the words "PLAYER 2" and a zero in the second player's display.

D. "Q*BERTS"/EXTRA "Q*BERTS"

- Each player will begin with three "Q*Bert" lives. (Dependent on Option/Parameter settings.)
- Extra "O*Berts" are earned by achieving certain score levels. (Dependent on Option/Parameter settings.)

IV. GAME PLAY AND SCORING

HOW TO PLAY

The object of the "Q*Bert" game is to change the color of the top of the cubes to the designated color by hopping onto them. When all the cubes in the pyramid have been changed to the designated color, the screen will advance to the next Round, with "Q*Bert" starting back on the top cube. At the beginning of each Level, there will be a short demonstration cycle with the "Q*Bert" character hopping around four cubes to explain to the player the play action of each Level. Each Level consists of four Rounds.

The game play starts with the player-controlled "Q*Bert" character appearing at the top of the pyramid. The joystick will move "Q*Bert" from cube to cube by hopping in any of four diagonal directions. "Q*Bert" can move anywhere on the pyramid, but jumping off will kill him. Hopping on the rotating disk will take "Q*Bert" back to the top of the pyramid. In the first two Rounds "Q*Bert" will have to avoid touching the red and purple balls. These deadly objects drop randomly onto the second-from-the-top level and bounce downwards. The red balls will fall off the bottom but the purple ball will stop at the bottom and hatch into "Coily", the snake which chases "Q*Bert". To destroy the snake, lure him to the edge, then jump unto a disk. The disk will take "Q*Bert" back to the top and "Coily" will fall off, awarding 500 points.

Starting at the third Round, other characters come into play. The green characters or objects are safe to hop onto and will award points. All other objects are deadly to touch. In the third Round the red balls will stop falling, but two purple characters, "Ugg" and "Wrong-Way", will appear at the lower portion of the pyramid and travel sideways and upwards. They will not chase "O*Bert" but will move randomly to get in "O*Bert's" way. In the third Round and every Round after, based on an internal timer, a green ball will appear and bounce down from the top of the pyramid. Hopping "O*Bert" onto the green

ball will award 100 points, and freeze all the characters on the screen for a few seconds, but "Q*Bert" will still be able to move to complete the color changes.

During the third Round of play, two green characters, "Slick" and "Sam", will appear, based on the internal timer. They will drop onto the second level from the top and hop randomly downwards. If they hop onto a cube that "Q*Bert" has already changed the color of, the cube will change to a different color, to thwart "Q*Bert". Hopping "Q*Bert" onto "Slick" or "Sam" will stop them and award 300 points.

Throughout the remaining Rounds, all the characters and objects will appear in random combinations with increasing speed.

To add variety to the game, the disks will change positions every Round, and in the higher Levels the number of disks will change. (See Round Progression Chart.)

During Level Two, the play action will increase in difficulty from changing the cubes to one color, to changing the color of the cubes twice. This means that each cube would have to be hopped on twice to change the pyramid to the designated color, completing the Round.

Starting at Level Three and for all remaining Rounds, and Levels, the play action will become more difficult. The object remains to change the cubes to the designated color, but if "O*Bert" hops on any cube, that cube will change color. So even if the cube has been changed to the designated color, it will change again.

There are also Bonus points awarded at the end of each Round for successfully completing the Round. The Bonus for the completion of the First Round is 1,000 points. This Bonus will progressively increase each Round by 250 points to a maximum of 5,000 points at Level Five.

CONTROL PANEL INSTRUCTIONS

Goal: Change the tops of all cubes to a new color by hopping onto them.

- Joystick moves "Q*Bert" from cube to cube. Hopping onto a disk will take you back to the top.
- All green objects are safe to hit. All other objects are deadly.
- Destroy the snake by leading him to the edge, then jumping on a disk.
- Stay on pyramid! Only jump off to use a disk.

IV. GAME PLAY AND SCORING

ROUND PROGRESSIONS

The following chart lists round progressions for "Q*Bert".

Commencing with Level V all characters will appear in each subsequent round. The number of disks and the Round Completion Bonus will remain the same value for the rest of the game. The characters and play action will gain more speed with each increased level of play.

	ROUND	DISKS	CHARACTERS ON SCREEN	ROUND COMPLETION BONUS
LEVEL I	1	2	Red Balls, Coily	1000
	2	2	Red Balls, Coily	1250
	3	2	Coily, Green Ball, Ugg/Wrong way, Slick/Sam	1500
	4	2	Red Balls, Coily, Green Ball, Slick/Sam	1750
LEVEL II	1	3	Coily, Green Ball, Ugg/Wrong Way, Slick/Sam	2000
	2	3	Coily, Green Ball, Ugg/Wrong Way, Slick/Sam	2250
	3	2	Red Balls, Coily, Green Ball, Slick/Sam	2500
	4	2	Red Balls, Coily, Green Ball, Ugg/Wrong Way, Slick/Sam	2750
LEVEL III	1	4	Red Balls, Coily, Green Ball, Slick/Sam	3000
	2	4	Coily, Green Ball, Ugg/Wrong Way, Slick/Sam	3250
	3	3	Red Balls, Coily, Green Ball, Ugg/Wrong Way, Slick/Sam	3500
	4	3	Red Balls, Coily, Green Ball, Ugg/Wrong Way, Slick/Sam	3750
LEVEL IV	1	6	Red Balls, Coily, Green Ball, Slick/Sam	4000
	2	6	Red Balls, Coily, Green Ball, Ugg/Wrong Way, Slick/Sam	4250
	3	5	Red Balls, Coily, Green Ball, Slick/Sam	4500
	4	4	Red Balls, Coily, Green Ball, Ugg/Wrong Way, Slick/Sam	4750
LEVEL V	1 2 3 4	7 6 6 5	In Level V all characters will appear in each Round	4750 5000 5000 5000
LEVEL VI THRU LEVEL IX	1 2 3 4	5 5 5 5	All characters will appear in each Round in Level VI thru Level IX	5000 5000 5000 5000

SCORING

- Luring "Coily" off the edge Scores 500 points and clears pyramid of characters
- Hopping onto "Slick" or "Sam" Scores 300 points
- Hopping onto Green Ball
 Scores 100 points and freezes characters
 but not "Q*Bert"
- Changing cubes to designated color Scores 25 points
- Changing cubes to intermediate color (in Level II or Up)
 Scores 15 points
- Unused disks
 Scores 50 points
- Round Completion Bonus See Round Progression Chart



V. SOUND/SPEECH, VI. GAME ADJUSTMENTS / OPTIONS

V. SOUND/SPEECH

ATTRACT MODE

SPEECH

OCCURENCE

"HELLO, I'M TURNED ON"

When game is powered up.

GAME MODE

SPEECH

OCCURENCE

(Garbled Nonsense Speech)

When "Q*Bert" is killed.

When the characters "Slick/Sam" and

"Ugg/Wrong Way" are present.

"BYE, BYE"

DEMONSTRATION MODE*

When player has finished entering his

initials on high score table.

VI. GAME ADJUSTMENTS/OPTIONS

A. CONTROL BOARD SWITCH ADJUSTMENTS

SWITCH 1

DEMONSTRATION MODE.
INFINITE LIVES
NORMAL PLAY
ATTRACT PLAY
NO SOUND
SOUND
NORMAL/FREE
FREE PLAY
NORMAL GAME
GAME MODE
COCKTAIL
UPRIGHT
NOT USED
KICKER
KICKER ON
KICKER OFF
NOT USED
NOT USED

^{*}IN DEMONSTRATION MODE THE PLAYER WILL HAVE INFINITE LIVES AND CAN PROGRESS THROUGH THE ROUNDS BY HITTING EITHER START BUTTON.

B. SOUND ADJUSTMENTS

The audio output is controlled by the potentiometer mounted on the service panel assembly (located inside the coin mechanism door).

Turning the potentiometer counter-clockwise will decrease the volume. Turning it clockwise will increase the volume.

IMPORTANT: Each of the potentiometers installed on the Sound/Speech board have been factory adjusted. The potentiometer settings should never be changed when performing the recommended calibration procedure.

C. MONITOR ADJUSTMENTS

Normally, few if any adjustments are required for proper monitor operation. However, after any major repairs to the monitor chassis refer to the attached monitor manual.

VII. BOOKKEEPING AND SELF TEST

BOOKKEEPING

The battery back-up bookkeeping functions of Q*Bert (GV-103) are contained in Self Test steps 3 and 4. These are in addition to the electromechanical coin counter located inside the front door panel. Every time a coin is inserted into a coin slot, the counter is energized, incrementing the count.

SELF TEST

The self-test consists of six functions which may be used to identify problems in the video system and to change program parameters.

The self-test mode is entered by setting the self-test toggle switch located inside the cash door to "TEST". A selection of available tests is displayed on the monitor. To return to the GAME mode at any time, the operator needs only to set the toggle switch back to "GAME".

Selection of tests is done with the push button switch labled "SELECT". Upon entering the test mode, a flashing arrow points to the first test selection. Momentarily depressing the "SELECT" button will advance through each selection one by one.

When the arrow is pointing to the desired test, the operator may begin that test by pressing the "SELECT" button and holding it down until the test appears on the screen.

Once a test has been selected, the operator can return to the selection list by holding down the select switch until it re-appears. The eight tests are as follows:

1. MONITOR ADJUSTMENT

Four patterns can be displayed on the screen for adjusting monitor color, brightness, contrast and convergence. The patterns are: Color bars, a cross-hatch, a gray scale, and a dot pattern. By momentarily pressing the select switch, the operator may cycle through the four patterns.

2. DIP SWITCHES

A functional description of the eight Dip Switches located on the Logic Board Assy. is displayed. Changing any switch will cause an immediate update of the description displayed on the screen.

3. DISTRIBUTIONS

Selecting this test will first display a distribution option. The distributions can be reset to zero by pressing either start button, and then pressing the SELECT button momentarily. Following the latter, a cleared distribution screen will be displayed; or the distribution screen can be viewed without clearing it by pressing the SELECT button momentarily when in this test mode. The distribution screen will show three categories of counts — 1) Level/Round; 2) Time; 3) Score. These categories, used with the coin meter count, can be used to derive the game percentages and averages.

The categories are presented in three vertical pairs of columns displaying the level of category and the number of players to attain that level. The left category is a list of the Levels and Rounds up to Level 3, Round 4. Next to each Level/Round is the number of players to reach that Level/Round. The middle two columns are a list of game durations in 45 second increments and the number of players to last that long next to it. The right two columns are a category of players scores in increments of 3000 points and the number of players to attain that score level.

At the bottom of each category will be displayed the number of players to go beyond the defined levels. The number of players in each category level are independent of the other categories, so each player will be listed once each for Level, Time and Score. The number of players in each category level are given in 4 digit values only, so the distribution table should be reset every two weeks or so to insure that meaningful information will be contained in it.

4. OPTIONS/PARAMETERS

This test will allow the operator to view and change all game options on one screen. During this test the screen will display seven operator adjustable options. Pressing the "SELECT" button momentarily will advance the arrow to the next option desired. When the arrow is pointing to the appropriate option, the

VII. BOOKKEEPING AND SELF TEST

operator can then adjust that option by pressing either of the control panel start buttons, to select the desired value for each option.

- A. Reset High Score Table Pressing either start button will reset all 23 high scores to random values and initials starting at 3000 points for No. 1.
- B. Factory Preset Using this option will reset all the following options to the factory recommended levels: 1 Coin/1 Credit, 3 Lives, Normal Difficulty, 1st Extra Life at 8000 Points, Each additional life at each subsequent 14000 Points.
- C. Coin/Credit Combinations Pressing either Start button will cycle thru three coin combinations:
 - 1) 1 coin = 1 play
 - 2) 1 coin = 2 play
 - 3) 2 coin = 1 play
- D. Lives Per Game Pressing either Start button will cycle thru three choices; 3 Lives Per Game, 4 Lives Per Game, 5 Lives Per Game.
- E. Difficulty Two choices may be selected with either Start button; Normal or Hard.
- F. 1st Extra Life There are six choices, from 6000 Points to 11000 Points, that will be displayed by pressing either Start button. Any value can be chosen to award the first extra life by stopping on that choice and then selecting the next option.
- G. Each Additional Life There are six choices, from 12000 Points to 17000 Points, that will cycle thru by pressing either Start button. Any value can be selected to award additional lives at each subsequent Point level chosen.

5. MEMORY

For each RAM memory chip; a green check (\checkmark) or red (x) appears signaling that the chip is good or bad respectively.

For each ROM memory chip, a check sum is displayed. If you have a suspect ROM, refer to your distributor for the correct check sum number.

6. SWITCHES

A colored square is displayed for each player button. Pressing a button causes the appropriate square to change color. For each coin mechanism, a digit is displayed (initially 0). Inserting a coin into a coin chute will increment the appropriate value without affecting the coin meter.

7. SOUND TEST

After selecting this test a count will appear on the screen representing the various sounds that are produced by the Q*Bert game. There will be 36 different sounds produced and the screen count will stop at 41. Pressing either Start button will suppress all sound output and speed up the count so a particular sound can be sought out and checked.

Note: The count on the screen represents the binary signal code that will be sent to the A6 Sound/Speech board through the six sound input lines on the A6J1 connector. When executing the Sound Test sequence, there will be no sounds produced on counts 16, 29, 30, 31 and 32. There are no sounds assigned to these numbers.

8. OBJECT PRIORITY

Visual inspection must be used to determine the priority of two or more objects occupying the same area of the screen; that is, which objects appear to be in front of others and which are behind.

A total of 62 identical objects are placed on the screen in 4 rows. Each object overlaps another such that the first object appears to be in front, and succeeding objects appear to be placed behind all previous ones. When this display is completed, the procedure is repeated such that each new object appears to be in front of all the previous ones.

VIII. GENERAL INFORMATION

A. PRINTED CIRCUIT BOARDS ARE DESIGNATED AS FOLLOWS:

A1 Logic Board Assy.

A3 Power Supply Assy.

A6 Sound/Speech Assy.

B. WIRE COLORS ARE SHOWN AS NUMBERS:

0 Black	5 Green
1 Brown	6 Blue
2 Red	7 Purple
3 Orange	8 Slate
4 Yellow	9 White

For example, 688 is a BLUE- SLATE-SLATE striped wire.

C. FUSES

BOTTOM PANEL

F1	Primary Power		4 Amp SLO-BLO
F2	6.3 VAC		3 Amp SLO-BLO
F3	Monitor		2 Amp SLO-BLO
F4	9 VAC		10 Amp SLO-BLO
F5	15 VAC		1 Amp SLO-BLO
F6	Knocker	+30VDC	1 Amp SLO-BLO

POWER SUPPLY ASSY. (A3)

F1 1	+5VDC Source		5 Amp SLO-BLO
F2 1	Sound/Speech Assy.	+30VDC	1½ Amp SLO-BLO
F31	Sound/Speech Assy.	+12VDC	1/4 Amp SLO-BLO
F32	Sound/Speech Assy.	-12VDC	1/4 Amp SLO-BLO
F4 1	Coin Meter	+20VDC	1 Amp SLO-BLO

VIII. GENERAL INFORMATION

POWER SUPPLY SPECIFICATIONS

LOCATION	VOLTAGE	PROTECTION
Logic Board Assy.	+5VDC	Voltage adjustable. 6Amps over-voltage protection and fused for over-current protection.
Sound/Speech Board	eech Board +30VDC 1.5Amps fused for over- reference for this circuit Zener controlling the b follower pass transistor	
Sound/Speech Board	+12VDC -12VDC	100 milliamps fused for over-current protection. The plus and minus 12 volts supplies are the 7812 and 7912 IC regulators respectively.
Coin Meter	+20VDC	Full wave rectified unfiltered voltage, fused for over-current protection.
Coin Chute Lights	+4.5VDC	Full wave rectified unfiltered voltage, fused for over-current protection.
Monitor and Marquee	100VAC or 115VAC, 60HZ	Isolated, fused AC voltage.

IX. THEORY OF OPERATION

INTRODUCTION

The character based graphics system designated GG-II has two main subdivisions. The first subdivision is the Central Processor Unit (CPU) which has three partitions:

- a. Microprocessors
- b. Memory
- c. Input and Output ports (I/O)

The Intel 8088 microprocessor is used and 32K bytes of memory is reserved for programming space and has 5 input ports and 5 output ports. The second subdivision is the video state machine which generates and controls the video signal to the monitor. The state machine has three partitions:

- a. System Clock (CLK)
- b. Foreground generator (FGND)
- c. Background generator (BGND)

The system clock is driven by a 20MHZ crystal, divided down for a 5MHZ dot clock.

All inputs and outputs including the video control and general purpose I/O are memory-mapped, (i.e. everything within the system can be addressed in a single segment of 64K addresses as memory).

The video control unit is divided into an "object-oriented" foreground driver and "character-oriented"

background driver. The screen resolution is 256 pixels horizontally, and 240 lines vertically for both foreground and background. The CPU communicates with the foreground driver and background driver by writing data into the designated memory areas in a certain format. The foreground is designed to display moving objects on the screen with a minimum overhead to the processor. The game programs will only have to specify the vertical and horizontal position and the object select number to the foreground driver. The background video supplements the foreground with relatively static figures on the screen. The CPU specifies all the character positions on the screen with desired "character" patterns.

A 5MHZ system clock drives a 9 bit horizontal dot counter and an 8 bit vertical line counter. The horizontal counter counts from 0 to 255 during active scan line and 256 to 317 during horizontal blanking time. When the horizontal counter reaches 317, the horizontal counter resets to 0. At the beginning of the horizontal blanking time (horizontal counter = 256) it increments the vertical counter. The vertical counter counts from 0 to 239 during active vertical scan time and 240 to 255 during vertical blanking time.

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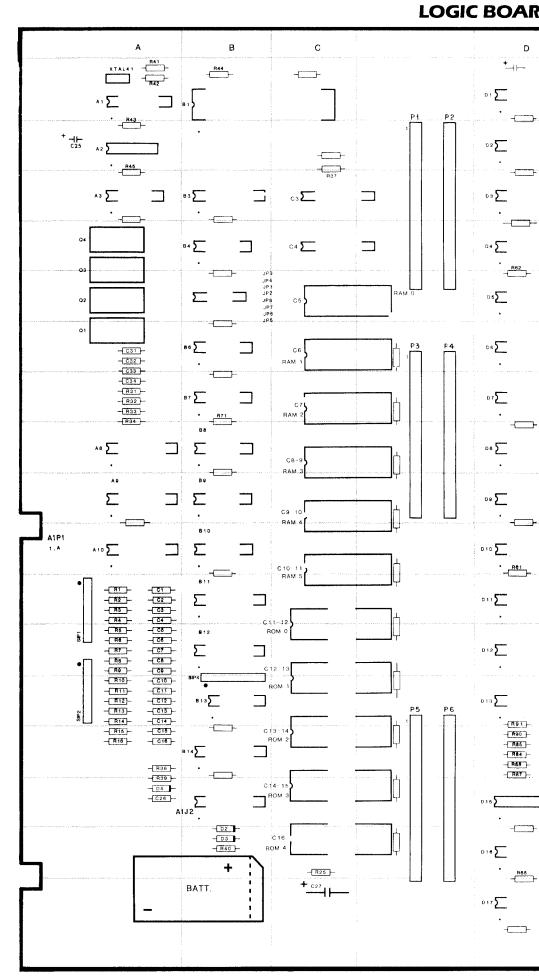
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X. WIRING AND SCHEMATI

LOGIC BOARD ASSY. (A1), PARTS LIST

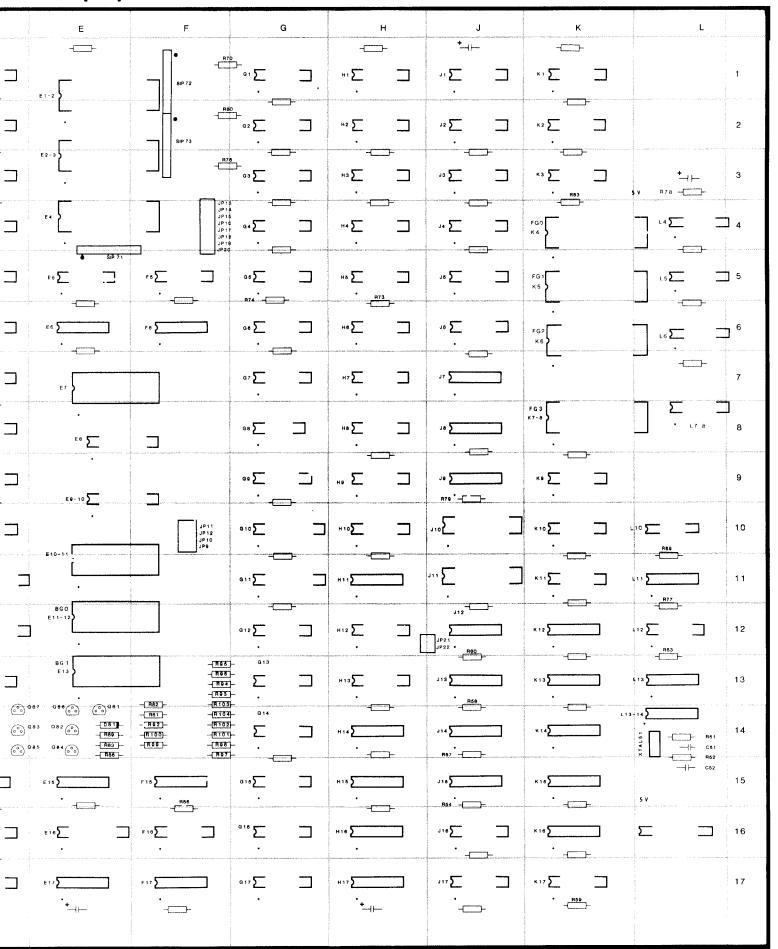
MISCELLANEOUS ELECTRONIC COMPONENTS

C27		COMPONENTS	
C1-C16	REFERENCE	DESCRIPTION	
C25 C26 C26 C27 C27 C27 C27 C31-34 C31-34 C32-26 C51 C31 C31 C31 C31 C31 C31 C31 C31 C31 C3		Capacitor, 0.1 UF 50V AX. CR.	
C27 C31-34 C3pacitor, 100 UF, 25V EL-AX C3pacitor, 0.1 UF, 50V AX. CR. +80%-20% C51 Capacitor, 100 PF, 100V CMD 5% CAPACITORS ALL UNMARKED CAPACITORS ALL POLARIZED UNMARKED CAPACITORS D2 Diode, 1N4454 Diode, 1N4733A D81 Diode, 1N4148 C9-275 C81-Q87 C74 C81-Q87 C77 C81-R16 Resistor, 270 OHM, 5% VAW C9-38 R41, R42 Resistor, 130 OHM, 5% VAW C9-58 R57, R58 Resistor, 130 OHM, 5% VAW C9-58 R57, R58 Resistor, 180 OHM, 5% VAW C9-58 R57, R58 Resistor, 180 OHM, 5% VAW C9-58 R57, R58 Resistor, 180 OHM, 5% VAW C9-58 R59-R61 Resistor, 180 OHM, 5% VAW C9-58 R63, R64 Resistor, 180 OHM, 5% VAW C9-58 R64, R64 Resistor, 180 OHM, 5% VAW C9-58 R65, R59 R61 Resistor, 180 OHM, 5% VAW C9-58 R64, R87 Resistor, 180 OHM, 5% VAW C9-58 R65, R87 Resistor, 180 OHM, 5% VAW C9-58 R68, R87 Resistor, 180 OHM, 5% VAW C9-58 R69		Capacitor, 100 UF, 25V EL-AX Capacitor, 0.1 UF, 50V AX. CR.	XO-212 XO-230
C51 C52 CADACITOR, ALL UNMARKED CAPACITORS ALL POLARIZED UNMARKED CAPACITORS Diode, 1N4454 Diode, 1N4733A CO-274 D4 Diode, 1N4733A CO-274 D8I Diode, 1N4148 CO-261 CAPACITORS D1 Diode, 1N4148 CO-261 CAPACITORS CRIBBER CAPACITORS ACO-229		Capacitor, 100 UF, 25V EL-AX Capacitor, 0.1 UF, 50V AX. CR.	
CAPACITORS .01 UF, 50V AX. CR. +80%-20% XO-229 ALL POLARIZED UNMARKED .01 UF, 25V AX. TANT. 10% XO-127 D2 Diode, 1N4454 XO-275 D4 Diode, 1N4148 XO-261 D81 Diode, 1N4148 XO-261 O1-O4 Transistor, 2N6044 XO-309 RB-D87 Transistor, MPSA70 XO-3309 R1-R16 Resistor, 330 OHM, 5% 1/4W XO-34 R38 Resistor, 330 OHM, 5% 1/4W XO-34 R39 Resistor, 330 OHM, 5% 1/4W XO-68 R41, R42 Resistor, 130 OHM, 5% 1/4W XO-58 R43 Resistor, 18 OHM, 5% 1/4W XO-5 R51, R52 Resistor, 18 OHM, 5% 1/4W XO-5 R51, R52 Resistor, 18 OHM, 5% 1/4W XO-5 R57, R58 Resistor, 16 OHM, 5% 1/4W XO-5 R57, R58 Resistor, 16 OHM, 5% 1/4W XO-5 R63, R64 Resistor, 18 OHM, 5% 1/4W XO-5 R73, R74 Resistor, 18 OHM, 5% 1/4W XO-5 R76-R80 Resistor, 18 OHM, 5% 1/4W XO-5	C52	Capacitor, 100 PF, 100V CMD 5%	
CAPACITORS 10 UF, 25V AX. TANT. 10% XO-127 D2 Diode, 1N4454 XO-275 D4 Diode, 1N41733A XO-275 D81 Diode, 1N4148 XO-261 O1-O4 Transistor, 2N6044 XO-120 O8I-O87 Transistor, MPSA70 XO-309 R1-R16 Resistor, 470 OHM, 5% 1/4W XO-34 R38 Resistor, 330 OHM, 5% 1/4W XO-34 R39 Resistor, 270 OHM, 5% 1/4W XO-34 R40 Resistor, 130 OHM, 5% 1/4W XO-58 R41 Resistor, 180 OHM, 5% 1/4W XO-58 R43 Resistor, 180 OHM, 5% 1/4W XO-5 R51, R52 Resistor, 180 OHM, 5% 1/4W XO-5 R57, R58 Resistor, 180 OHM, 5% 1/4W XO-36 R59-R61 Resistor, 180 OHM, 5% 1/4W XO-5 R63. R64 Resistor, 180 OHM, 5% 1/4W XO-5 R73, R74 Resistor, 180 OHM, 5% 1/4W XO-5 R73, R74 Resistor, 180 OHM, 5% 1/4W XO-5 R81 Resistor, 180 OHM, 5% 1/4W XO-171 R	CAPACITORS ALL POLARIZED	.01 UF, 50V AX. CR. +80%-20%	XO-229
SIP 73 X-TAL 1	UNMARKED CAPACITORS D2 D4 D81 O1-O4 O81-O87 R1-R16 R38 R39 R40 R41, R42 R43, R45 R51, R52 R53, R54, R56 R57, R58 R59-R61 R63, R64 R70 R73, R74 R76-R80 R81 R82 R83, R84 R85 R86, R87 R88 R89, R90 R91 R92 R93 R94 R95 R96 R97 R98 R99 R100 R101 R102 R103 R104 SIP 1, SIP 2, SIP 4 SIP 71, SIP 72, SIP 4 SIP 71, SIP 72, SIP 72, SIP 73 X-TAL 1	Diode, 1N4454 Diode, 1N473AA Diode, 1N474BA Diode, 1N474BA Transistor, 2N6044 Transistor, 470 OHM, 5% ¼W Resistor, 330 OHM, 5% ¼W Resistor, 130 OHM, 5% ¼W Resistor, 140 OHM, 5% ¼W Resistor, 150 OHM, 5% ¼W Resistor, 160 OHM, 5% ¼W Resistor, 170 OHM, 5% ¼W Resistor, 170 OHM, 5% ¼W Resistor, 180 OH	XO-275 XO-274 XO-261 XO-309 XO-35 XO-34 XO-172 XO-5 XO-5 XO-5 XO-5 XO-5 XO-5 XO-5 XO-5 XO-5 XO-174 XO-24 XO-171 XO-24 XO-171 XO-24 XO-171 XO-24 XO-171 XO-24 XO-171 XO-24 XO-171 XO-24 XO-171 XO-25 XO-173 XO-14 XO-5 XO-173 XO-14 XO-5 XO-173 XO-14 XO-5 XO-173 XO-174 XO-5 XO-173 XO-173 XO-174 XO-5 XO-173 XO-174 XO-5 XO-175 XO-175 XO-177 XO-176 XO-177 XO-177 XO-178 XO-179 XO-179 XO-179 XO-179 XO-170 XO-170 XO-171 XO-24 XO-171 XO-24 XO-171 XO-24 XO-171 XO-24 XO-171 XO-24 XO-171 XO-27 XO-171 XO-27 XO-173 XO-174 XO-175 XO-177



C DIAGRAMS, PARTS LISTS

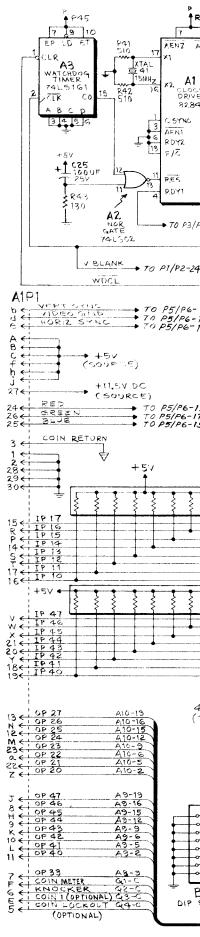
RD ASSY. (A1), COMPONENT LOCATION

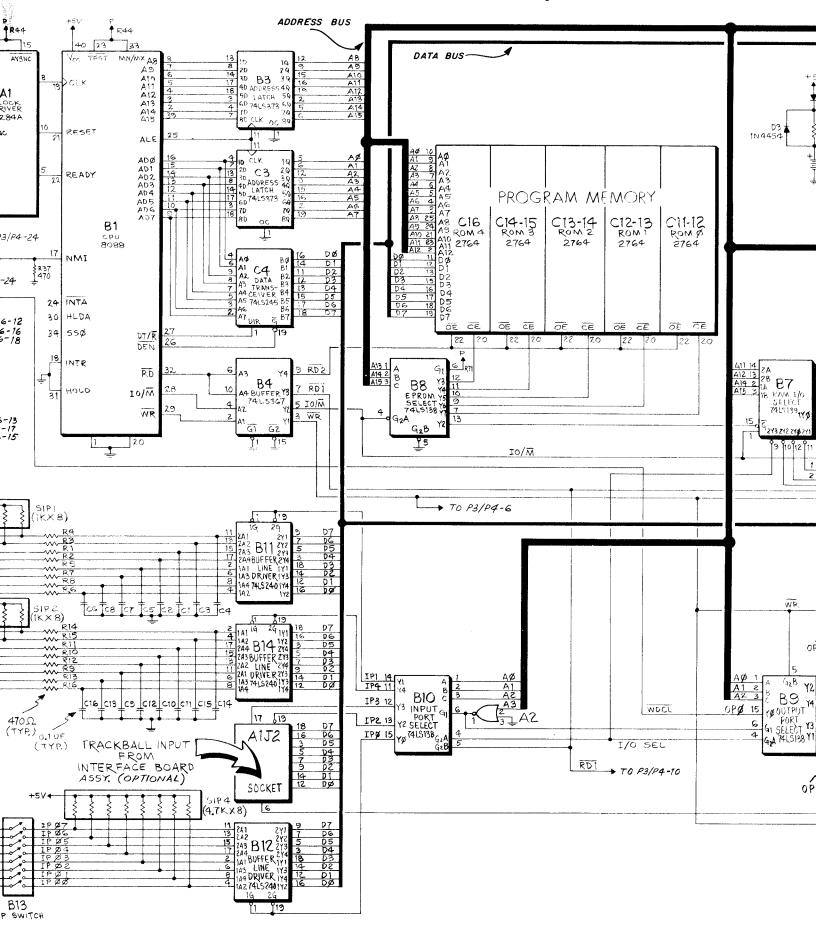


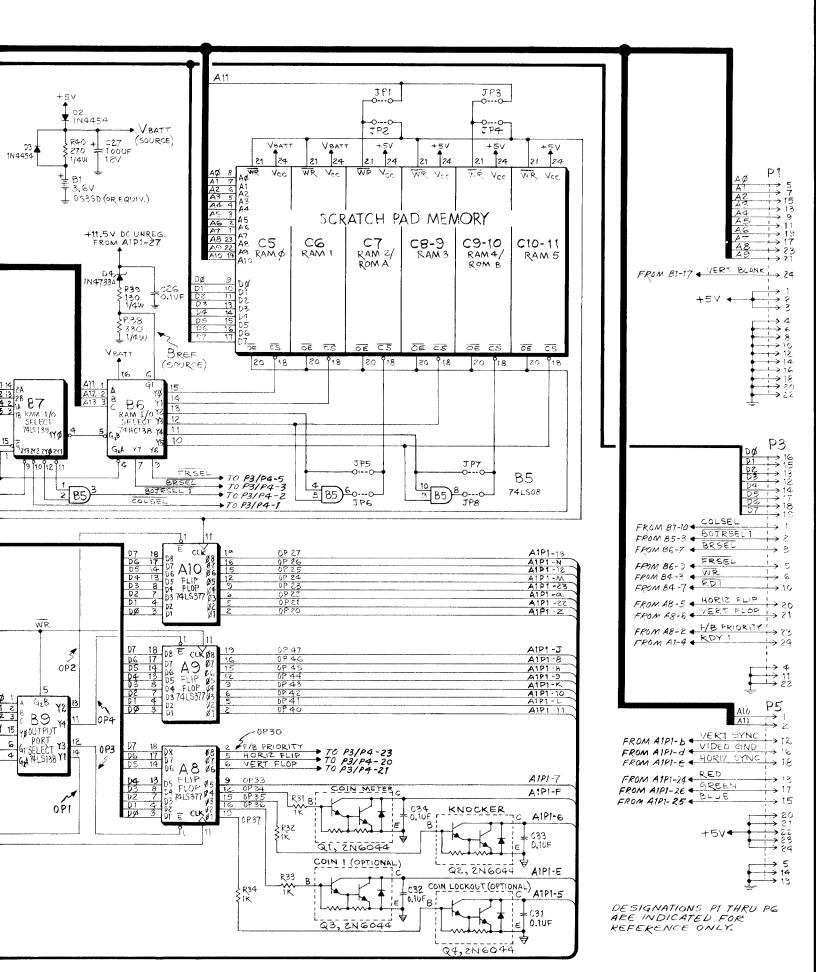
LOGIC BOARD ASSY. (A1), PARTS LIST (CONT.)

INTEGRATED CIRCUITS

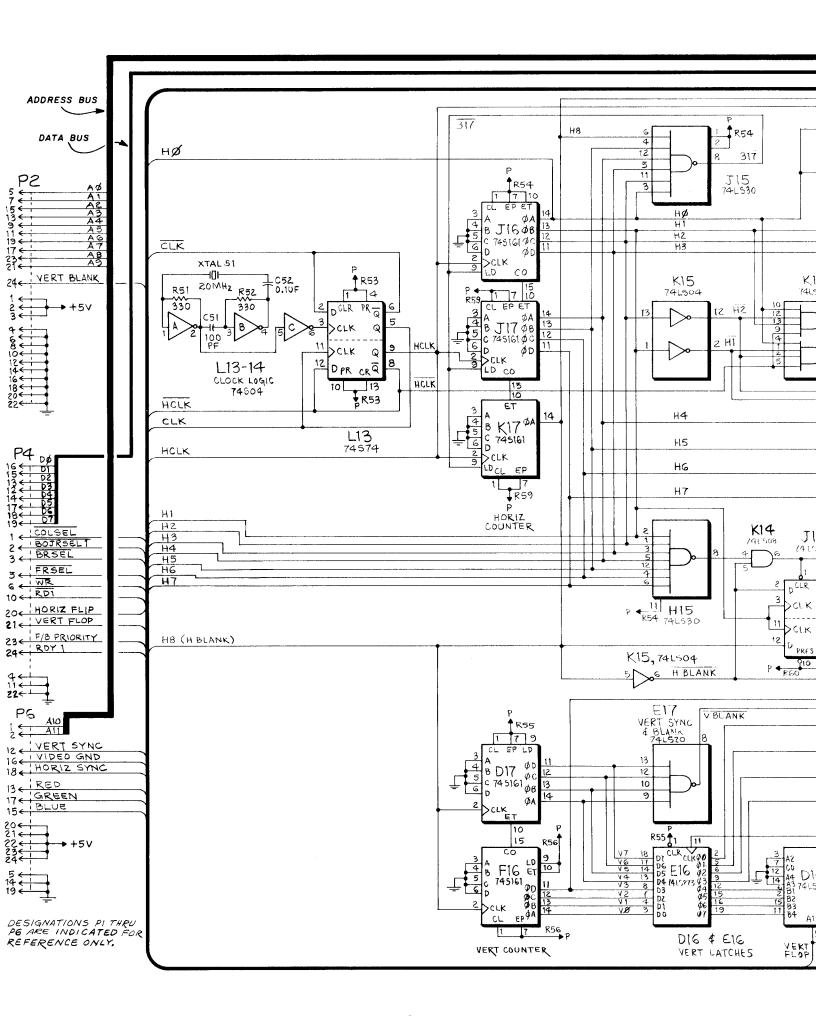
REFERENCE	DESCRIPTION	PART NO.	REFERENCE	DESCRIPTION	PART NO.
	Logic Board Assy.	MA-378	G7	74157 Quad 2-input multiplexer	XO-114
Αì	8284 CLK Driver	XO -4 78	G8	74LS74 Dual D-type flip flop	XO-434
A2	74LS02 Quad 2-input "NOR" gate	XO-428	G9	74S157 Quad 2-input multiplexer	XO-124
A3	74161 Synchronous 4-bit counter	XO-192	G10	74LS245 Octal bus transceiver	XO-79
A8, A9, A10	74LS377 Octal "D" Flip Flop	XO-97	G11	74LS374 Octal D-type flip flop	XO-96
B1	8088 CPU	XO-490	G12	74LS157 Quad 2-input multiplexer	XO-390
B3	74LS373 Octal D-type flip flop	XO-445	G13, G14, G15	7489 64-bit RAM	XO-88
B 4	74LS367 Hex 3-state buffer	XO-444	G16, G17	74LS174 Hex D flip flop	XO-442
B5	74LS08 Quad 2-input "AND" gate	XO-86	H1, H2, H3, H4	74\$189 64-bit RAM	XO-89
B6	74HC138 Decoder/demultiplexer	XO-190	H5, H6,	74S161 Synchronous presettable	XO -4 88
B7	74LSI39 Dual 1 of 4 decoder	XO-419		binary counter	
B8, B9, B10	74LSI38 I of 8 dedocer	XO-437	H7, H8, H9, H10	74LS157 Quad 2-input multiplexer	XO-390
B11, B12, B14	74LS240 Octal Buffer/line driver	XO-91	HII	74LS260 Dual 5-input "NOR" gate	XO-93
C3	74LS373 Octal D-type flip flop	XO-445	H12	74LS298 Quad 2-port register	XO-118
C4	74LS245 Octal Bus transceiver	XO-79	H13	74LS157 Quad 2-input multiplexer	XO-390
C5	RAM Ø 6116LP-4	XO-191	H14	74LS00 Quad 2-input	XO-427
C6	RAM 1 6116LP-4	XO-191		"NAND" gate	
C7	RAM 2 2128-2	XO-195	H15	74LS30 8 input "NAND" gate	XO-432
C8-9	RAM 3 2128-2	XO-195	H16, H17	74LS86 Dual 2-input exclusive	XO -4 35
C11-12	ROM Ø 2764 8K x 8 EPROM	XO-489	u 10 10	"OR" gate	
C12-13	ROM 1 2764 8K x 8 EPROM	XO-489	JI, J2, J3,	745100 / 4 5% D444	VO 00
C13-14	ROM 2 2764 8K x 8 EPROM	XO-489	J4, J5, J6	74S189 64-bit RAM	XO-89
D1	74LS139 Dual 1 of 4 Decoder	XO-419	J7 J8	74LS04 Hex inverter	XO-418
D2, D3, D4, D5,			J9	74LS32 Quad 2-input "QR" gate 7408 Quad 2-input "AND" gate	XO-433 XO-404
D6, D7, D8, D9, D10	74157 0 4 2 (4 16-4	VO 114	110, J11	93422 256 x 2 bipolar RAM	XO-100
Dil	74157 Quad 2-input multiplexer 74LS374 Octal D-type flip flop	XO-114 XO-96	J10, J11	74LS02 Quad 2-input "NOR" gate	XO-428
D12	74LS374 Octal D-type filp flop 74LS244 Octal buffer/line driver	XO-96 XO-117	J13	74LS74 Dual D-type flip flop	XO-434
D12 D13	74LS157 Quad 2-input multiplexer	XO-390	J14	7407 Hex buffer/driver	XO-384
D15	74LS86 Quad 2-input exclusive	XO-435	J15	74LS30 8 input "NAND" gate	XO-432
Dis	"OR" gate	VO-133	J16, J17	74S161 Synchronous presettable	XO-488
D16	74LS283 4-bit binary full adder	XO-95	2.2, 2	binary counter	7.0
D17	74S161 Synchronous presettable	XO-488	K1, K2, K3	74LS379 Quad D-type flip flop	XO-98
2.,,	binary counter	7.0 100	K4	FG0 2764-3 8K x 8 EPROM	XO-489
E1-2, E2-3, E4	93419 64 x 9 bipolar RAM	XO-99	K5	FG1 2764-3 8K x 8 EPROM	XO-489
E5	74LS283 4-bit binary full adder	XO-95	K6	FG2 2764-3 8K x 8 EPROM	XO-489
E6	74LS30 8-input "NAND"	XO-432	K7-8	FG3 2764-3 8K x 8 EPROM	XO-489
E7	4801 1K x 8 RAM	XO-193	K9, K10, K11	74LS157 Quad 2-input multiplexer	XO-390
E8, E9-10	74LS245 Octal Bus Transceiver	XO-79	K12	74LS260 Dual 5-input	XO-93
E10-11	4801 1K x 8 RAM	XO-193		"NOR" gate	
E11-12	2732A (BGØ) 4K x 8 EPROM	XO-485	K13	74LS32 Quad 2-input "OR" gate	XO -4 33
E13	2732A (BGI) 4K x 8 EPROM	XO -4 85	K14	74LS08 Quad 2-input	XO-86
E15	74LS86 Quad 2-input exclusive	XO-435		"AND" gate	
	"OR" gate		K15	74LS04 Hex inverter	XO-418
E16	74LS273 8-bit register	XO-94	K16	74LS20 Dual 4-input	XO-430
E17	74LS20 Dual 4-input "NAND" gate	XO-430		"NAND" gate	
F5	74LS283 4-bit binary full adder	XO-95	K17	74S161 Synchronous presettable	XO-488
F6	74LS32 Quad 2-input "OR" gate	XO -4 33		binary counter	
F15	74LS04 Hex inverter	XO-418	L4, L5, L6, L7	74LS166 8-bit shift register	XO-391
F16	74S161 Synchronous presettable	XO-488	L10	74LS74 Dual flip flop	XO-434
	binary counter		LII	74LS20 Dual 4-input	XO-430
F17	74LS86 Quad 2-input exclusive	XO-435	113	"NAND" gate	VO 440
C1 C2 C2	"OR" gate		L12	74LS161 Synchronous presettable	XO-440
G1, G2, G3,	7416167 0 - 4 2 : 4 - 2'	VO 300	113	binary counter	V() 07
G4, G5	74LS157 Quad 2-input multiplexer	XO-390	LI3	74S74 Dual D-type pos. edge trig. flip flop (T. I. only)	XO-87
G6	74LSI61 Synchronous presettable	XO-440	L13-14	74S04 Hex inverter	XO-400
	binary counter		LIJ 11	7 7301 FIEA MINERALI	7.0 roo

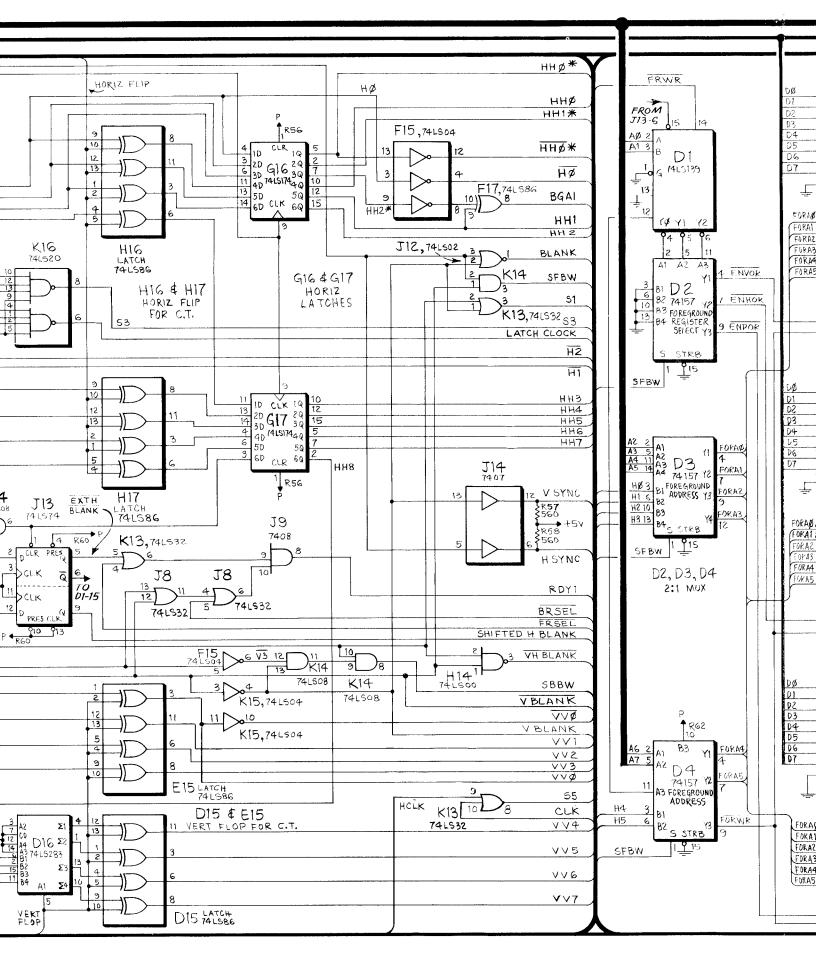


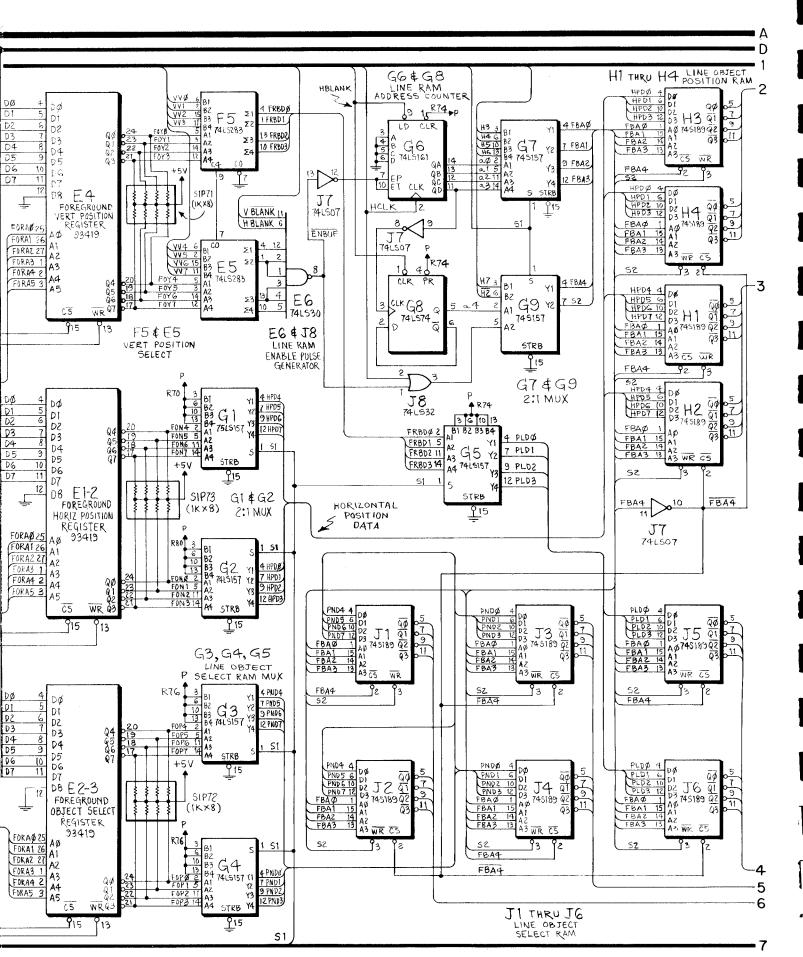




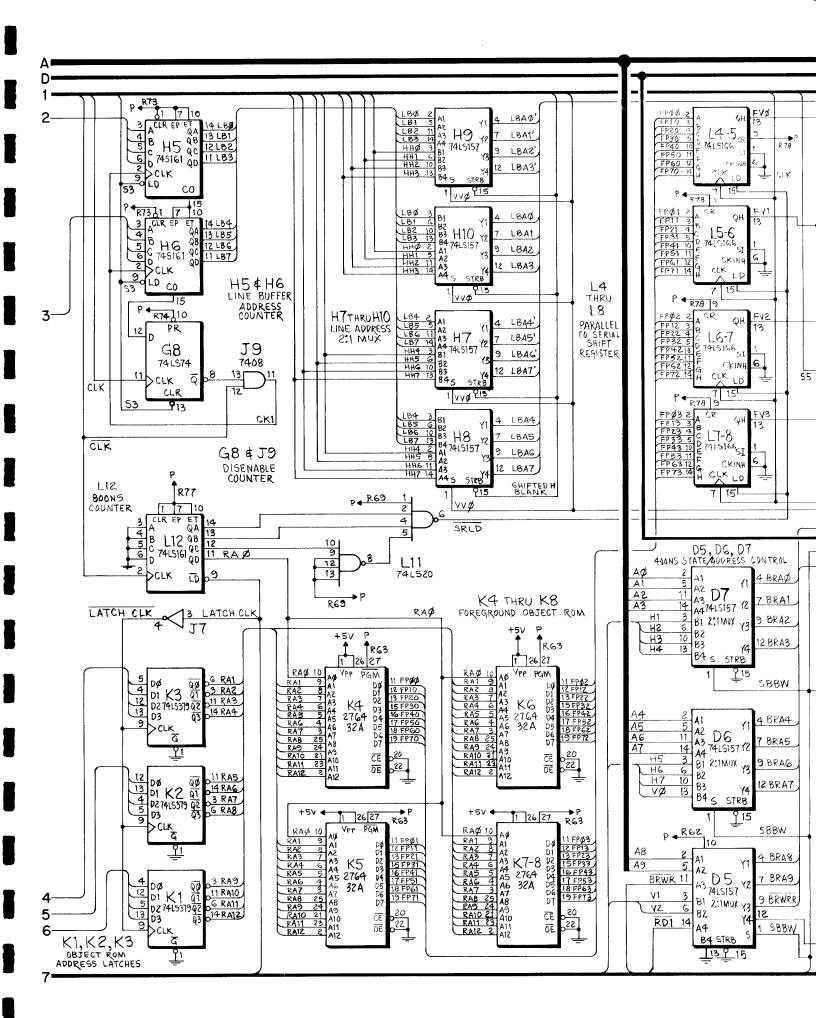
LOGIC BOARD ASSY. (A1), SCHEMATIC DIAGRAM, SHEET 1 OF 3

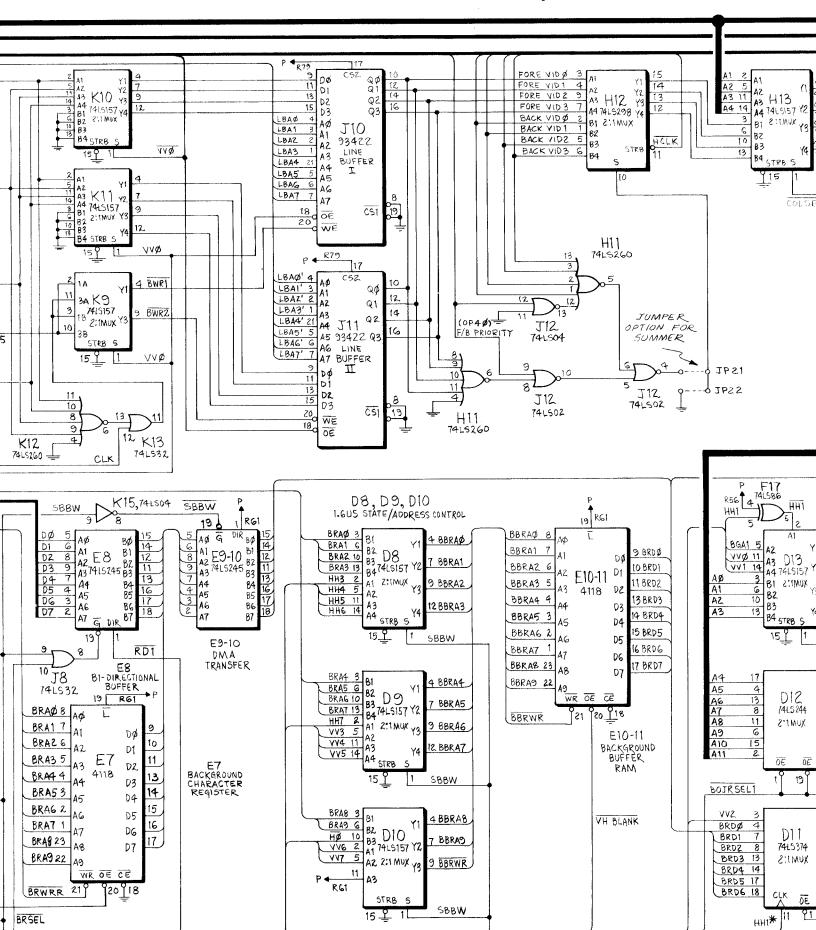


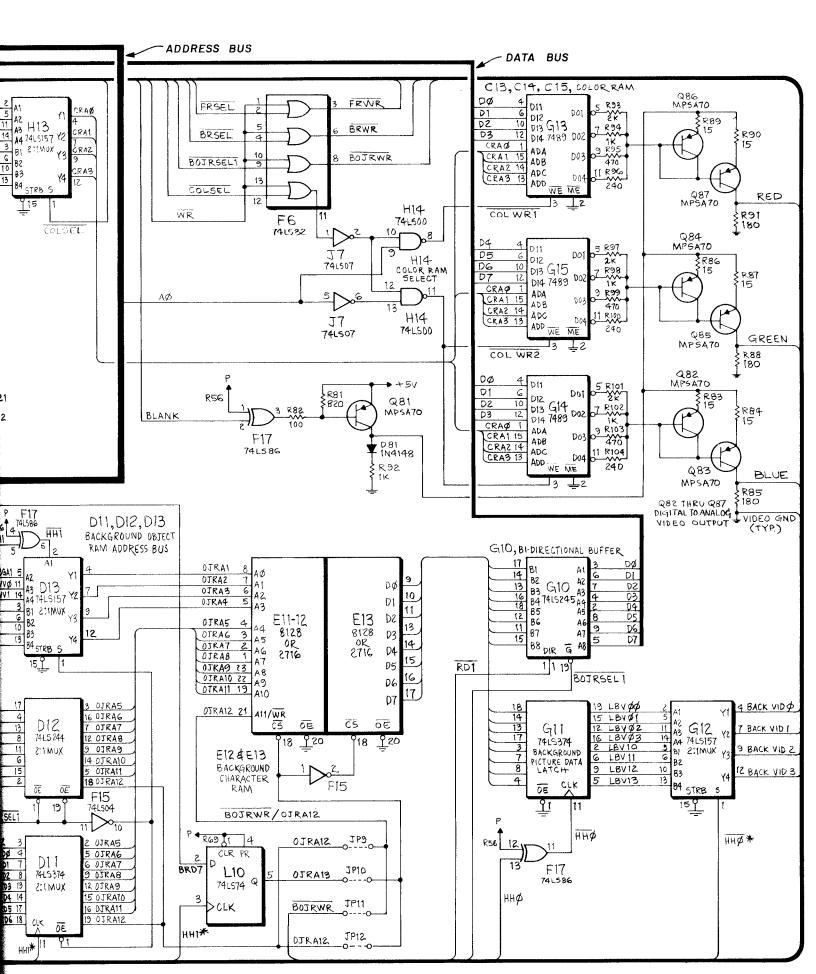




LOGIC BOARD ASSY. (A1), SCHEMATIC DIAGRAM, SHEET 2 OF 3

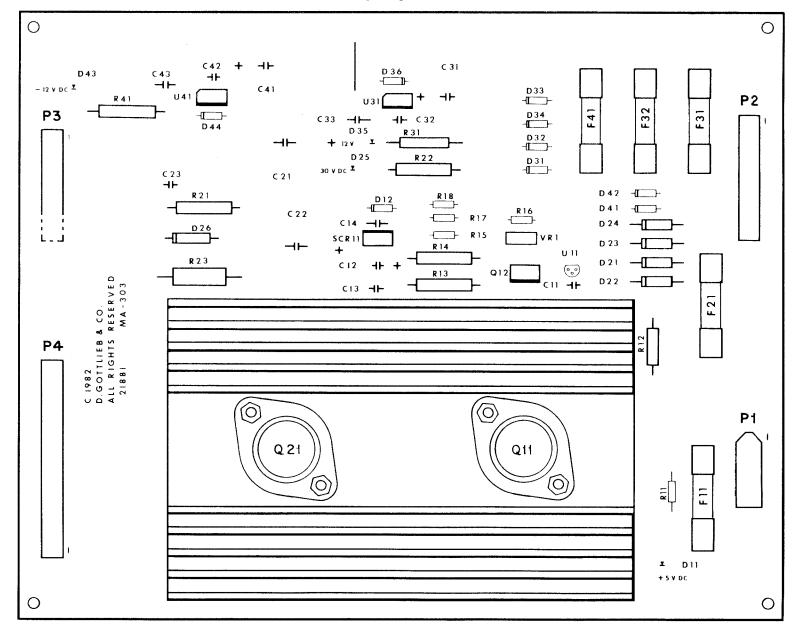






LOGIC BOARD ASSY. (A1), SCHEMATIC DIAGRAM, SHEET 3 OF 3

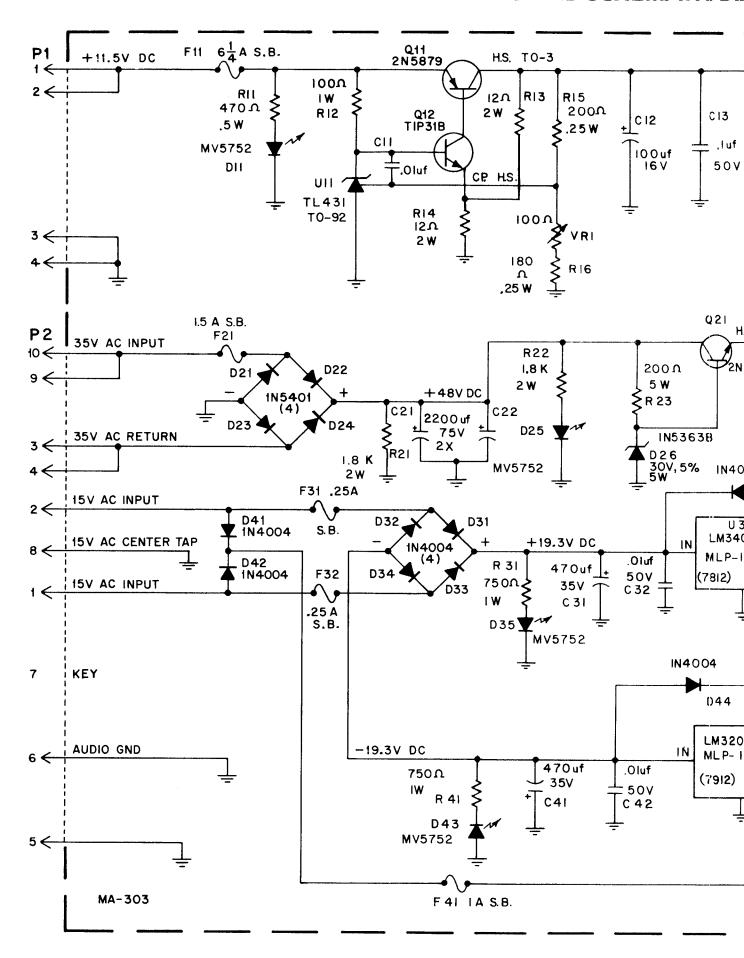
POWER SUPPLY ASSY. (A3), COMPONENT LOCATION



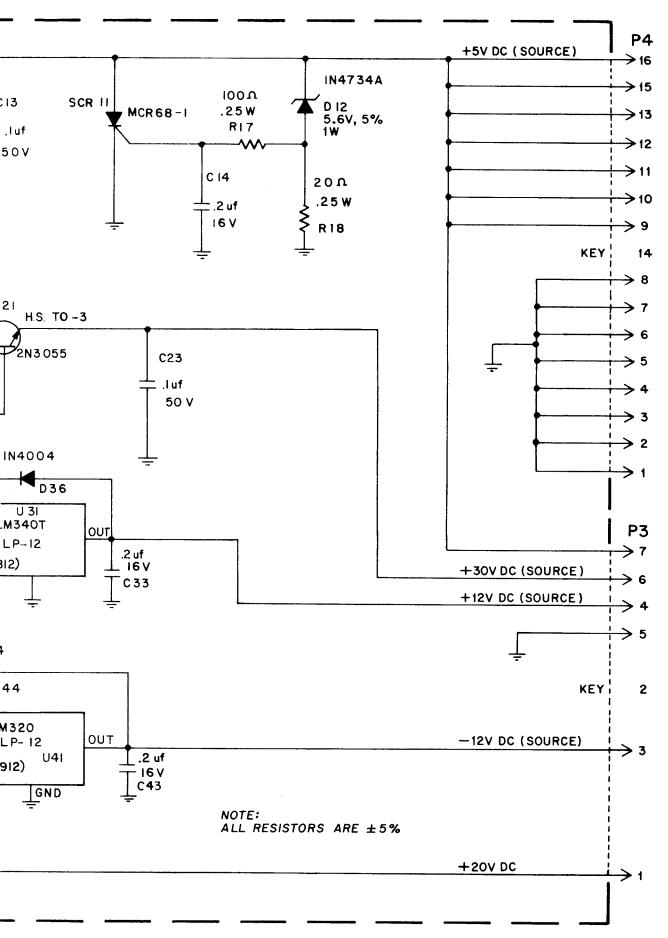
POWER SUPPLY ASSY. (A3), PARTS LIST

REFERENCE	DESCRIPTION	PART NO.	REFERENCE	DESCRIPTION	PART NO.
	Power Supply Assy.	MA-303	P2	Connector, 10 PIN	XO-531
C11, C32, C42	Capacitor, .01UF, 16V	XO-278	P3	Connector, 7 PIN	XO-526
C12	Capacitor, 100UF, 16V	XO-235	P 4	Connector, 16 PIN	XO-372
C13, C23	Capacitor, 0.1UF, 100V	XO-234	QII	Transistor, PNP, 2N5879	XO-323
C14, C33, C43	Capacitor, 0.2UF, 16V	XO-205	Q12	Transistor, NPN, TIP31B	XO-641
C21, C22	Capacitor, 2200UF, 75V	XO-132	Q21	Transistor, NPN, 2N3055	XO-301
C31, C41	Capacitor, 470UF, 35V	XO-284	R11	Resistor, 470 OHM, 5% 1/2W	XO-55
D11, D25			R12	Resistor, 100 OHM, 5% IW	XO-137
D35, D43	Diode, Light Emitting MV-5752	XO-270	R13, R14	Resistor, 12 OHM, 5% 2W	XO-138
D12	Diode, Zener, 5.6V, 5%, 1W,	XO-255	R15	Resistor, 200 OHM, 5% ¼W	XO-143
	1N4734A		R16	Resistor, 180 OHM, 5% ¼W	XO-24
D21-D24	Diode, 1N5401	XO-263	R17	Resistor, 100 OHM, 5% ¼W	XO-28
D26	Diode, Zener, 30V, 5%, 5W,	XO-273	R18	Resistor, 20 OHM, 5% ¼W	XO-29
	1N5363B		R21, R22	Resistor, 1.8KOHM, 5% 2W	XO-135
D31-D34, D36			R23	Resistor, 200 OHM, 5% 5W	XO-133
D41, D42, D44	Diode, 1N4004	XO-254	R31, R41	Resistor, 750 OHM, 5% IW	XO-136
FII	Fuse, 61/4 AMP SLO-BLO	EL-8	SCR11	Silicon Controlled Rectifier	XO-131
F21	Fuse, 1.5 AMP SLO-BLO	EL-34	UII	Diode, Programmable Zener TL431	XO-272
F31, F32	Fuse, ¼ AMP SLO-BLO	EL-5	U31	Voltage Regulator -12V, LM 340T	XO-473
F41	Fuse, 1 AMP SLO-BLO	EL-6	U41	Voltage Regulator -12V, LM 320	XO-130
PI PI	Connector, 4 PIN	PS-87	VRI	Potentiometer, 100 OHM	XO-134

X. WIRING AND SCHEMATIC DIA

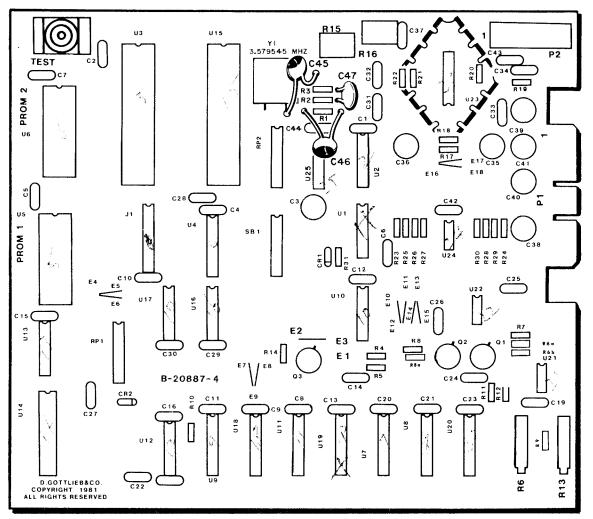


DIAGRAMS, PARTS LISTS



POWER SUPPLY ASSY. (A3), SCHEMATIC DIAGRAM

SOUND/SPEECH ASSY. (A6), COMPONENT LOCATION



SOUND/SPEECH ASSY. (A6), PARTS LIST

REFERENCE	DESCRIPTION	PART NUMBER	REFERENCE	DESCRIPTION	PART NUMBER
	Sound/Speech Assembly	MA-2 16	R 15, R 16	Potentiometer, 10K ohm	XO-109
C1, C2	Capacitor, 0. IUF, 25V	XO-248	R 18, R2 1	Resistor, 100K ohm, 5%, 1/4W	XO-45
C4, C13	,		R22	Resistor, 2K ohm, 5%, ¼W	XO- 14
C 15, C 16, C 19			R3 1	Resistor, 68K ohm, 5%, 1/4W	XO-189
C20, C21, C23			RP 1, RP2	Resistor, Dip	XO-168
C25, C26			SB 1	Switch, Dip	XO-505
C28-C30, C33			SW 1	Switch, Momentary Pushbutton	XO-5 15
C42, C44			U1	IC, 74 14	XO-397
C3 1-C32	Capacitor, .047UF, 25V	XO-222	U2	IC, SN74LS74N	XO-434
C37	Capacitor, 4.7UF, 35V	XO-291	U3	CPU, R6502-13	XO-360
C3, C38-C4 1	Capacitor, 47UF, 50V	XO-210	U4	IC, SN74LS 138N	XO-437
C 14, C24	Capacitor, 100PF	XO-223	U5, U6	EPROM, 2716	PR-53
C22	Capacitor, 300PF	XO-283	U7-U9, U11, U18	IC, SN74LS75	XO-394
C27	Capacitor, 1UF, 50V	XO-2 17	U 10	IC, SN74LS02N	XO-428
C36	Capacitor, 470UF, 35V	XO-284	U 12	IC, SN7407N	XO-384
C45, C46	Capacitor, 33PF	XO-277	U 13	IC, Inverter, SN74LS05N	XO-411
C47	Capacitor, -01 UF, 100V	XO-202	U 14	Voice Chip, SC01	XO468
CR1	Diode, 1N4 148	XO-26 1	U 15	RRIOT, R6532-18	XO-361
CR2	Diode, Zener, 1N5225B	XO-269	U 16	IC, SN74LS04N	XO-4 18
Q1, Q3	Transistor, NPN, 2N2222A	XO-320	U 17	IC, SN74LS30N	XO-432
Q2	Transistor, PNP, 2N2907A	XO-321	U 19, 20	Converter, PMI, 1408A-6P	XO-4 16
R 1, R4, R5	Resistor, 5.6K ohm, 5%, ¼W	XO-19	U2 1, U22	IC, LM74 1CP	XO-393
R11, R12			U23	IC, LM379S	XO-395
R2, R3	Resistor, 2K ohm, 5%, ¼W	XO-14	U24	IC, Dual Comparitor, LM 193	XO-396
R6, R13	Potentiometer, 10K	XO- 108	U25	Inverter, 7404	XO-402
	Resistor, 10K ohm, 5%, 1/4W	XO-18	Y1	Crystal, 3.579545MHZ	XO-456
R8, R8A, R14	Resistor, 1K ohm, 5%, ¼W	XO-5		Socket 22 Pin Dip	XO-467
R6B	Resistor, 1.8K ohm, 5%, ¼W	XO-37		Socket 24 Pin (2)	XO-529
R9	Resistor, 2.2K ohrn, 5%, 1/4W	XO-27		Socket 40 Pin (2)	XO-530

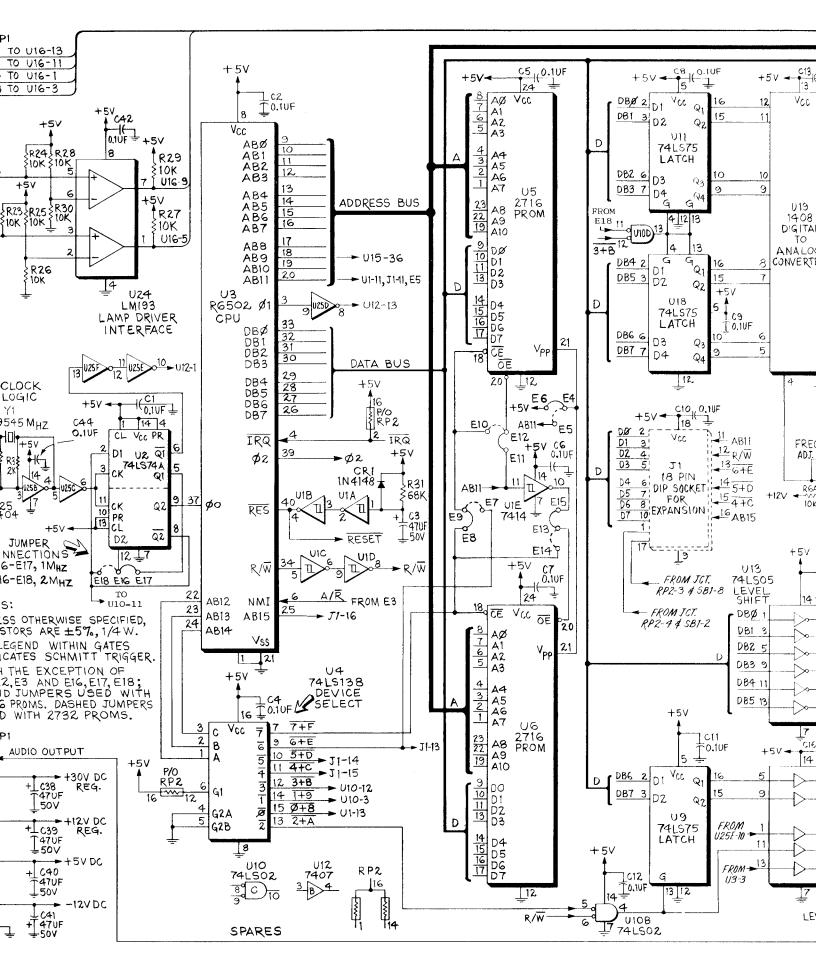


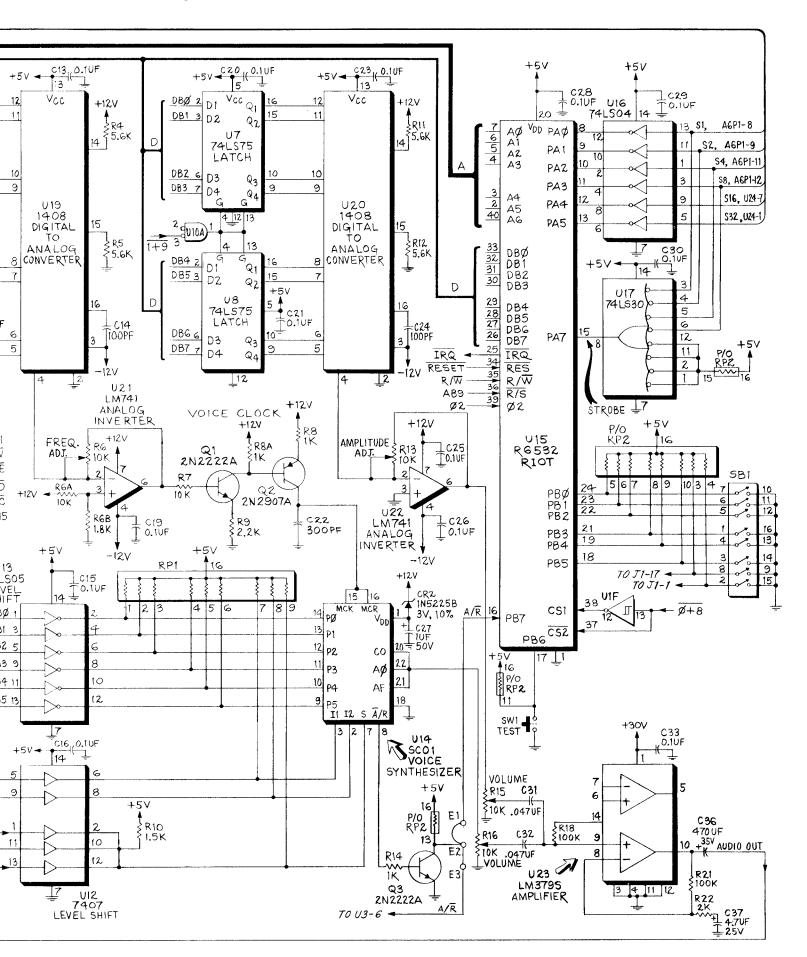
XO-20

Resistor, 1.5K ohm, 5%, 1/4W

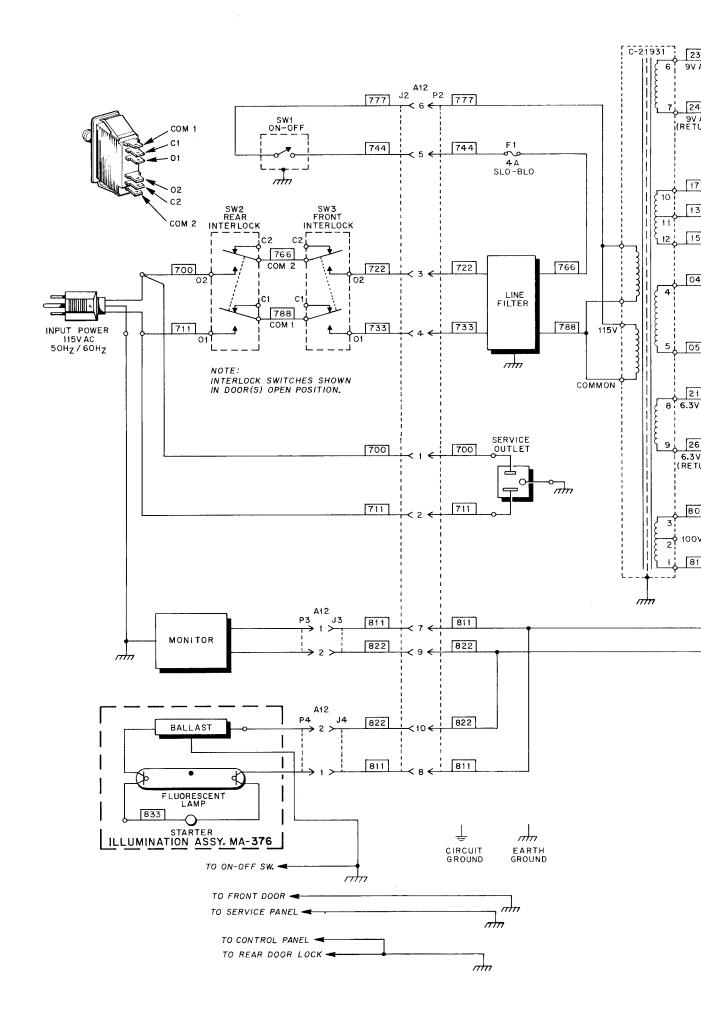
R 10

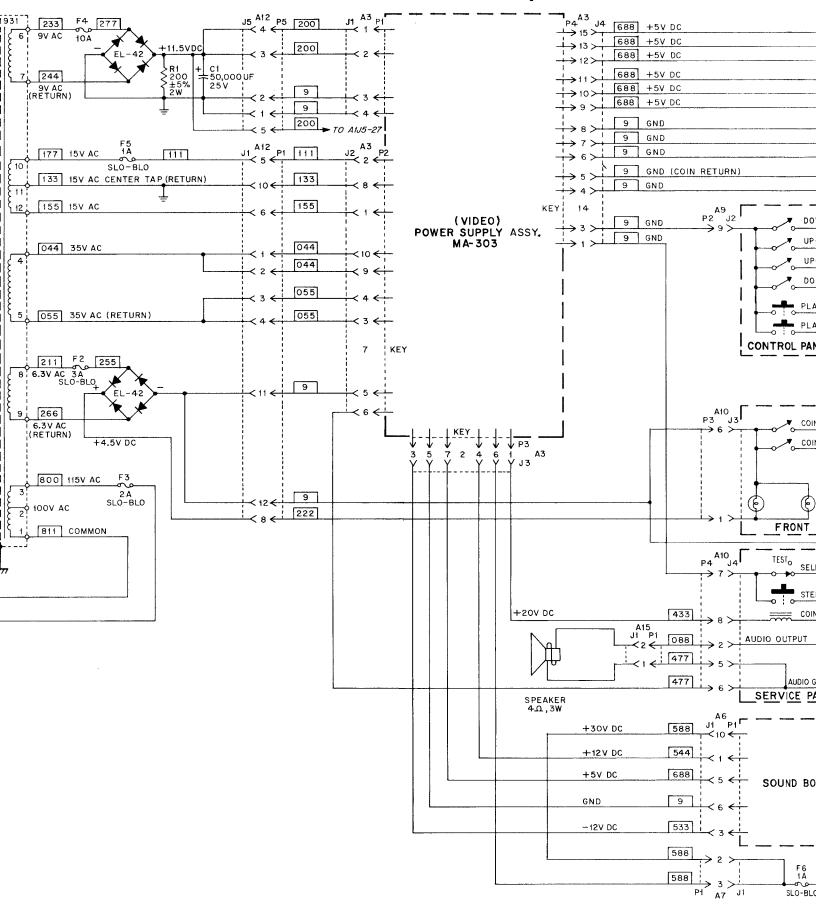
X. WIRING AND SCHEMATICE

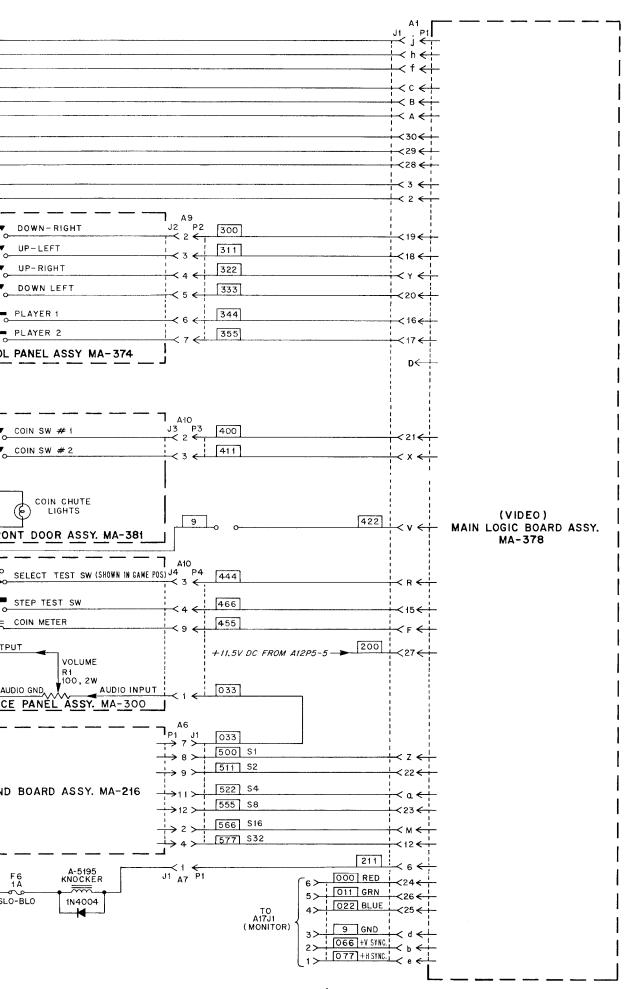




SOUND/SPEECH ASSY. (A6), SCHEMATIC DIAGRAM





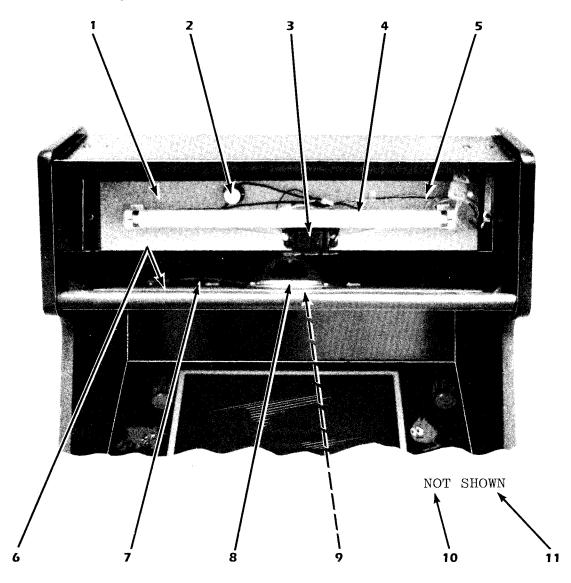


PRIMARY POWER/INTERCONNECTION DIAGRAM
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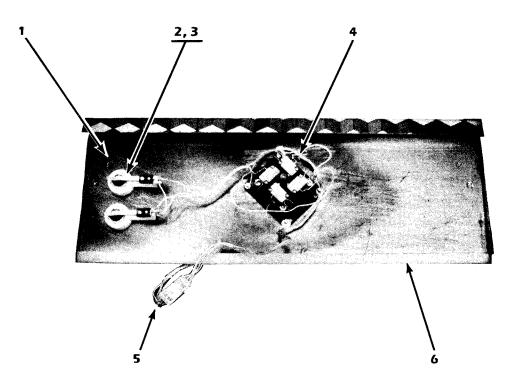
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SPEAKER/MARQUEE ASSY		. 34
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SPEAKER/MARQUEE ASSY. AND ILLUMINATION ASSY.



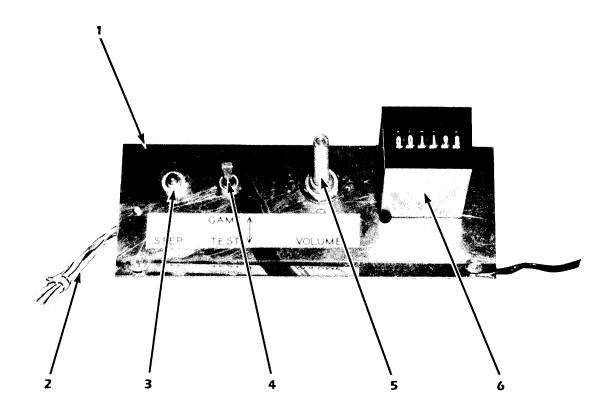
ITEM DESCRIPTION		PART NO	
1.	Illumination Assy.	MA-376	
2.	Starter	EL-69	
3.	Ballast (60 HZ)	EL-70	
4.	Lamp, Fluorescent	LA-4	
5.	Cable Assy.	MA-364	
6.	Speaker Assy.	MA-377	
7.	Cable Assy.	MA-318	
8.	Speaker	EL-83	
9.	Speaker Grill	B-20931	
10.	Marquee, Lexan (Clear)	DE-3	
11.	Lexan Overlay (Screen)	DE-2	

CONTROL PANEL ASSY.



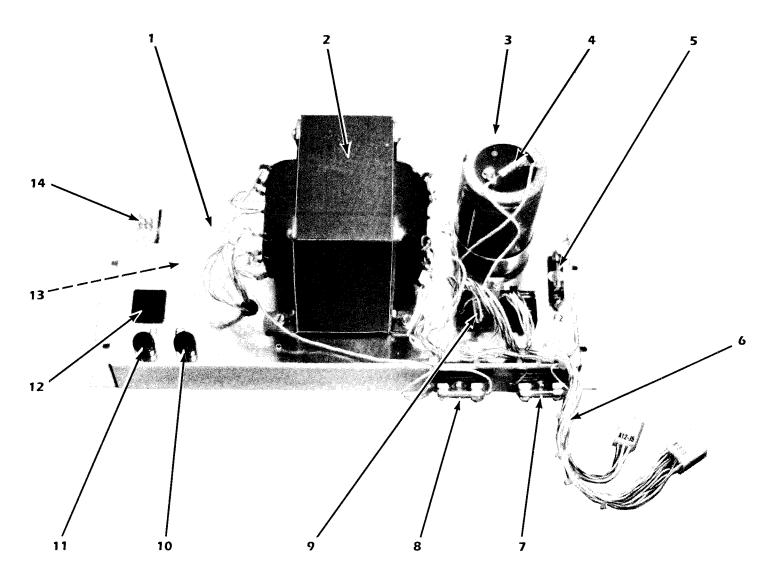
HEM	LIEM DESCRIPTION	
1.	Control Panel Assy.	MA-374
2.	Short Button (2)	A-21970
3.	Button Holder and Switch (2)	A-21971
4.	Joystick	C-22458
5.	Cable Assy.	MA-382
6.	Lexan Overlay	DE-1

SERVICE PANEL ASSY.



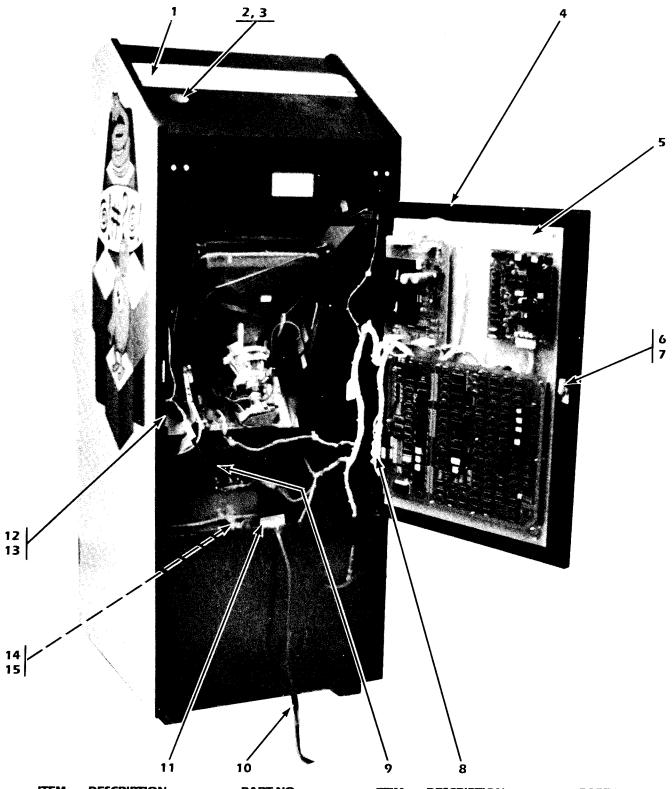
DESCRIPTION	PART NO.	
Service Panel Assy.	MA-300	
Cable Assy.	MA-316	
Switch (Push Button)	EL-57	
Switch (Toggle)	EL-85	
Volume Control	XO-104	
Coin Meter	EL-84	
	Service Panel Assy. Cable Assy. Switch (Push Button) Switch (Toggle) Volume Control	

BOTTOM PANEL ASSY.



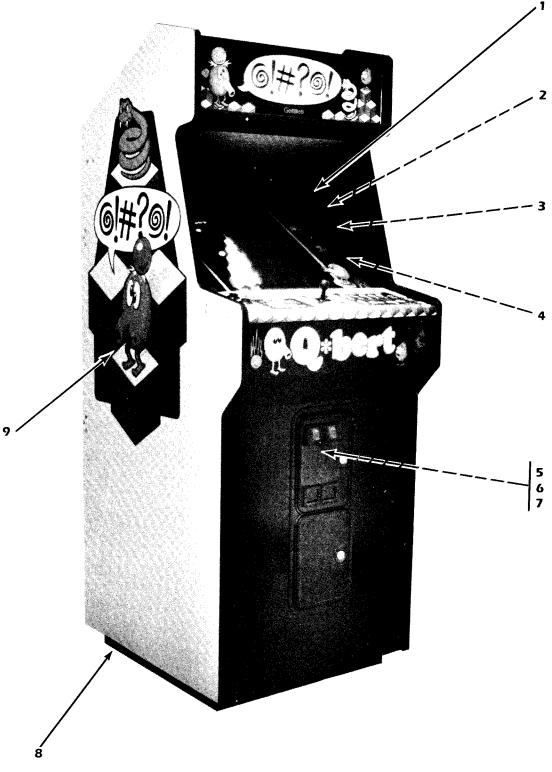
ITEM	DESCRIPTION	PART NO	
1.	Bottom Panel Assy.	MA-375	
2.	Transformer	C-21931	
3.	Capacitor, 50, 000UF, 25V	XO-141	
4.	Resistor, 200 OHM, 5%, 2W	XO-142	
5 .	Fuse, 3 AMP, SLO-BLO	EL-9	
6.	Cable Assy. (Secondary)	MA-314	
7.	Fuse, 1 AMP, SLO-BLO	EL-6	
8.	Fuse, 10 AMP	EL-23	
9.	Bridge Rectifier (2)	EL-42	
10.	Fuse, 2 AMP, SLO-BLO	EL-7	
LT.	Fuse, 4 AMP, SLO-BLO	EL-33	
12.	Service Outlet	A-18133	
13.	Line Filter	EL-50	
14.	Cable Assy. (Primary)	MA-363	

CABINET PARTS



TEM	DESCRIPTION	PART NO.	ITEM	DESCRIPTION	PART NO.
1.	Vent Channel (1)	D-21754	9.	Cable Assy. High Voltage	MA-360
2.	On-Off Switch	EL-56	10.	Line Cord	B-15357
3.	Switch Plate	A-22396	11.	Cover Plate, Line Cord	A-21955
4.	Back Door	D-21896	12.	Interlock Switch	EL-66
5.	Master Electronic Board	MA-380	13.	Cover, Interlock Switch	A-21888
6.	Rear Door Lock	MH-0	14.	Knocker Assy.	MA-384
7.	Anchor Plate, Lock	MH-1	15.	Fuse, 1 AMP, SLO-BLO	EL-6
8.	Cable Assy. Master Electronics	MA-383			

CABINET PARTS



ITEM	DESCRIPTION	PART NO.	ITEM	DESCRIPTION	PART NO.
1. 2. 3. 4. 5.	Top Glass (Screened) Monitor Filter Glass Monitor Mask Monitor Cable Assy., Front Door	SG-1 D-22465 D-22463 C-22462 MA-365	6. 7. 8. 9.	Interlock Switch Cover, Interlock Switch 3″ Leg Adjuster (4) Decal (Right) Decal (Left)	EL-66 A-21888 MH-21 DE-4 DE-4

SERVICE NOTES

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LIMITED WARRANTY

D. Gottlieb & Co. warrants to the initial purchaser of the D. Gottlieb & Co. machine that the items listed in the following schedule as installed and used in the original D. Gottlieb & Co. machine will for the applicable period set forth in the schedule, computed from the initial date of purchase from an authorized D. Gottlieb & Co. distributor, be free of defects in materials and workmanship:

SCHEDULE

GAME	ITEM	Warranty Period
Pinball	All Electronic Printed circuit boards	90 days
Pinball-Video	All Electronic Printed Circuit Boards	90 days
	Card Cage	90 days
	Television Monitor	30 days
Video	All Electronic Printed Circuit Boards	90 days
	Television Monitor	30 days

This Limited Warranty does not apply to any parts damaged in the course of handling or assembling by the customer or damage due to other than normal use or use in violation of instructions or reasonable practices, or further damaged in return shipment. This Limited Warranty is made only to the original customer, and is and shall be in lieu of all other warranties expressed or implied, and of all other obligations or liabilities on the part of D. Gottlieb & Co. and in no event shall D. Gottlieb & Co. be liable for any anticipated profits, consequential damages, loss of time, or other losses incurred by the customer in connection with the purchase or operation of D. Gottlieb & Co. machines or components thereof. The registration card with each D. Gottlieb & Co. factory-wired machine must be filled in and returned to D. Gottlieb & Co. within ten days after date of purchase for this Limited

Warranty to be effective. This Limited Warranty applies only to machines so registered.

THIS LIMITED WARRANTY IS IN LIEU OF ANY OTHER EXPRESS OR IMPLIED WARRANTIES, INCLUDING ANY IMPLIED WARRANTY OF MERCHANTABILITY OR FITNESS, AND OF ANY OTHER OBLIGATION ON THE PART OF THE SELLER AND D. GOTTLIEB & CO.













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