

**OTES:**

- ALL RESISTORS,  $\frac{1}{4}$  WATT  
UNLESS OTHERWISE SPECIFIED.
- ALL CAPACITORS, MFD.  
UNLESS OTHERWISE SPECIFIED.
- FOR LOGIC SPARES, SEE  
CHART IN SHEET #2 OF 2.

A) FROM SHEET #2 OF 2.  
 B) TO SHEET #2 OF 2.

The schematic diagram illustrates the power input section. It shows the connection of a power source (5V, +5V, +12V) through various regulators (U1-A, U1-B, U1-C, U1-D, U1-E, U1-F, U1-G, U1-H) to provide regulated voltages (+5V, -5V, +12V, GND). The diagram also includes bypass capacitors (C85, C86) connected between the power source and ground.

英語 IJG-1  
国語 IJG-2

A circuit diagram showing a ground connection. A horizontal line labeled "GND" at its center connects two points. The top point is connected to a component labeled "2J3 - 3". The bottom point is connected to a component labeled "2J3 + 4".

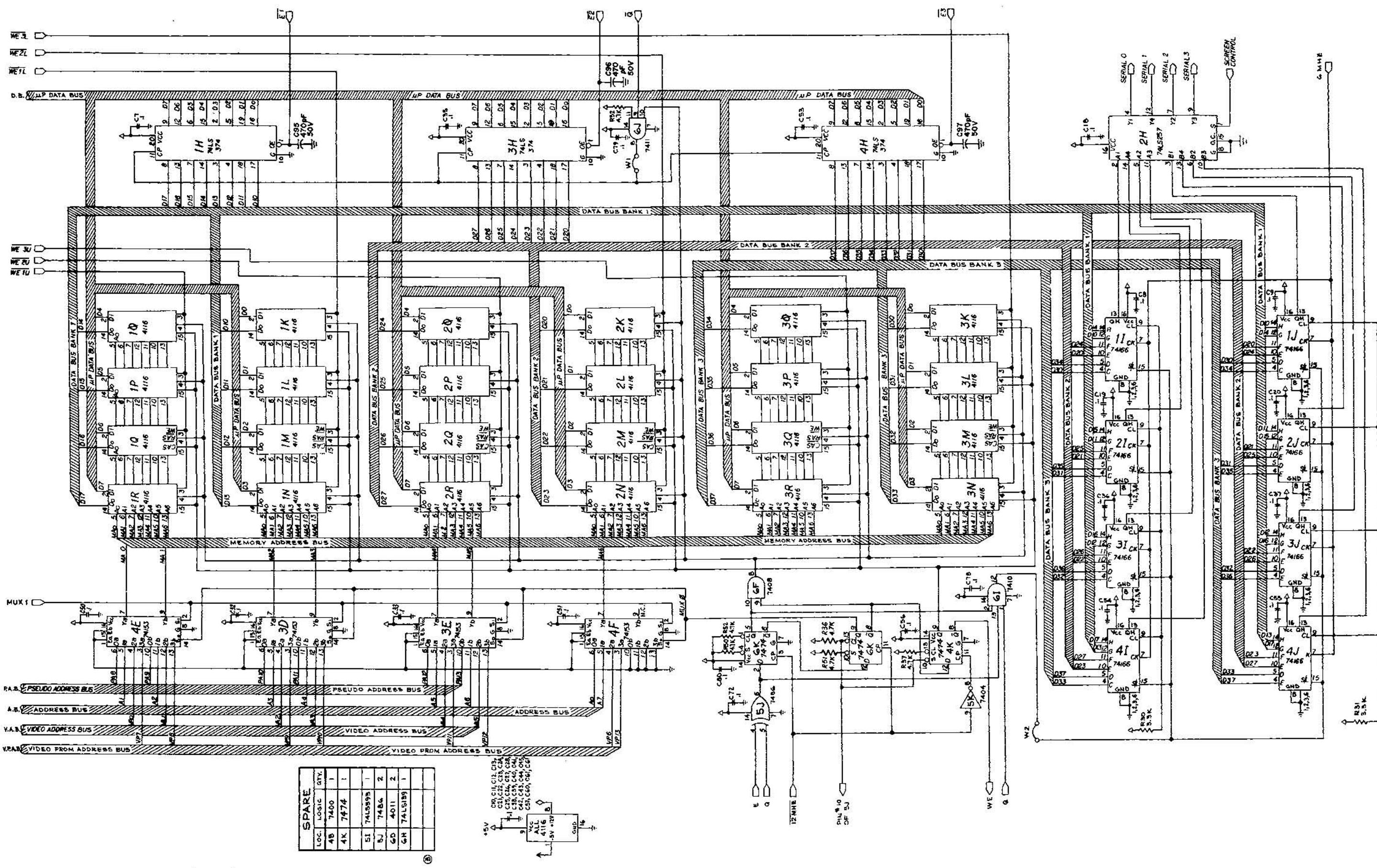
CONTROLS

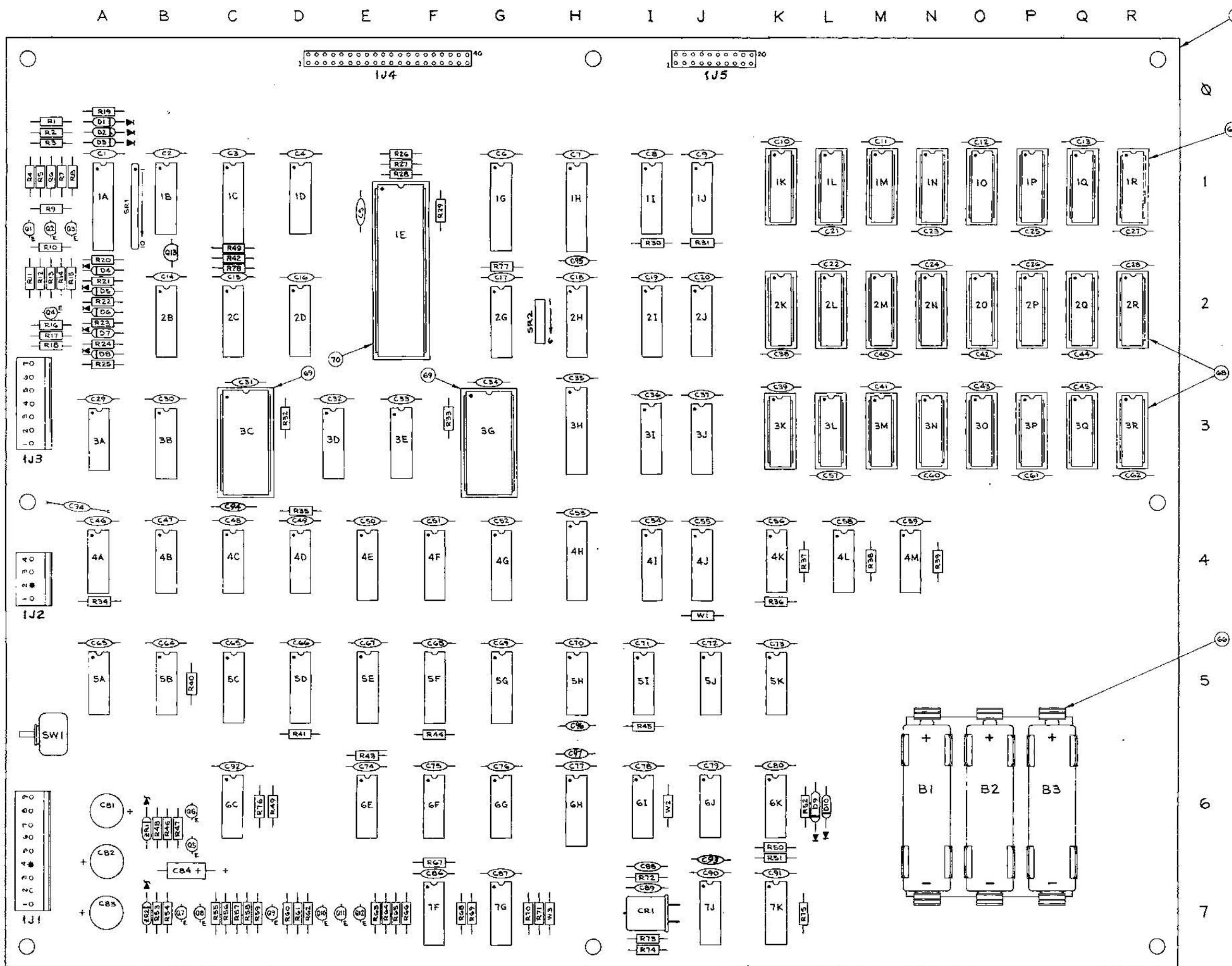
4A

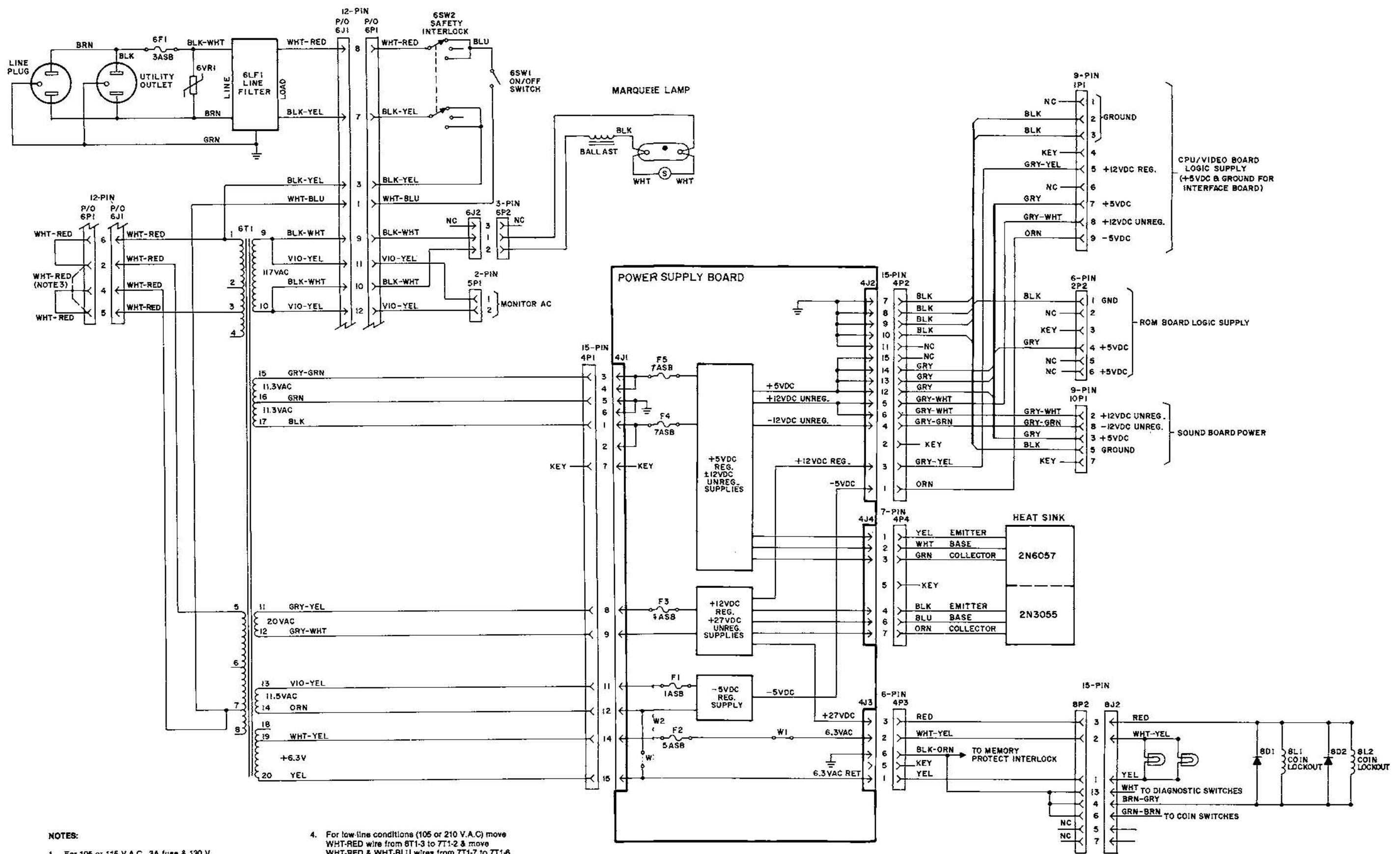
ALL  
7486 VERT.  
SYNC.

W1

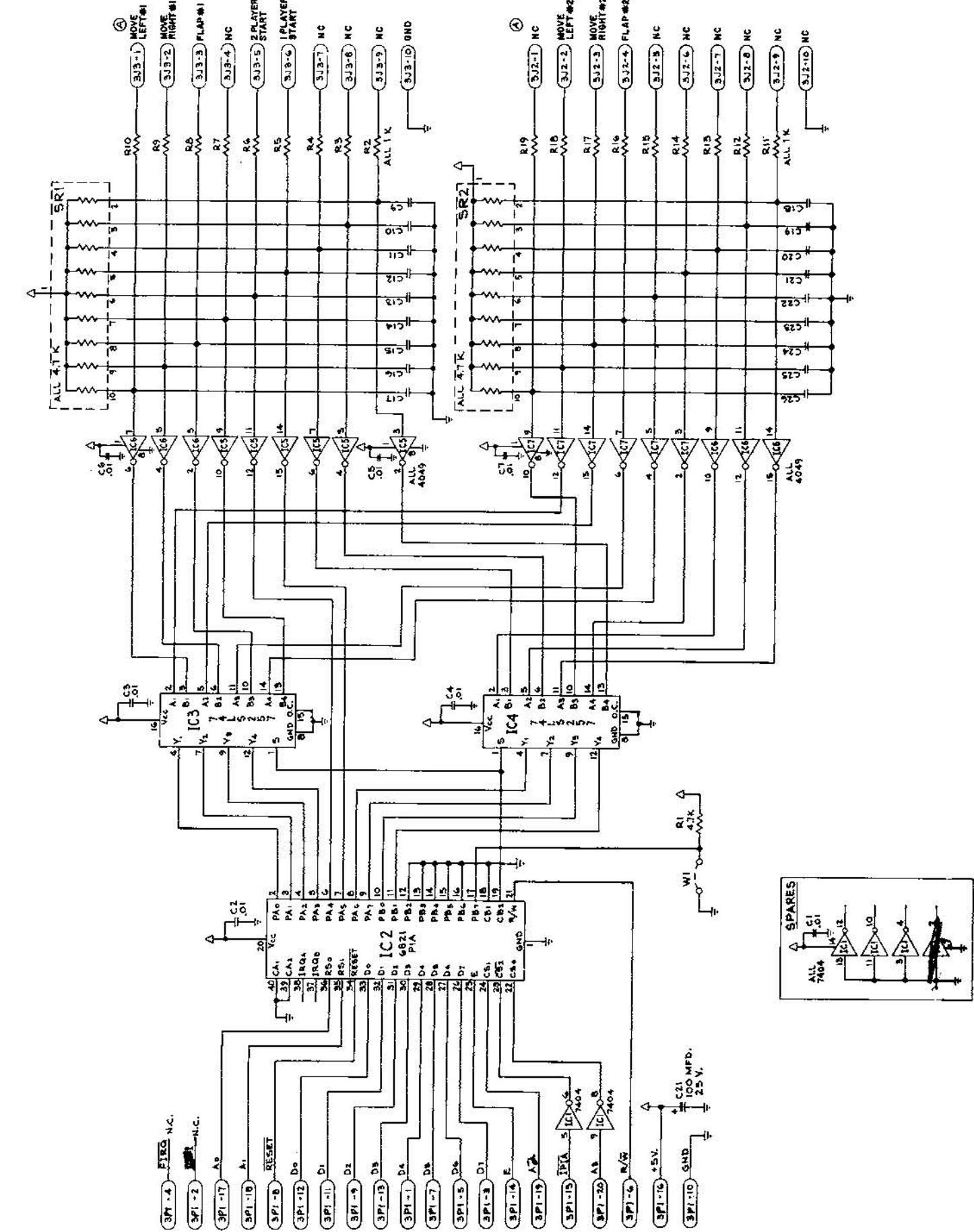
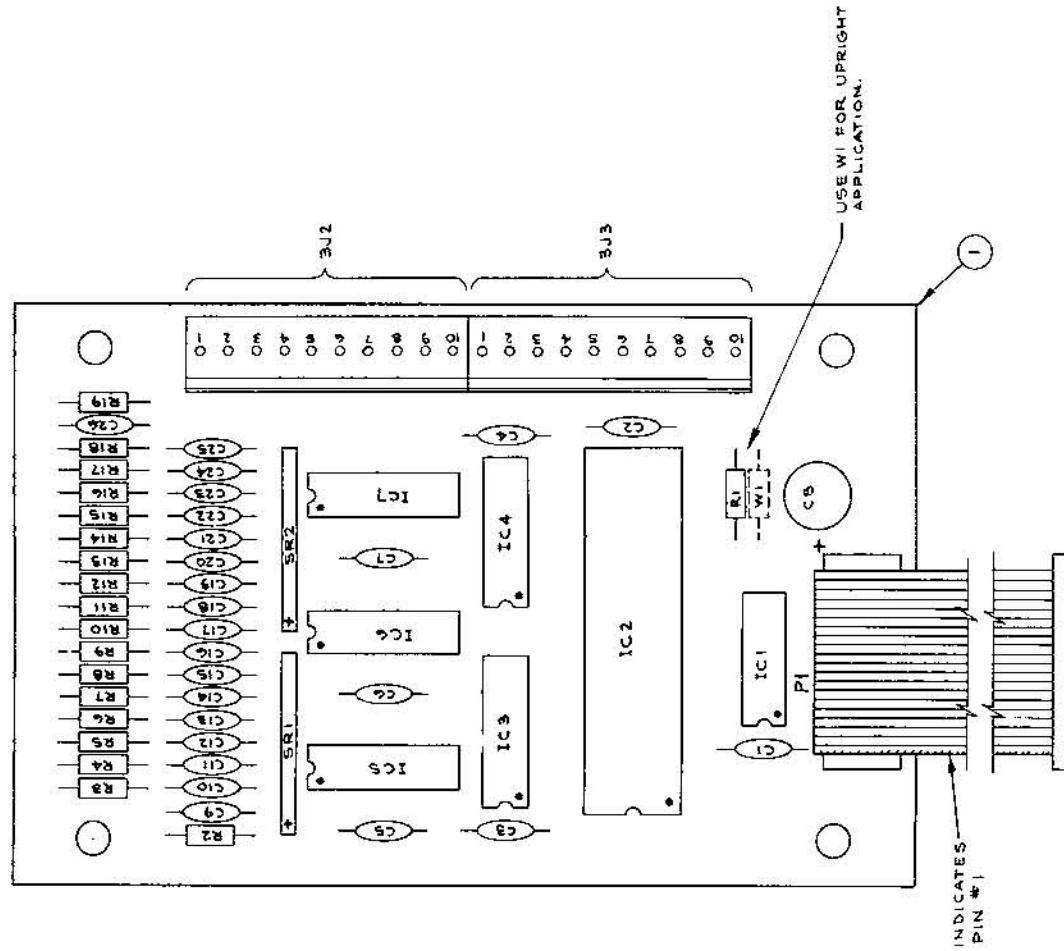
C58, C59, C72,  
C79,

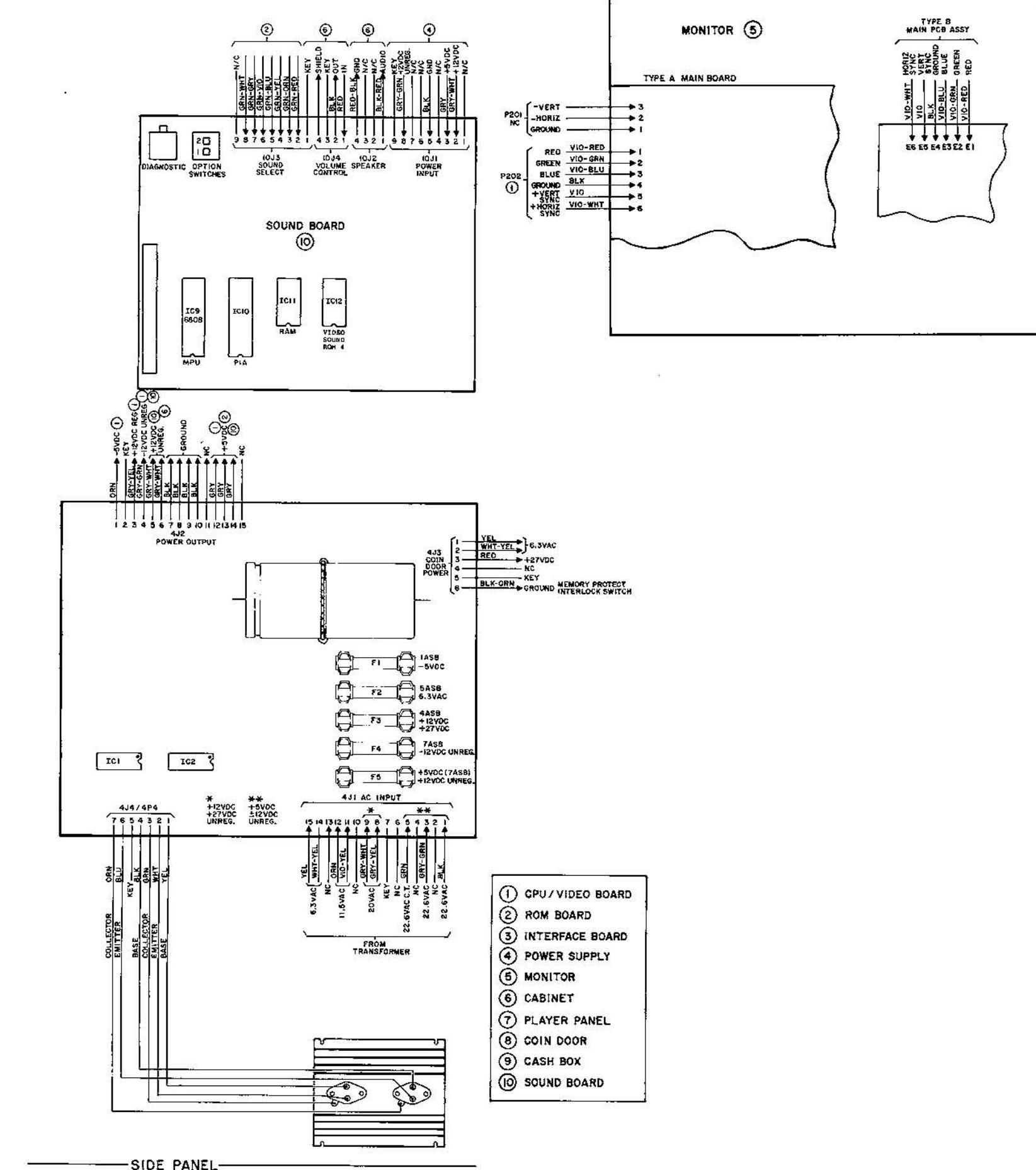
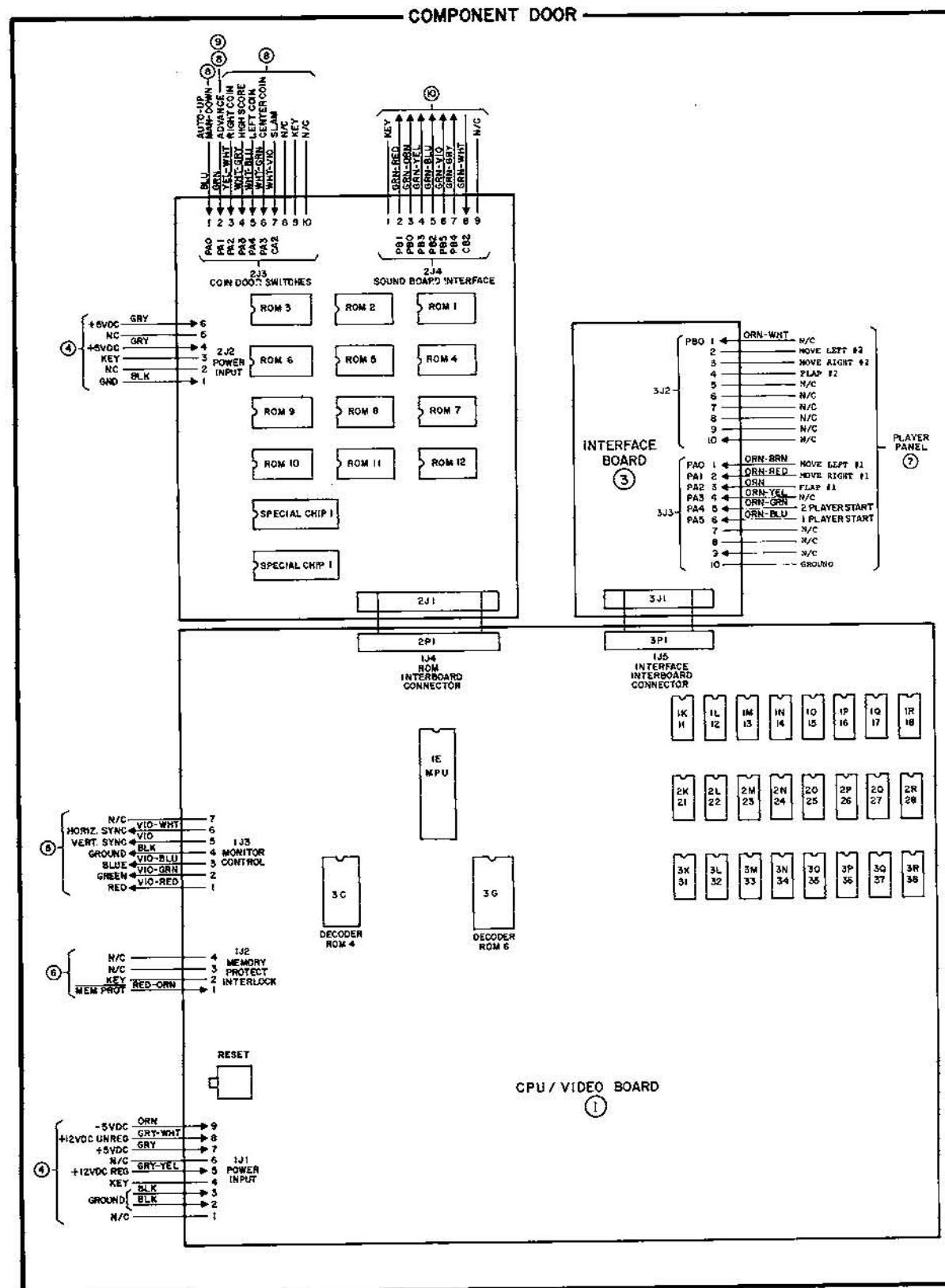


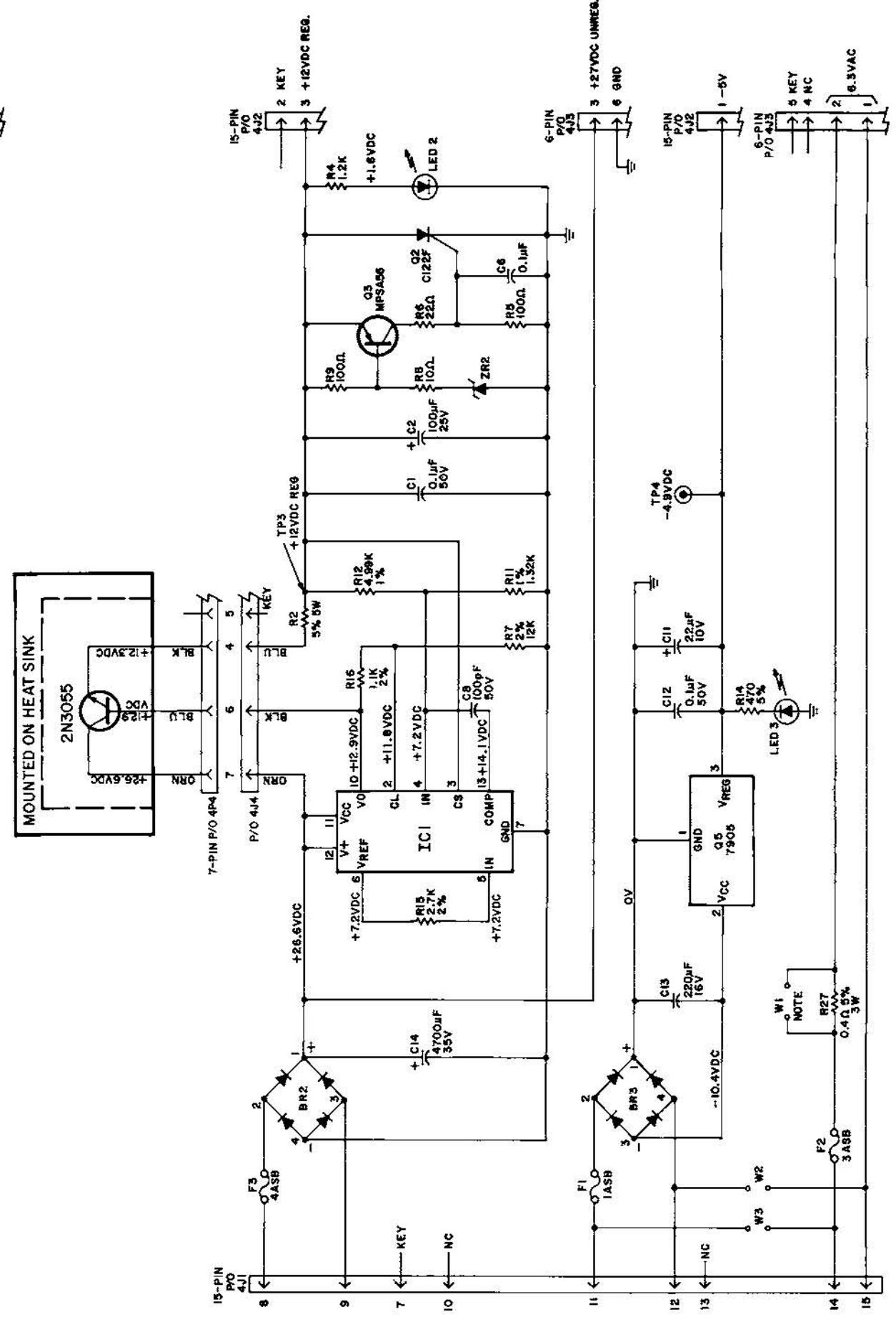
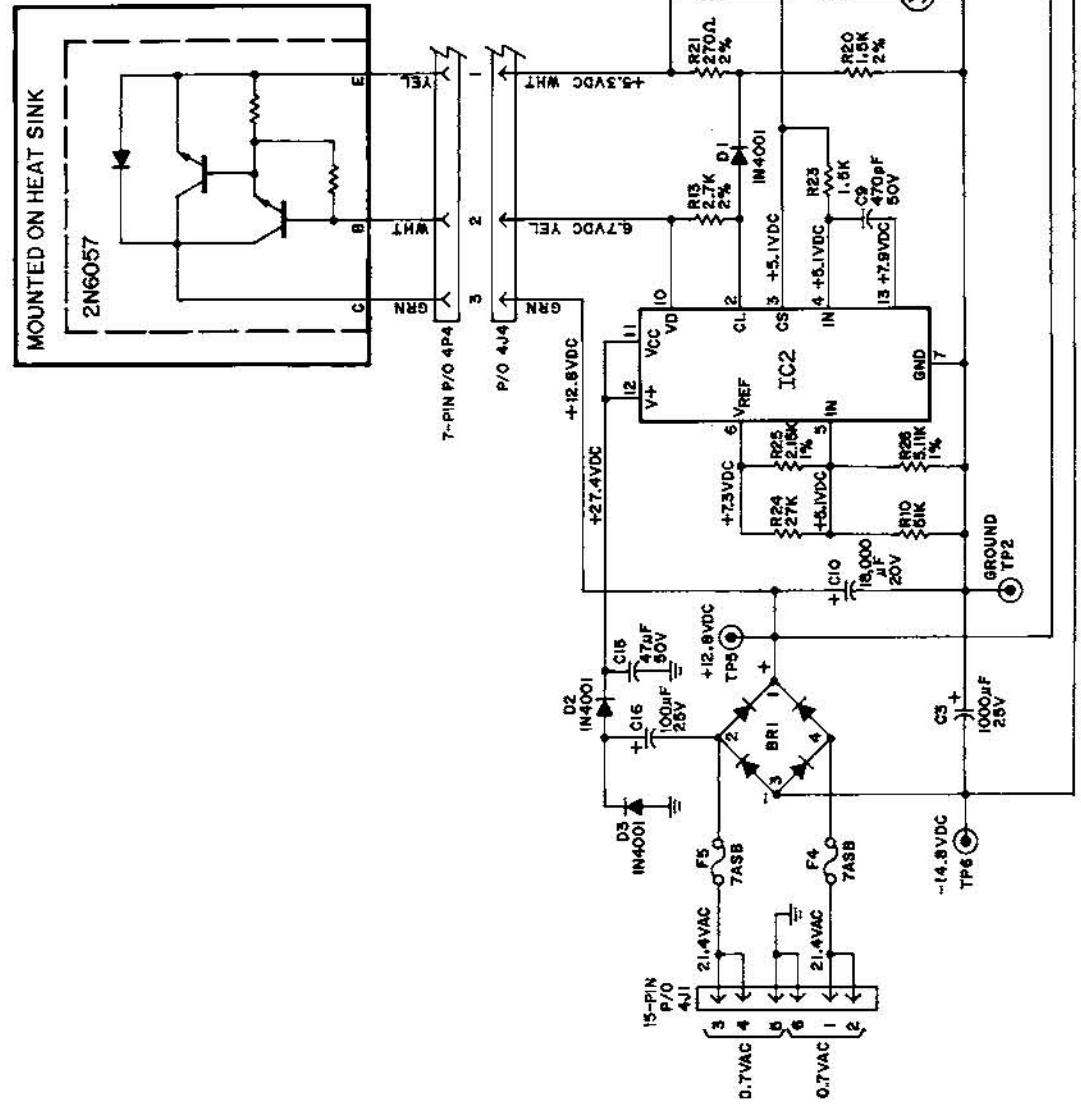


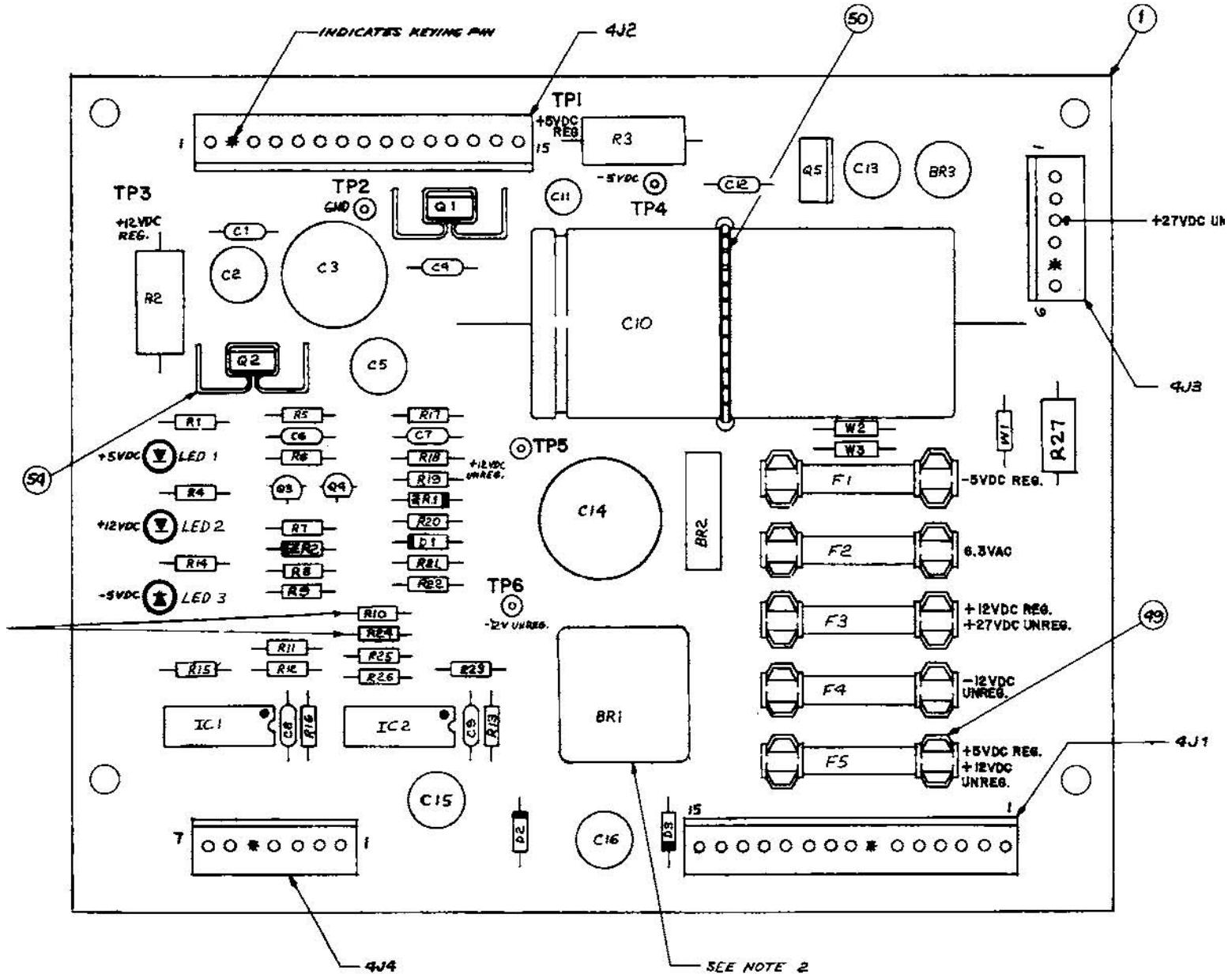


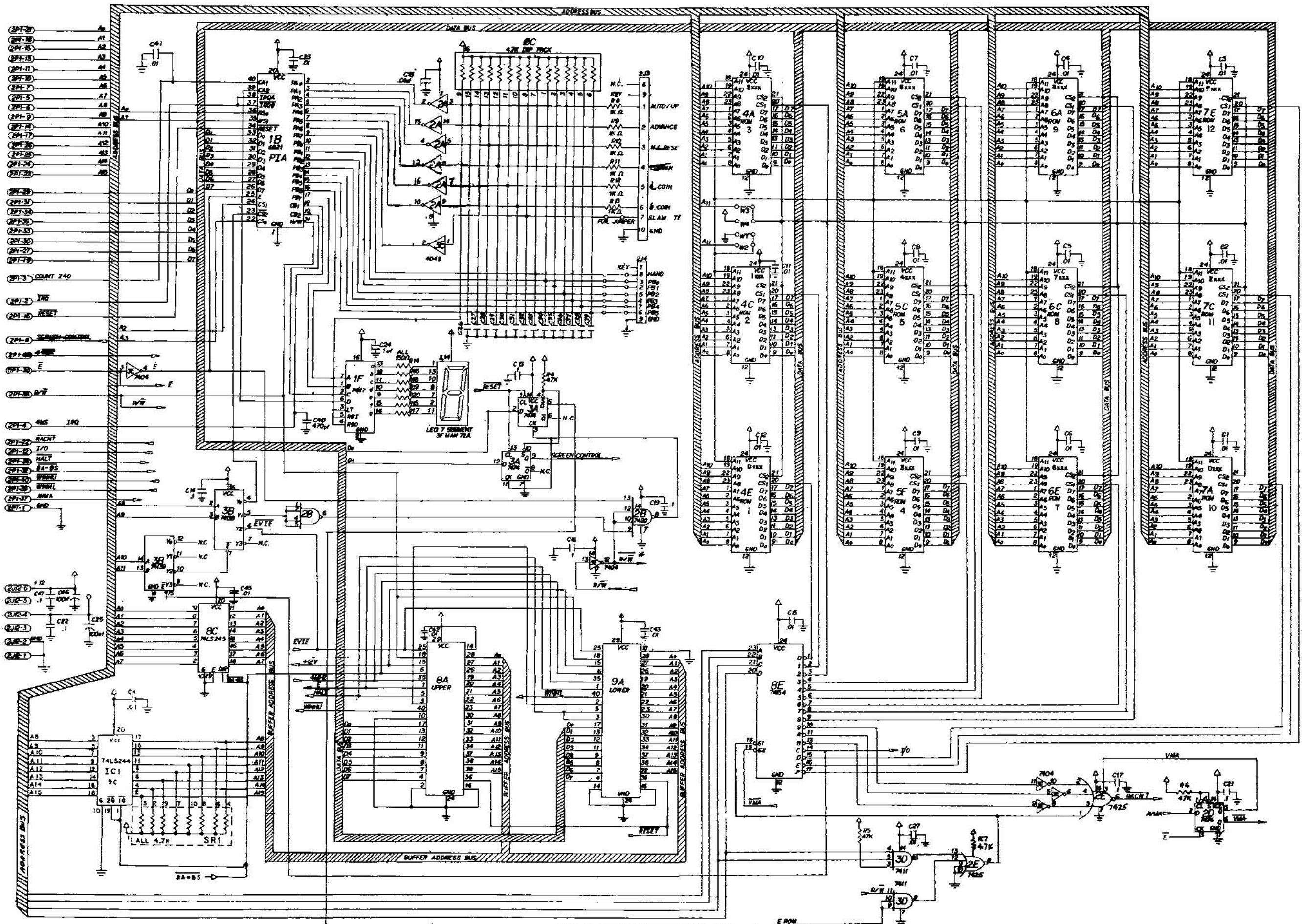
4	5220~ 09013-00	IC1	7404 HEX INVERTER	1
5	5281~ 09015-00	IC3, IC4	7415, 257 QUAD DATA MULTIPLEXER	2
6	5019~ 09062-00	SR1, SR2	4.7K OHM 9 RES., 10 PIN SIP	2
7	5010~ 09348-00	RETHRU R19	1K OHM 5%	16
8	5010~ 08991-00	R1	RESISTOR, C.F. 4.7K OHM 5% 1/4 WATT	1
9	5010~ 09354-00	W1	RESISTOR, C.R. 0 OHM 1/4 WATT	1
10	5043~ 09044-00	C1, T1, H1, C24	CAPACITOR, CERAMIC DISC 470 PF. SOV. XTR ±20%	16
11	5040~ 08942-00	C8	CAPACITOR, ELECT. RADIAL 100 MFD. 25 VOLT, +50-10%	1
12	5043~ 08980-00	C1, THRU C7	CAPACITOR, CERAMIC DISC, .01 MFD. 50 V. +BO-20%	7
13	5195~ 09440-00	P1	20 PIN RIBBON CABLE ASSY.	1
14	5281~ 09444-00	3 J2, 8J3	10 PIN HEADER 09-45-1101	2











D-9144 ROM Board

