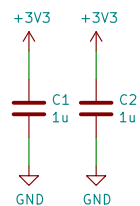
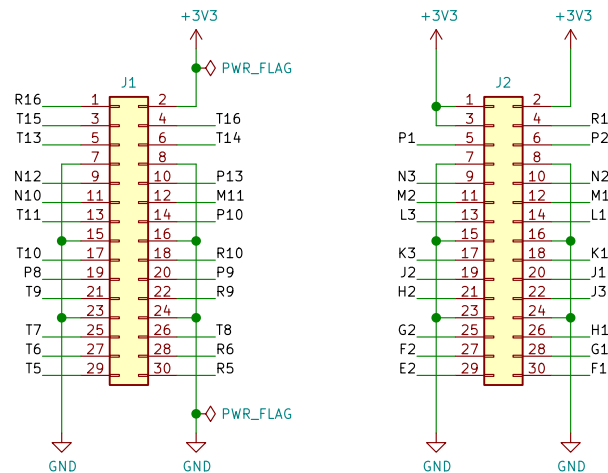
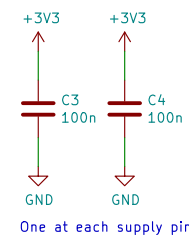
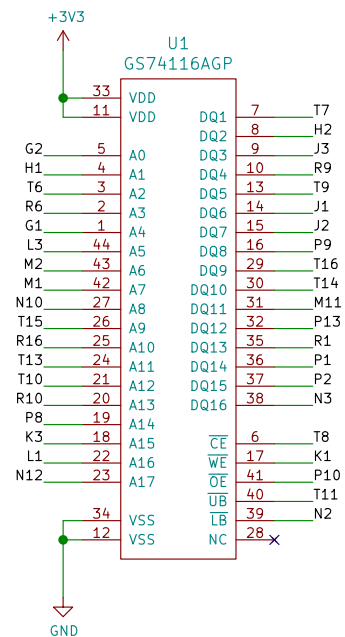


Names are ball IDs on the BGA.
Should make it easier to write PCF.
(actual mapping to SRAM pins is arbitrary,
to ease routing on 2-layer board.)

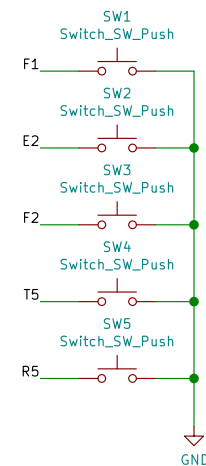


"Bulk"
place near to pin headers



One at each supply pin

5 spare IOs
use them for pushbuttons
(since none on dev board)



Luke Wren

Sheet: /

File: sram_dev_board.sch

Title: SRAM Breakout for HX8k-EVN

Size: A4

Date: 2019-04-04

Rev: A

KiCad E.D.A. kicad 5.0.2-bee76a070ubuntu18.04.1

Id: 1/1