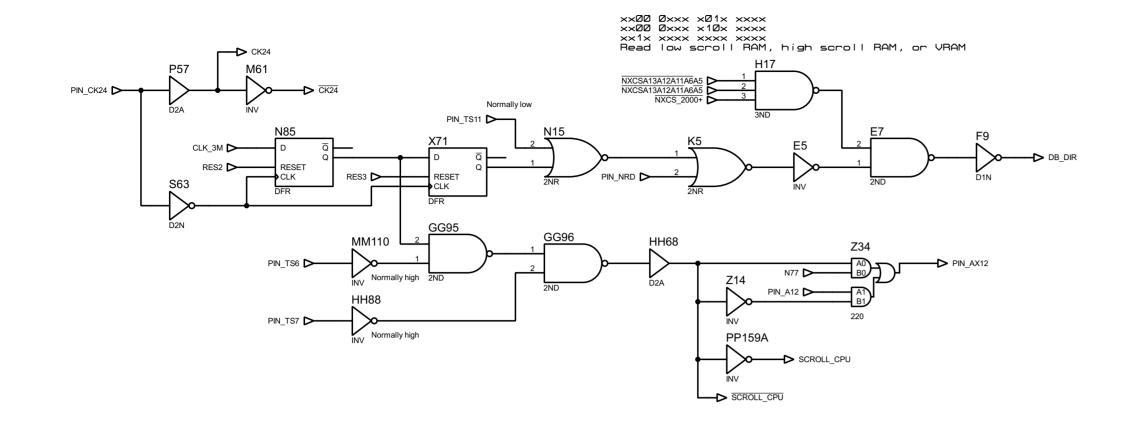
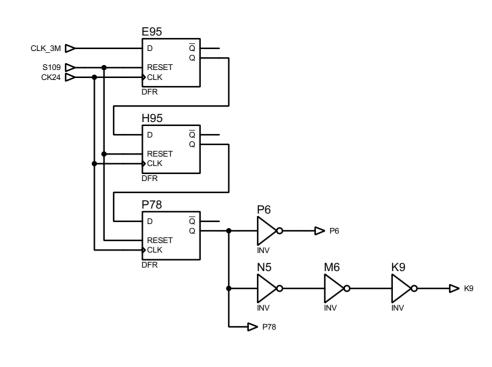
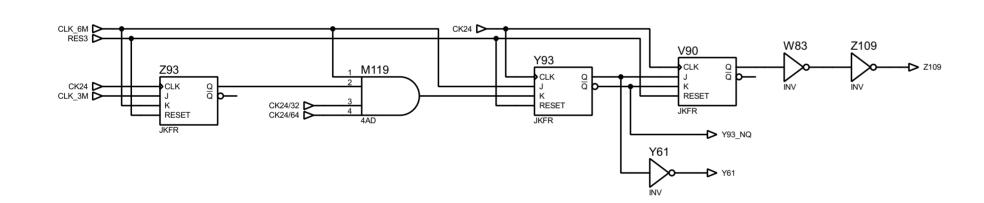
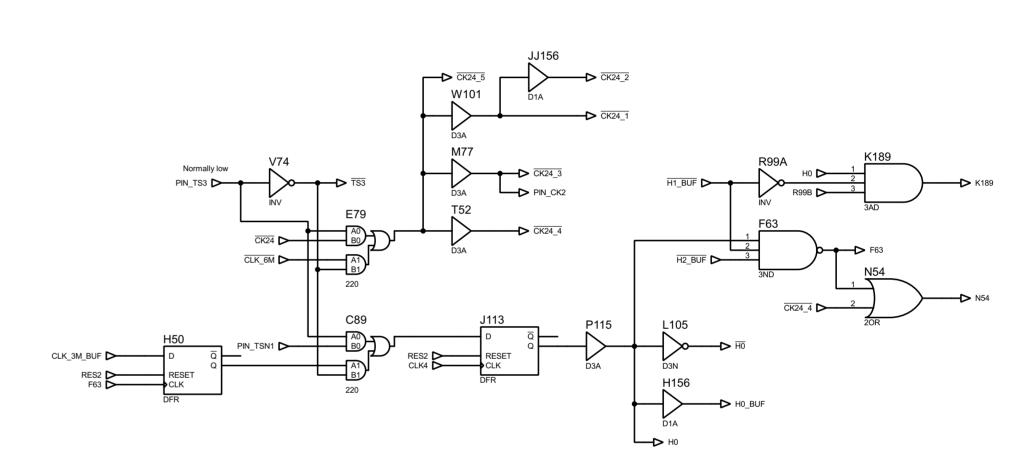


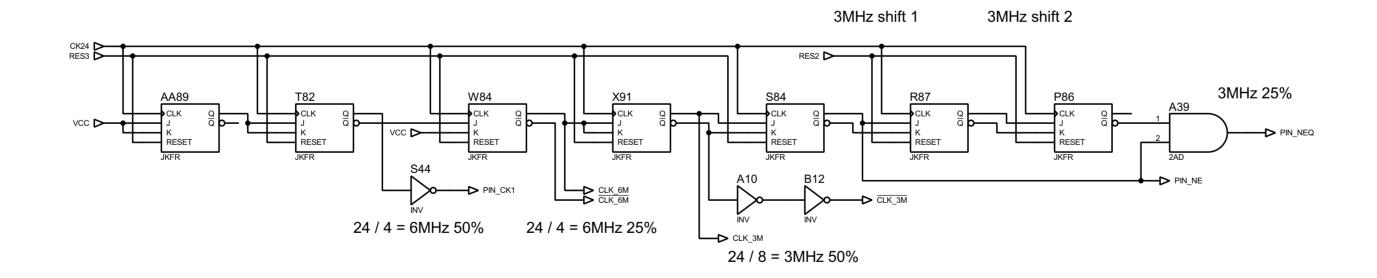
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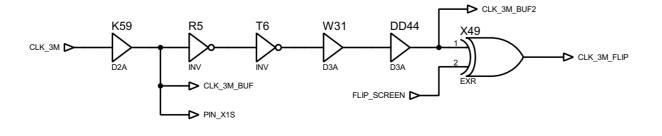








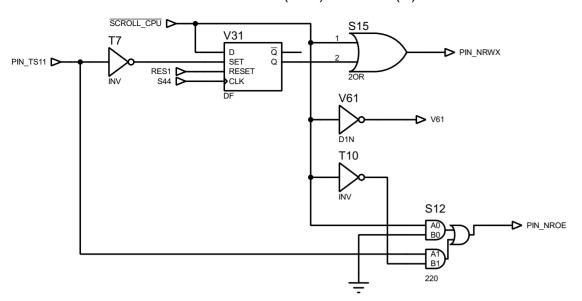


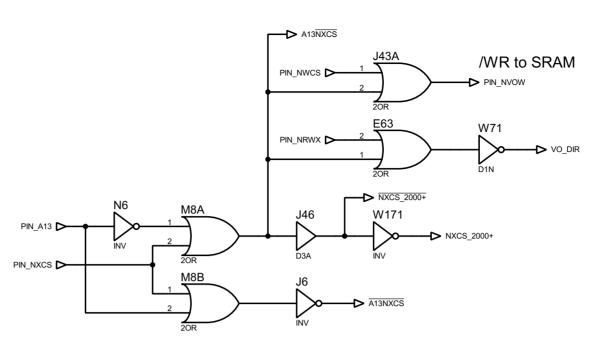


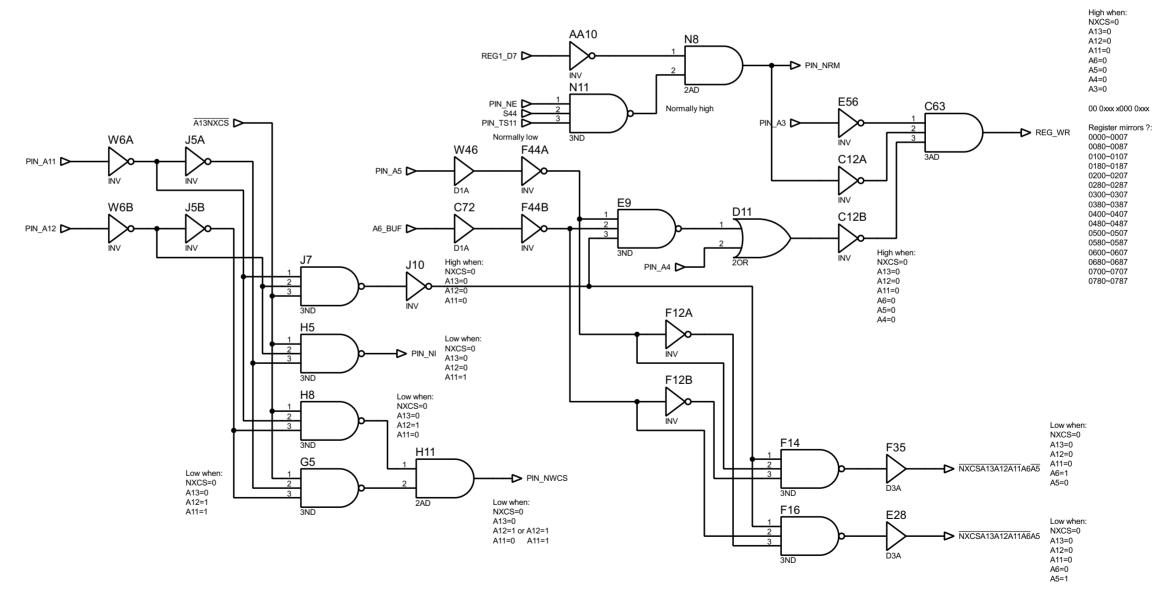
FILE NAME:	007121.pdsprj			DA	TE:		Π
DESIGN TITLE:	Konami 007121			14	1/05/	2021	
	CLOCK DIV				GE: of	61	
BY: Sean Gor	nsalves	REV:	0				

VRAM access:

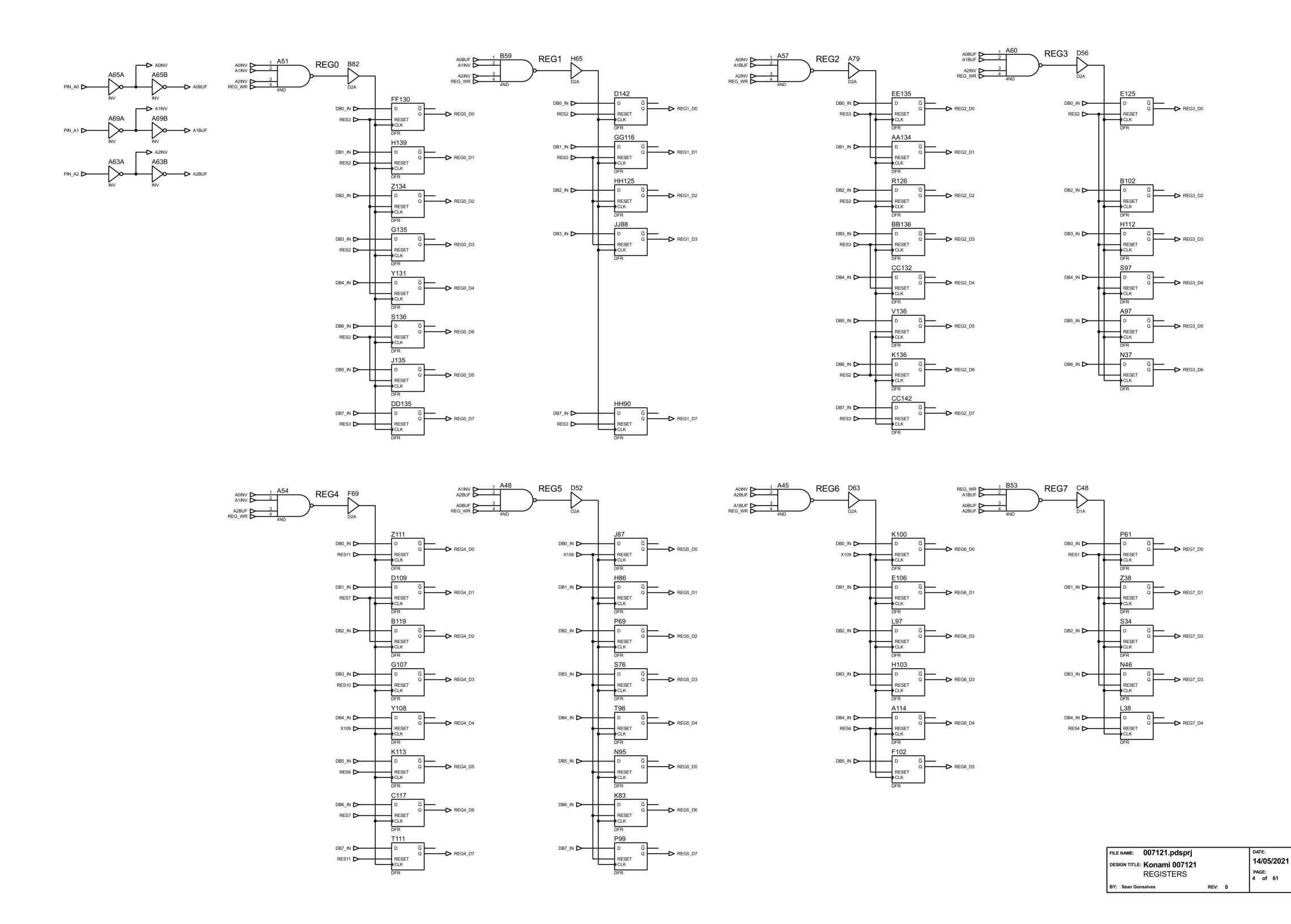
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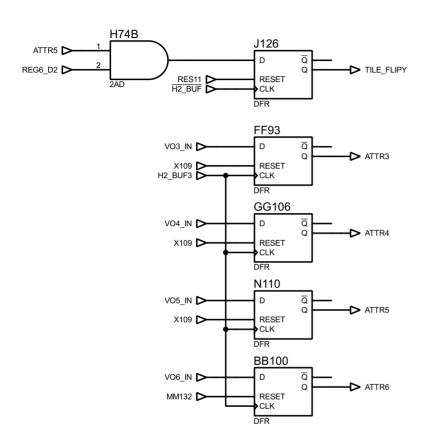


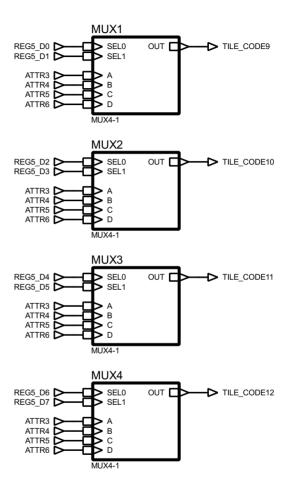




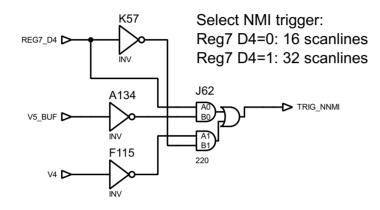
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DESIGN TITLE: Konami 007	7121	14/05/2021
ADDRESS	DECODING	PAGE: 3 of 61
BY: Sean Gonsalves	REV: 0	

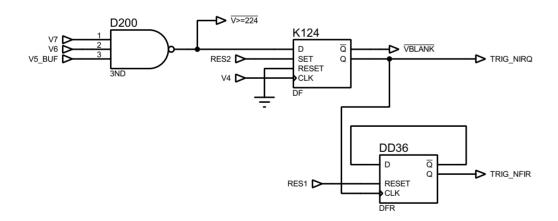


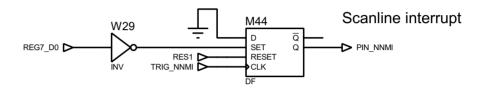


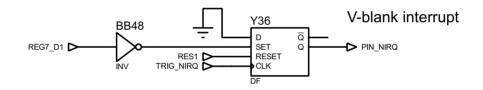


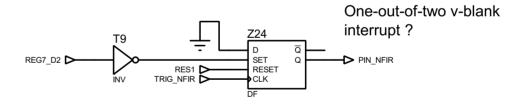


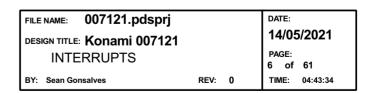


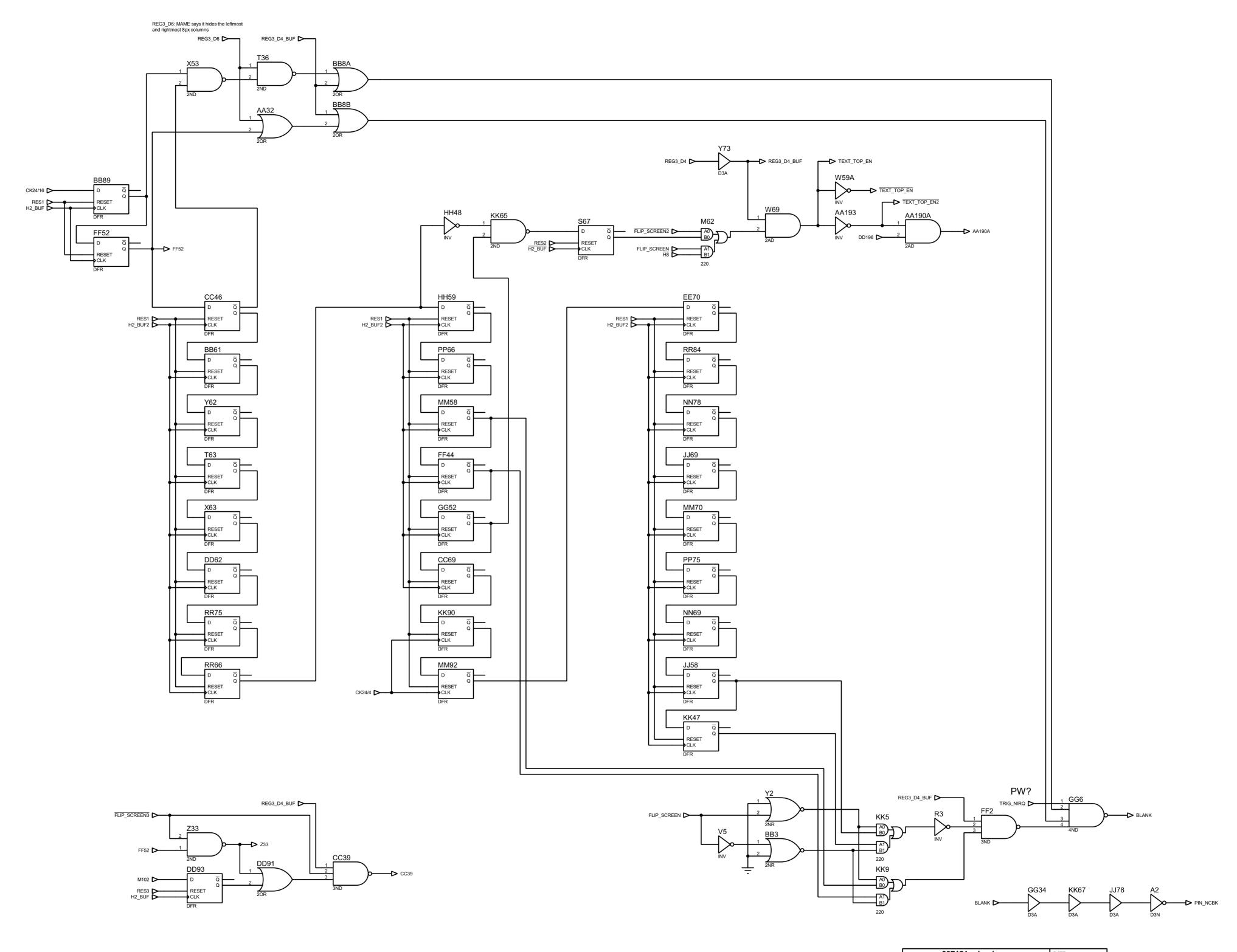




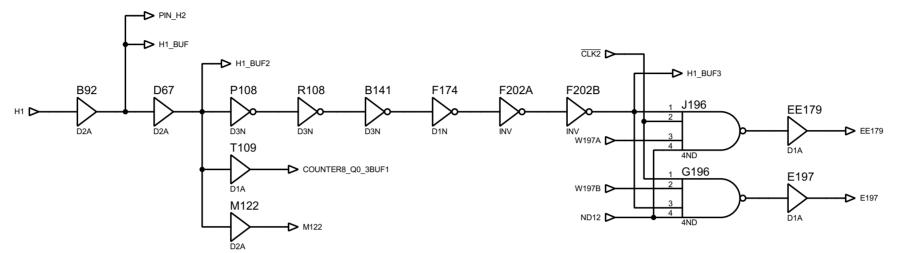




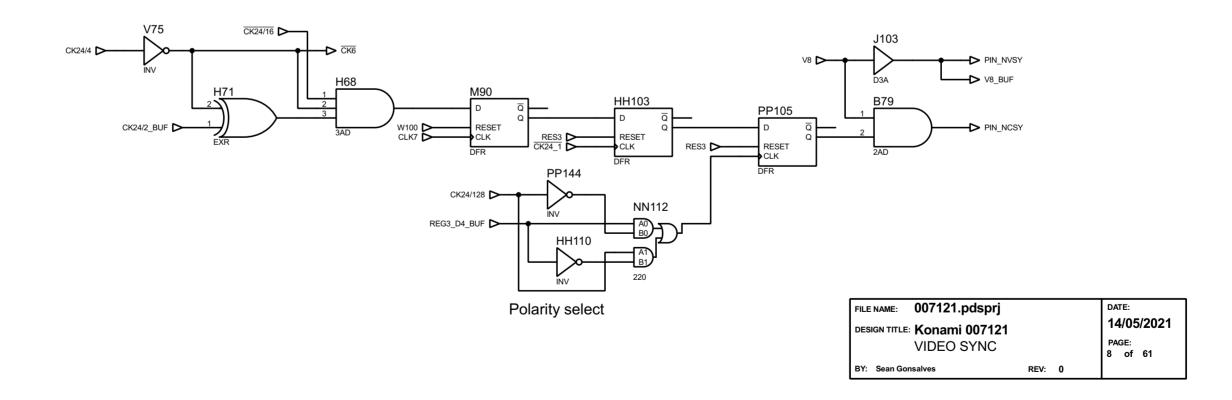


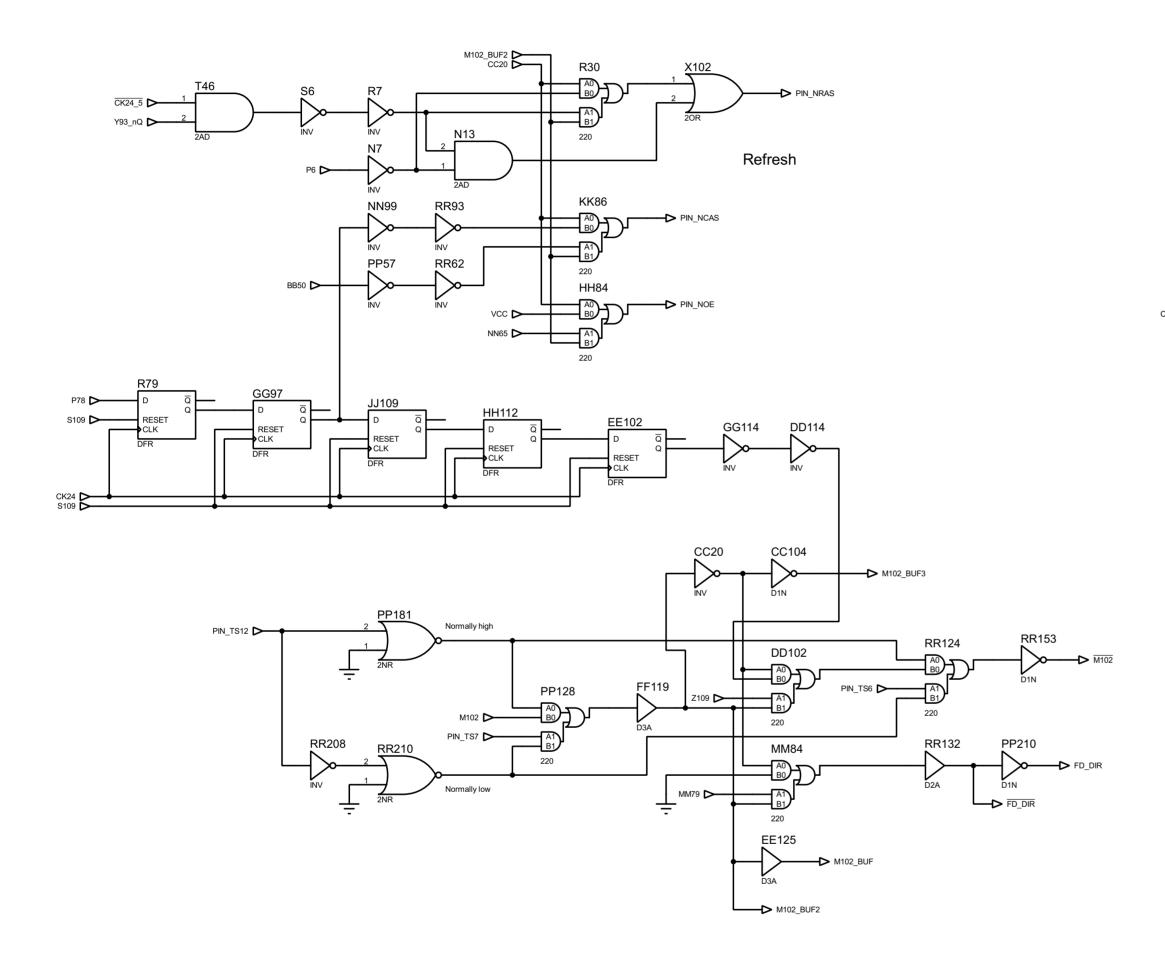


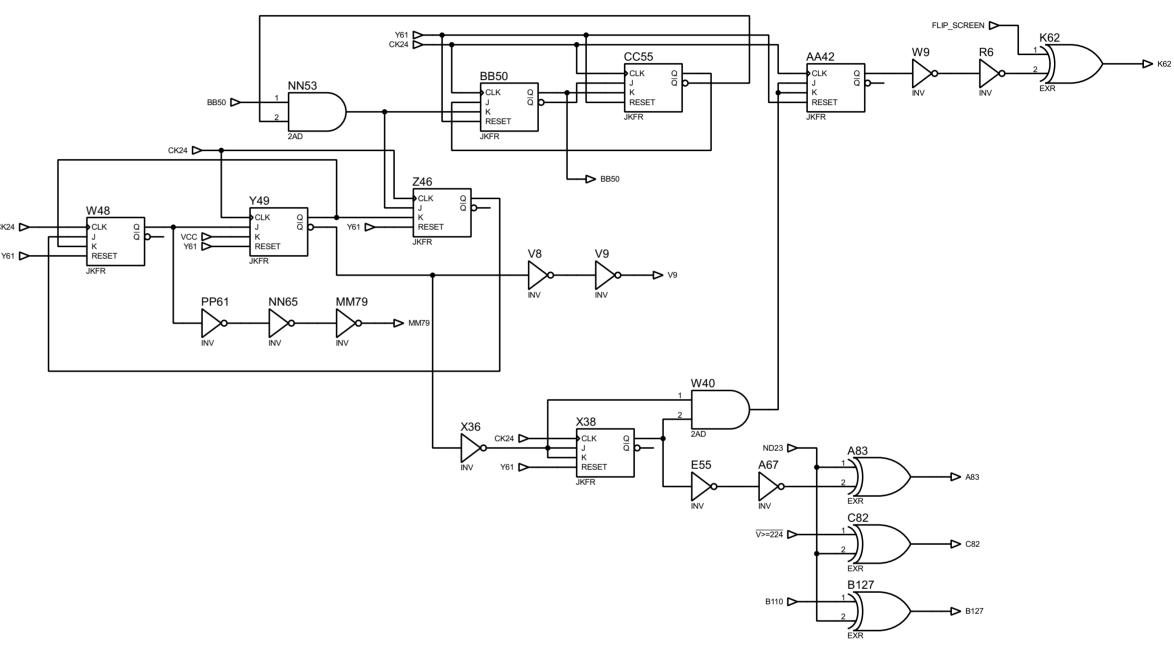
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DESIGN TITLE: Konami 007121			14/05/2021
BLANKING			PAGE: 7 of 61
BY: Sean Gonsalves	REV:	0	TIME: 04:43:34

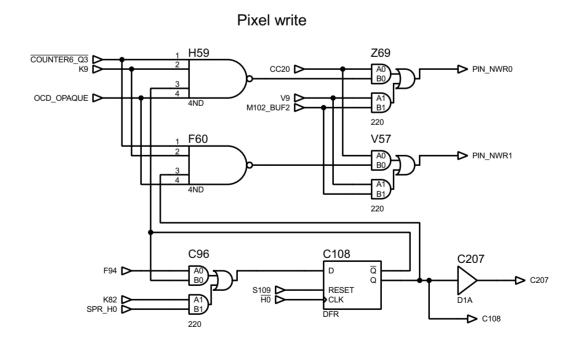


T109 or M112 must be an inverter?

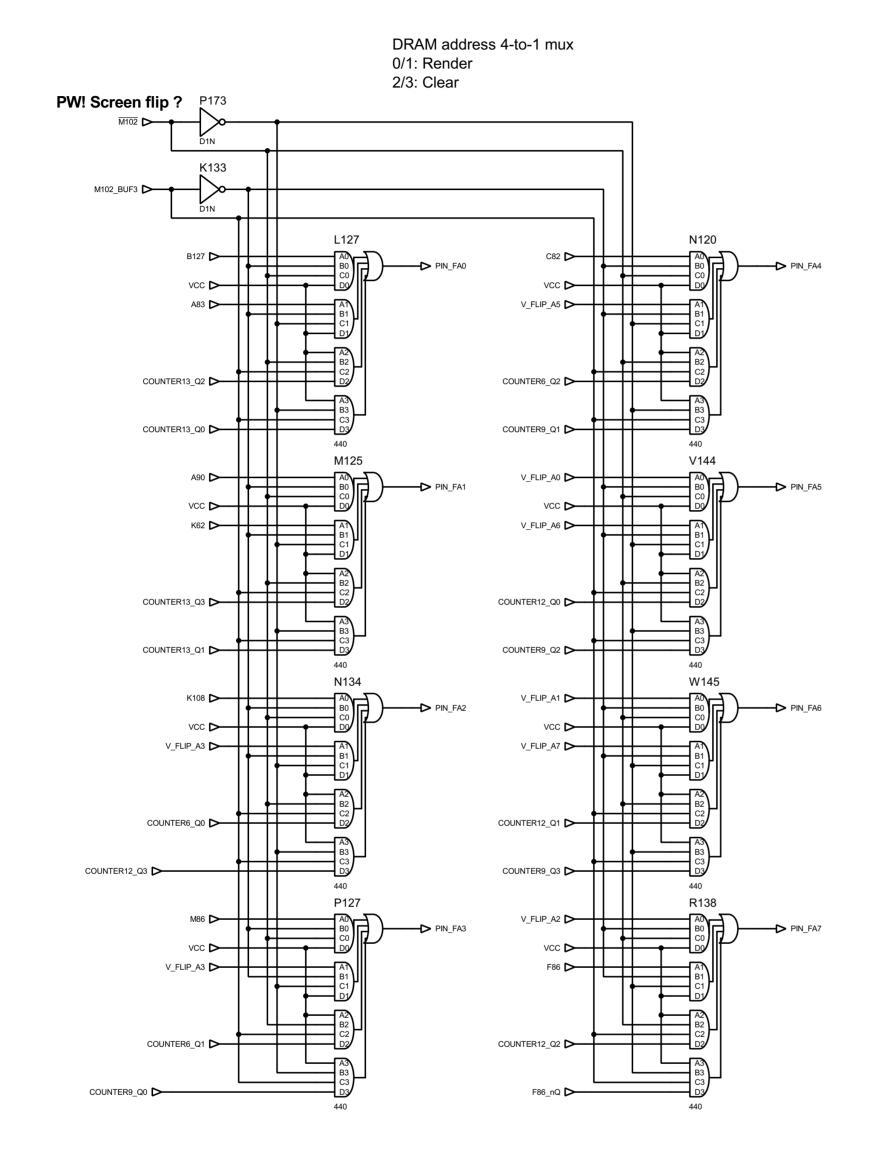


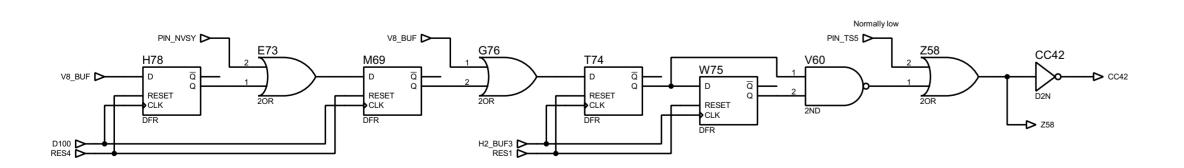


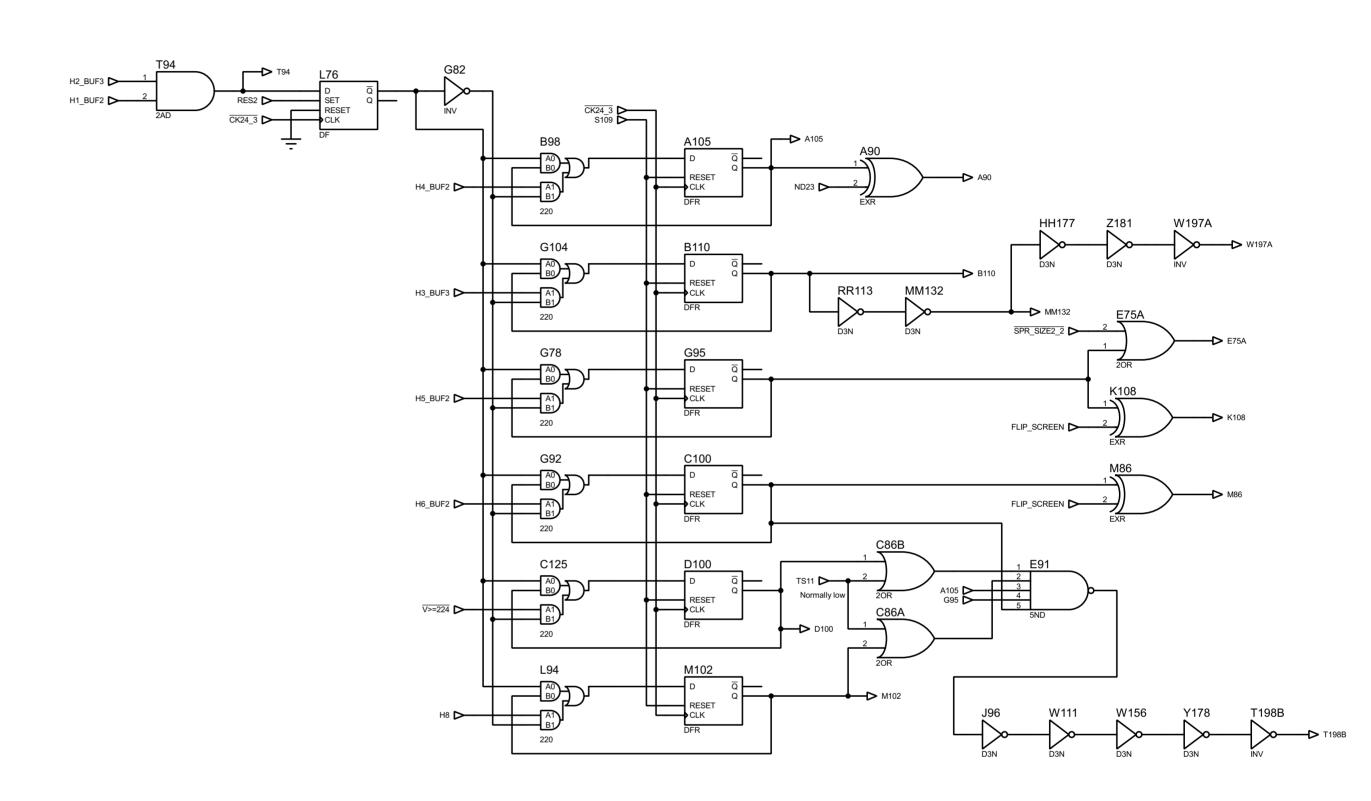


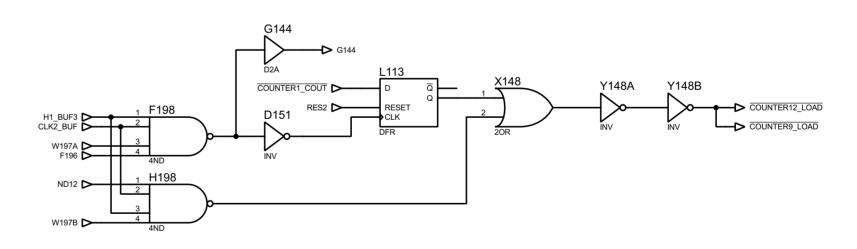


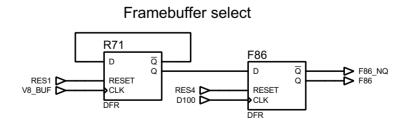
FILE NAME: 007121.pdsprj		DATE:	
DESIGN TITLE: Konami 007121		14/05/2021	
DRAM CONTROL		PAGE: 9 of 61	
BY: Sean Gonsalves R	EV: 0	0. 0.	



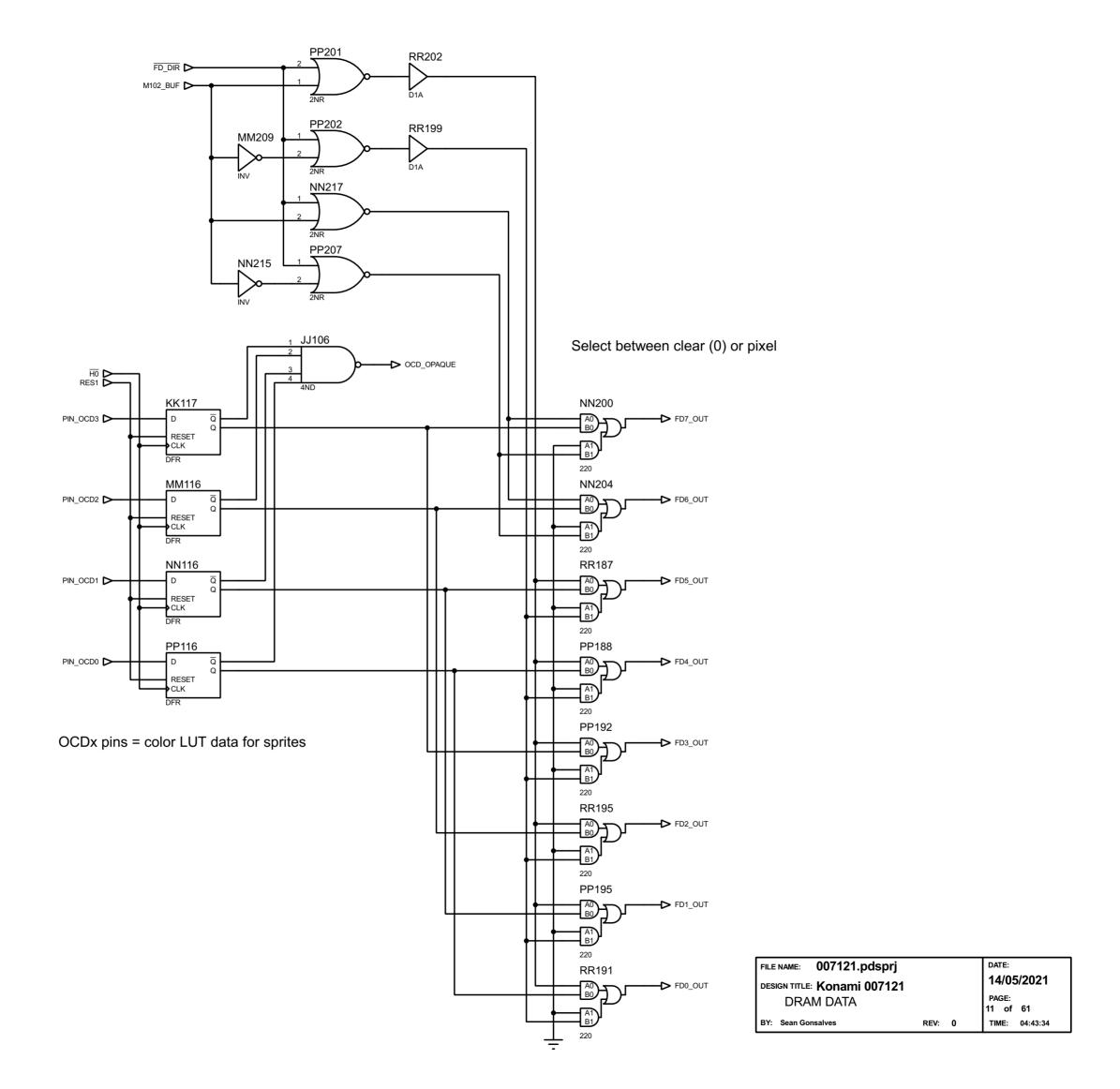


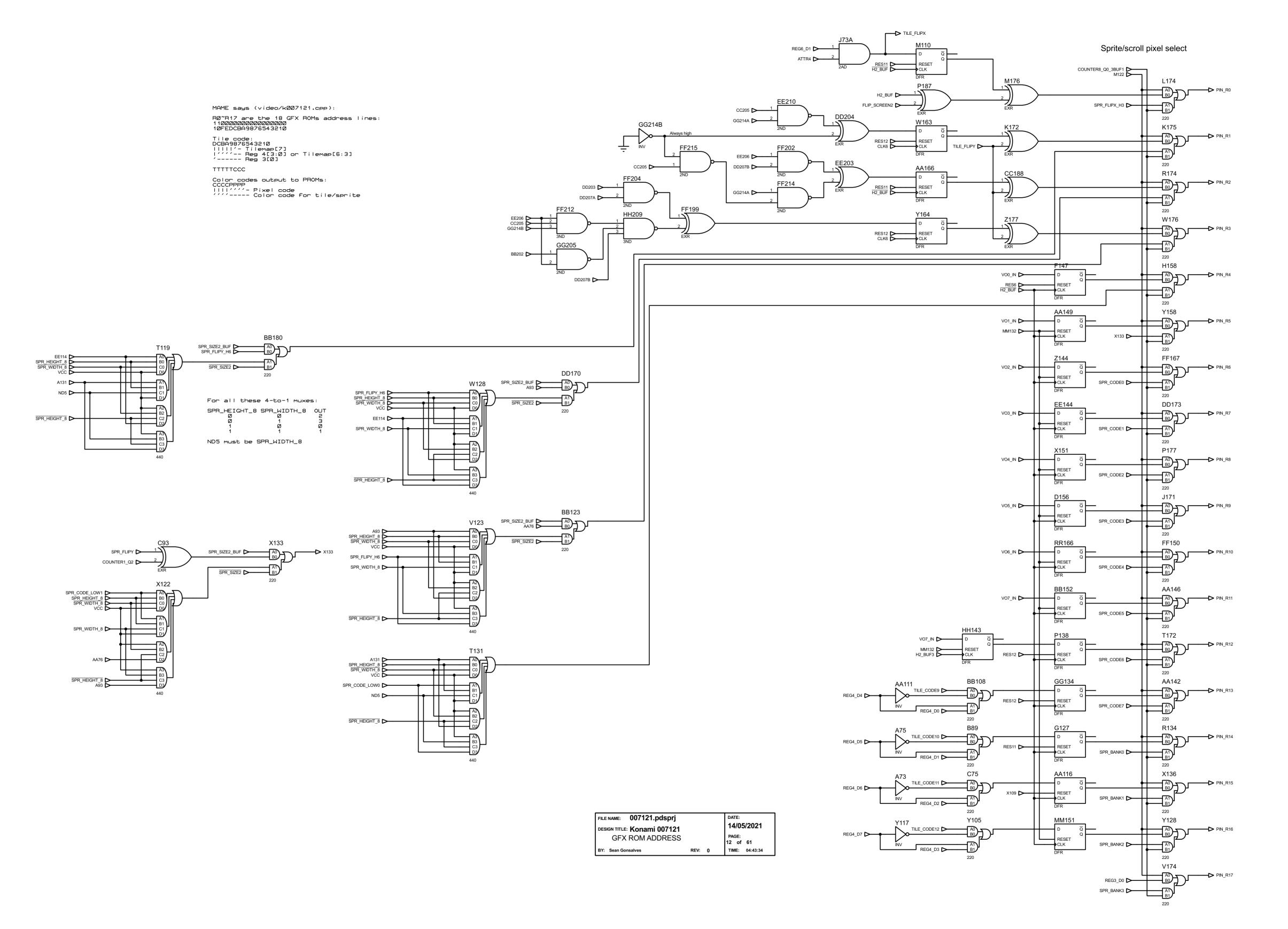


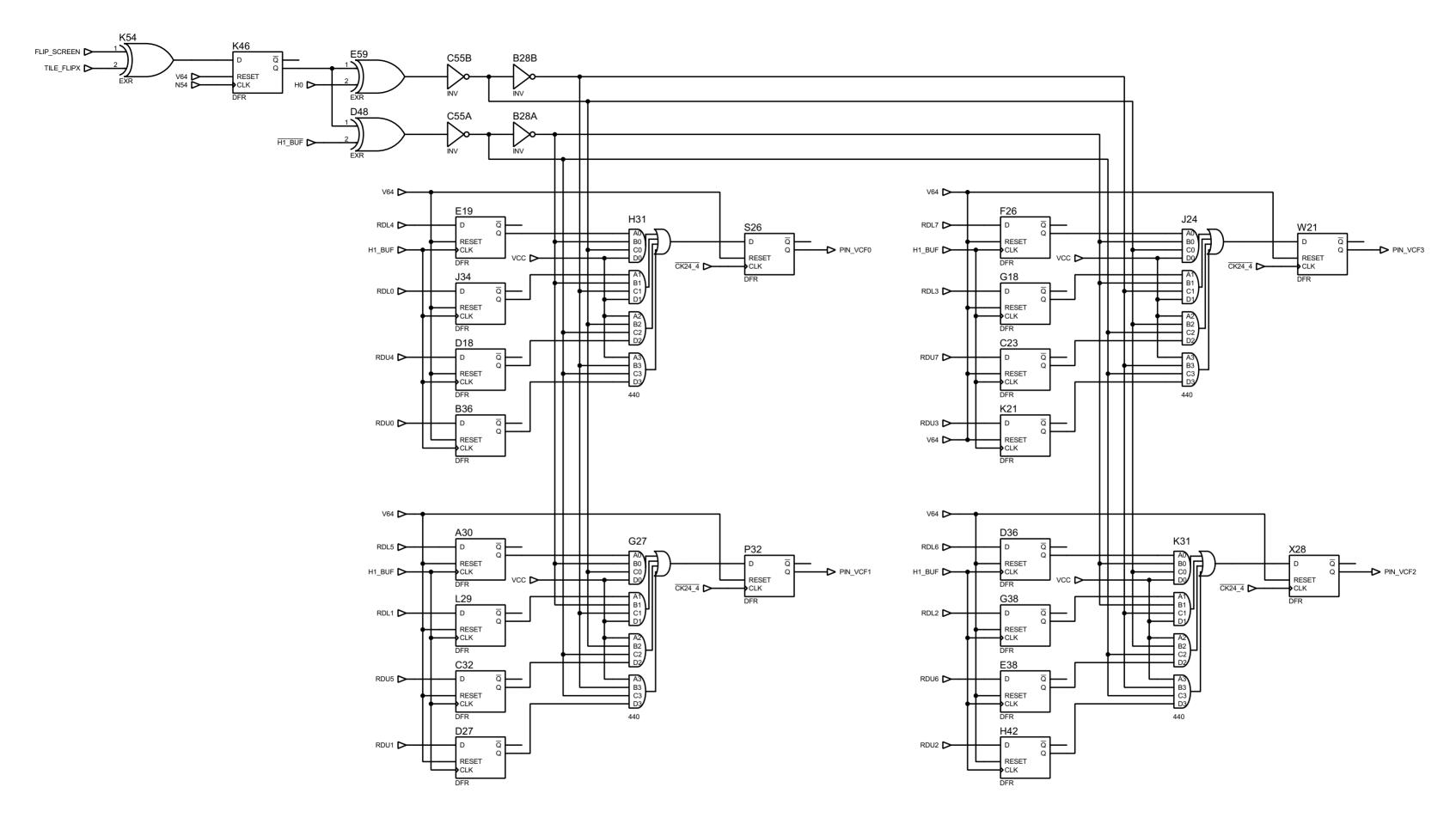




FILE NAME: 007121.pdsprj			DATE:
DESIGN TITLE: Konami 007121			14/05/2021
DRAM ADDRESS			PAGE: 10 of 61
BY: Sean Gonsalves	REV:	0	TIME: 04:43:34

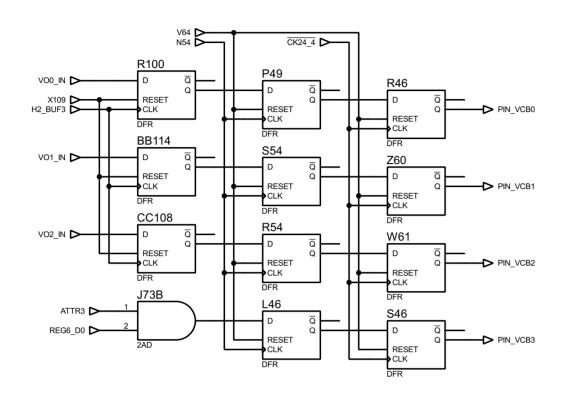


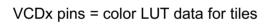


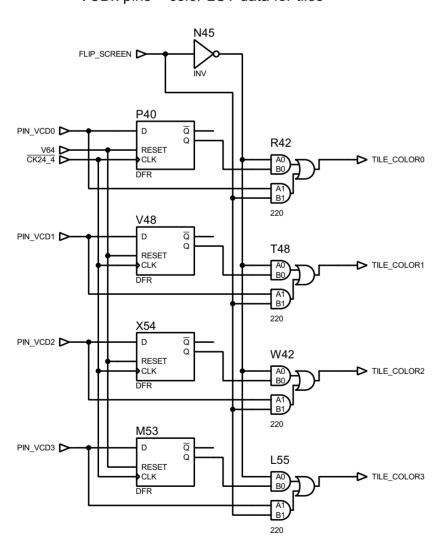


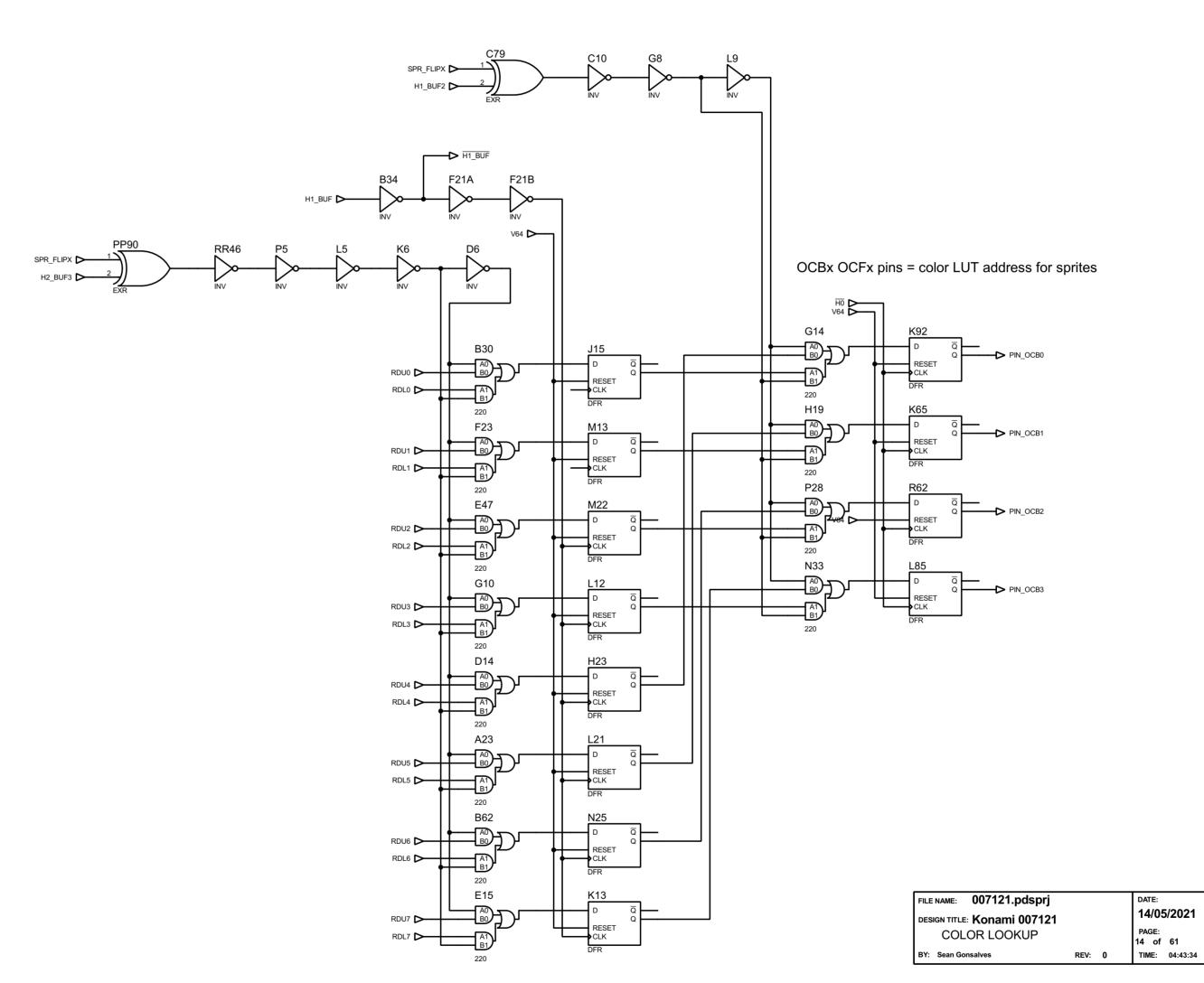
| DATE: | 14/05/2021 | | DATE: | | DATE:

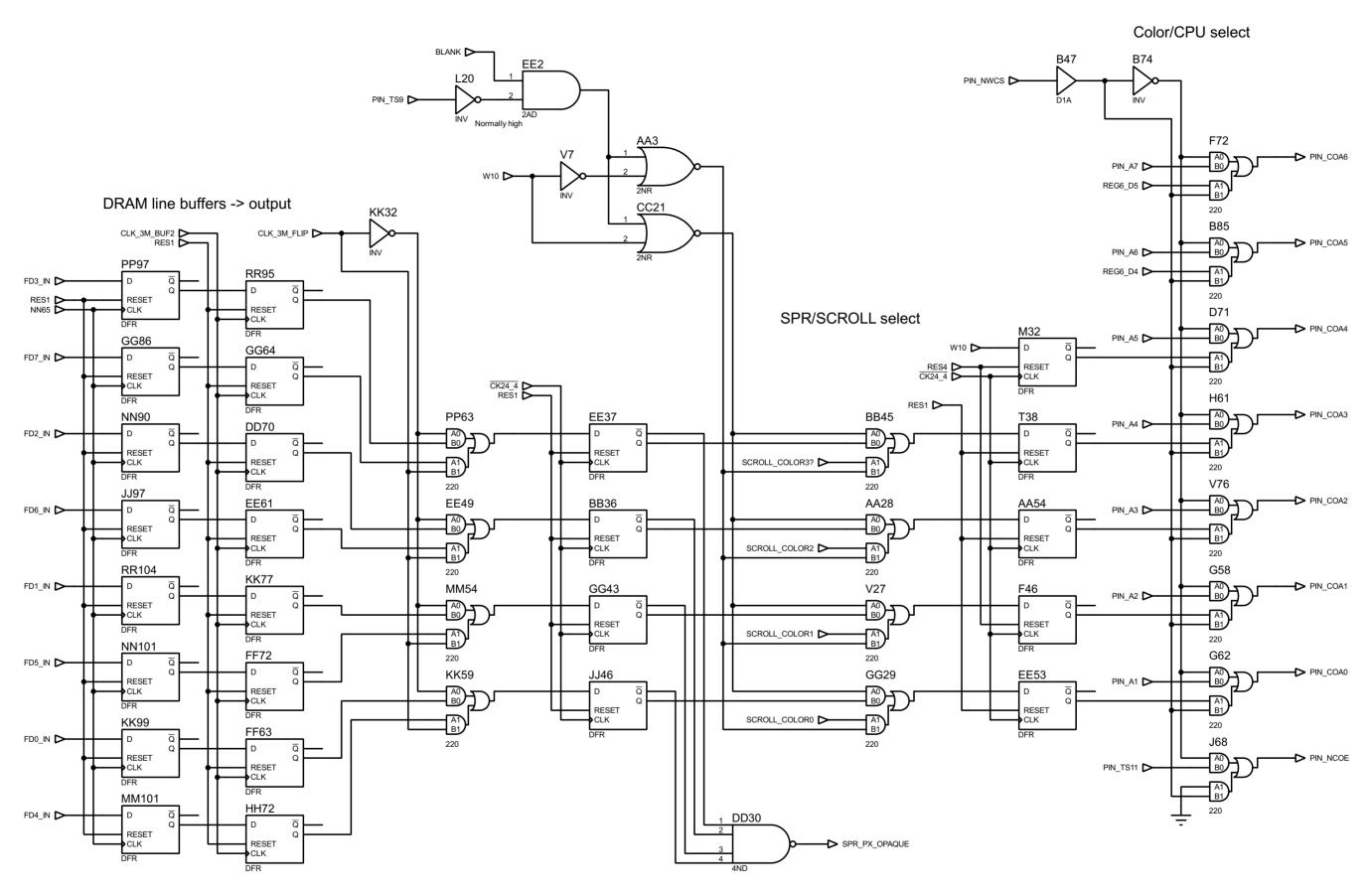
VCBx VCFx pins = color LUT address for tiles



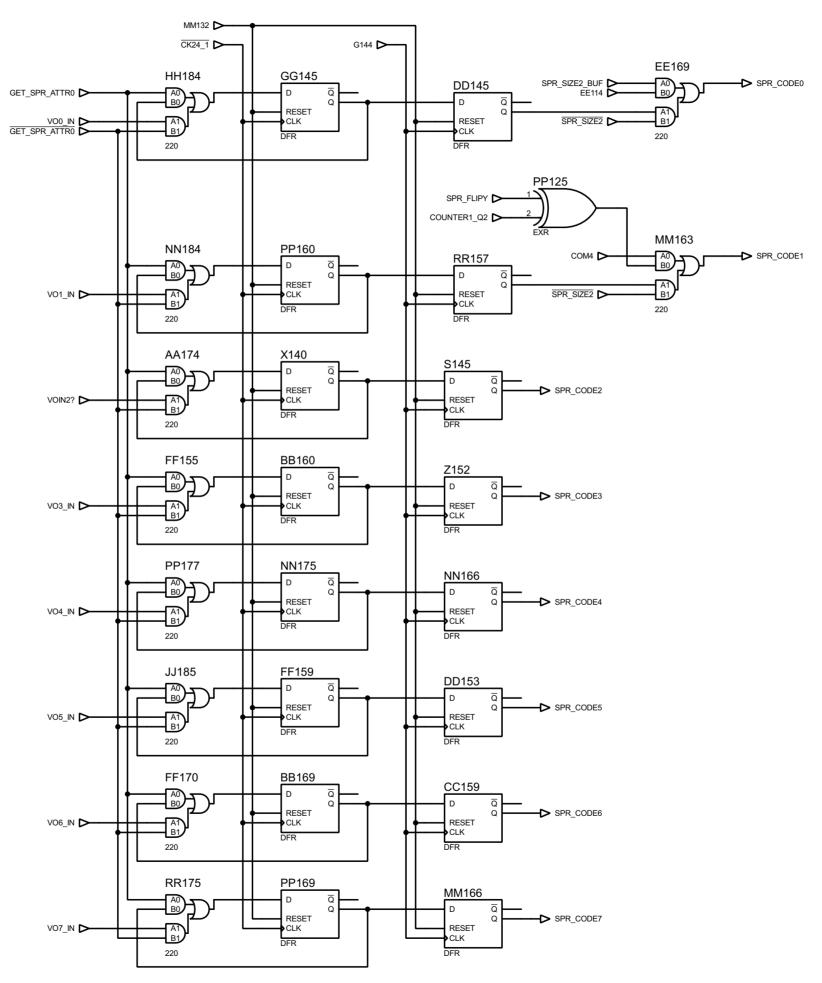








FILE NAME: 007121.pdsprj			DATE:
DESIGN TITLE: Konami 007121			14/05/2021
COLOR OUTPUT	•		PAGE: 15 of 61
BY: Sean Gonsalves	REV:	0	



FILE NAME: 007121.pdsprj

DESIGN TITLE: Konami 007121

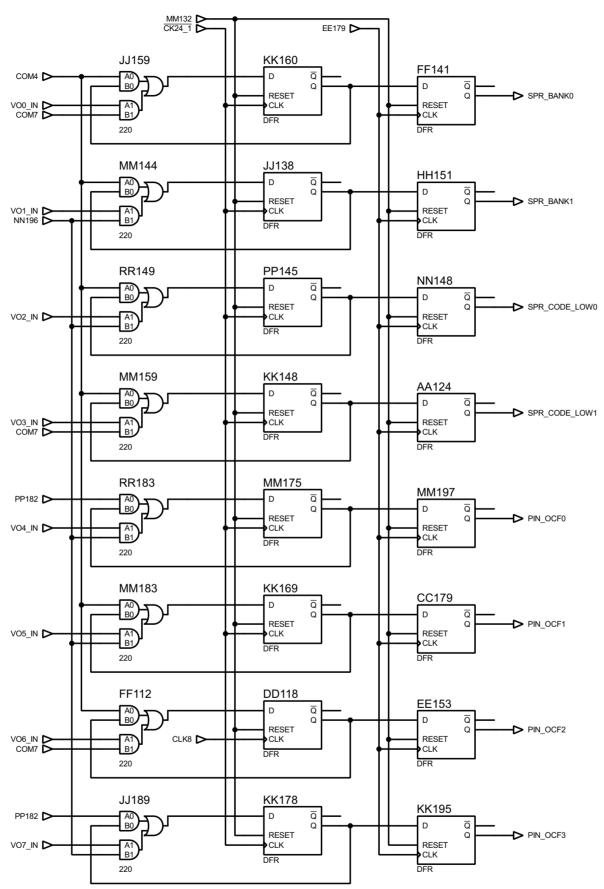
SPR ATTR 0 (TILE #)

BY: Sean Gonsalves

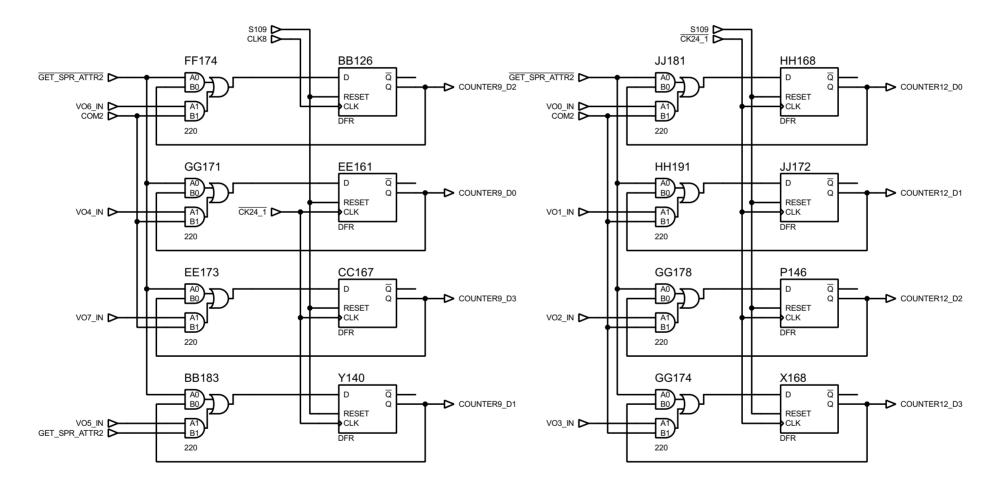
DATE:

14/05/2021

PAGE:
16 of 61

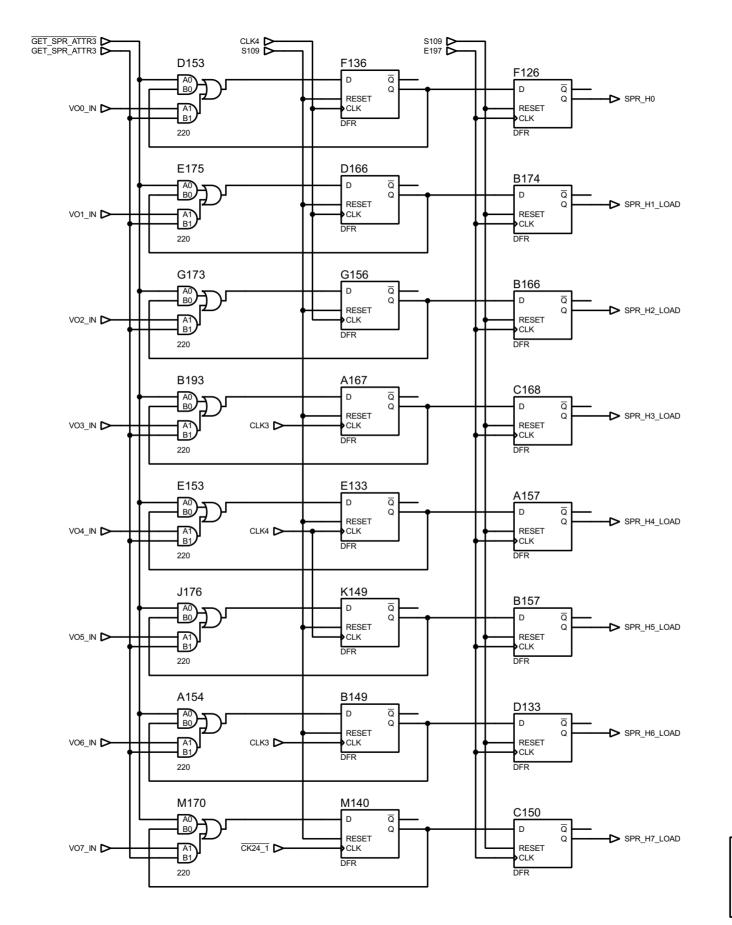


FILE NAME: 007121.pdsprj			DATE:
DESIGN TITLE: Konami 007121			14/05/2021
SPR ATTR 1			PAGE: 17 of 61
BY: Sean Gonsalves	REV:	0	

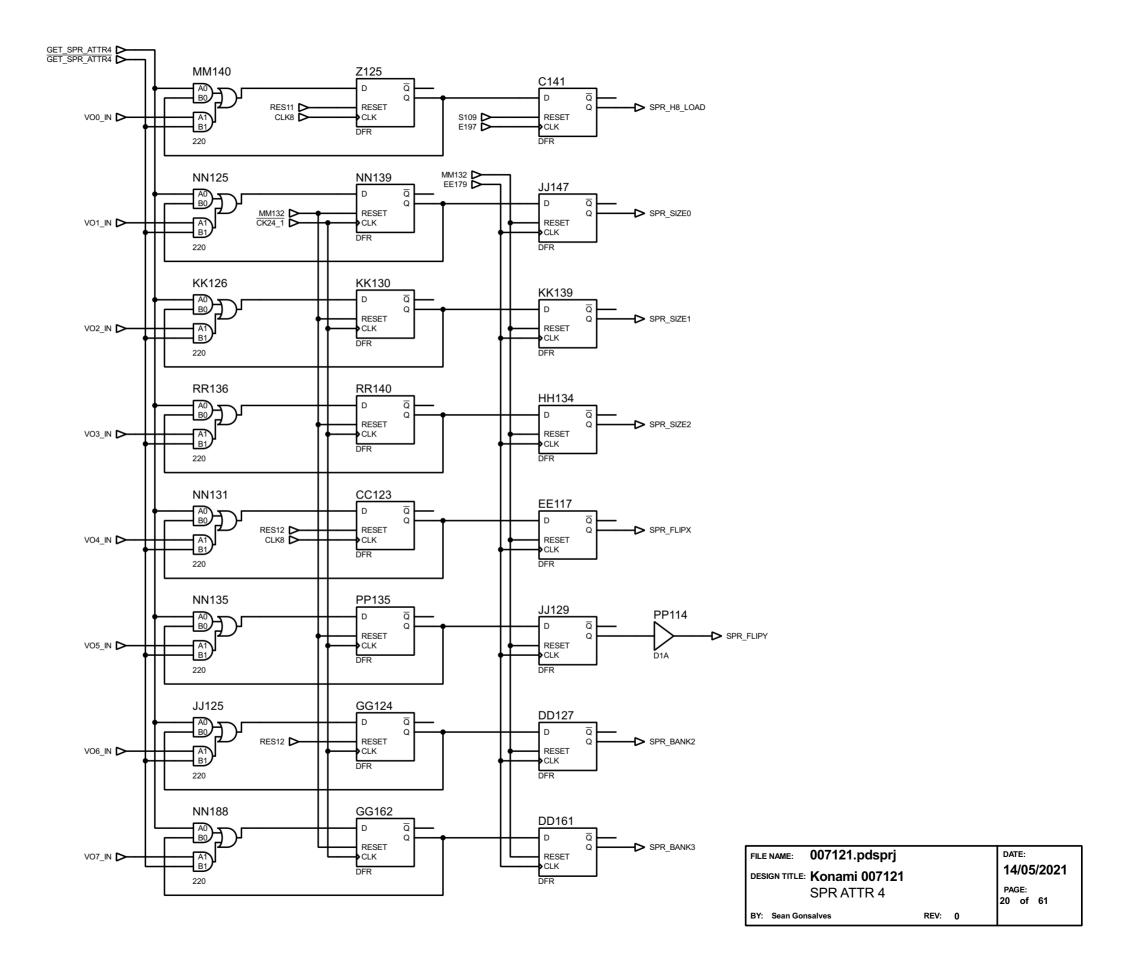


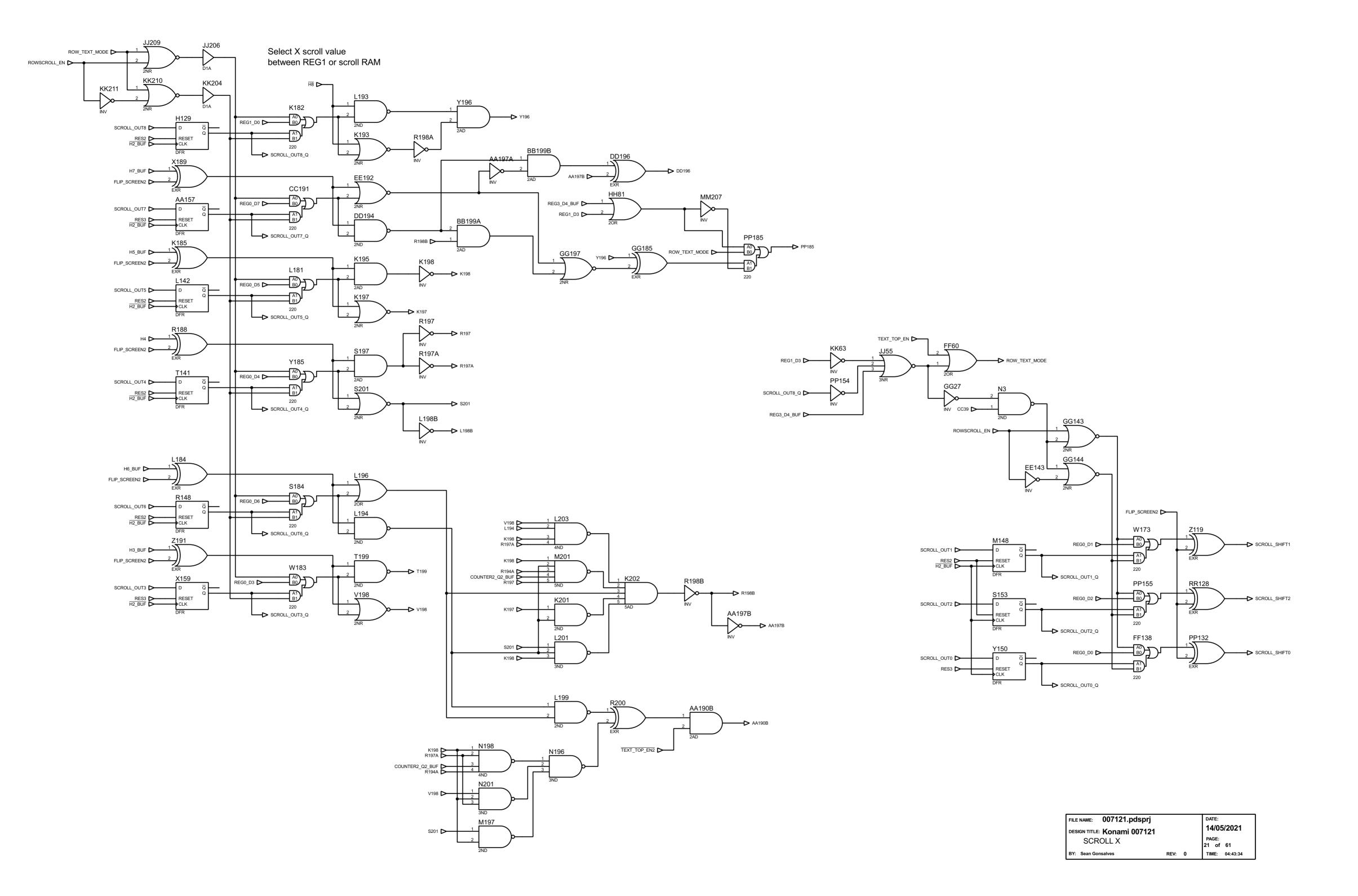
COM2 must be GET_SPR_ATTR2

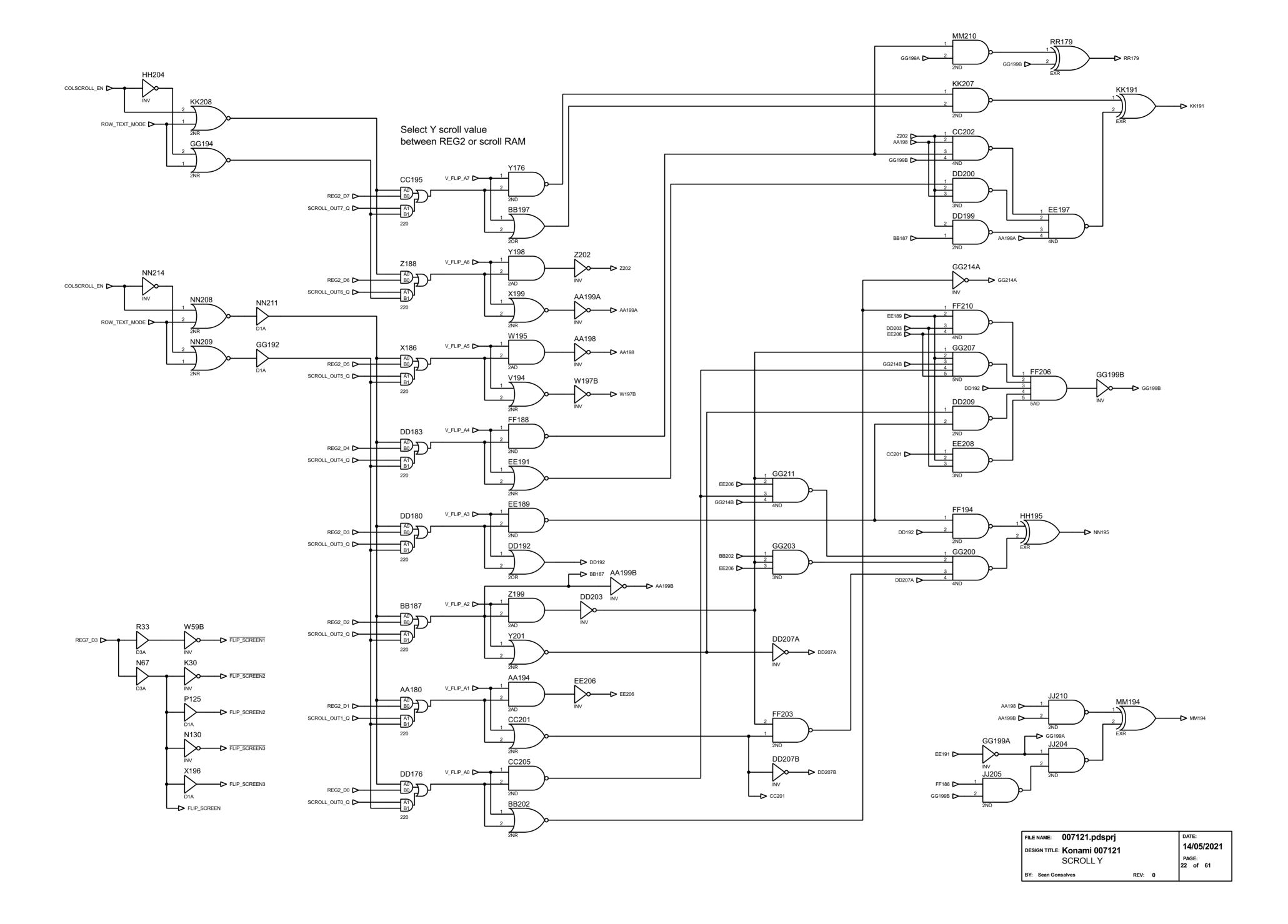
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DESIGN TITLE: Konami 007121			14/05/2021
SPR ATTR 2			PAGE: 18 of 61
BY: Sean Gonsalves	REV:	0	TIME: 04:43:34



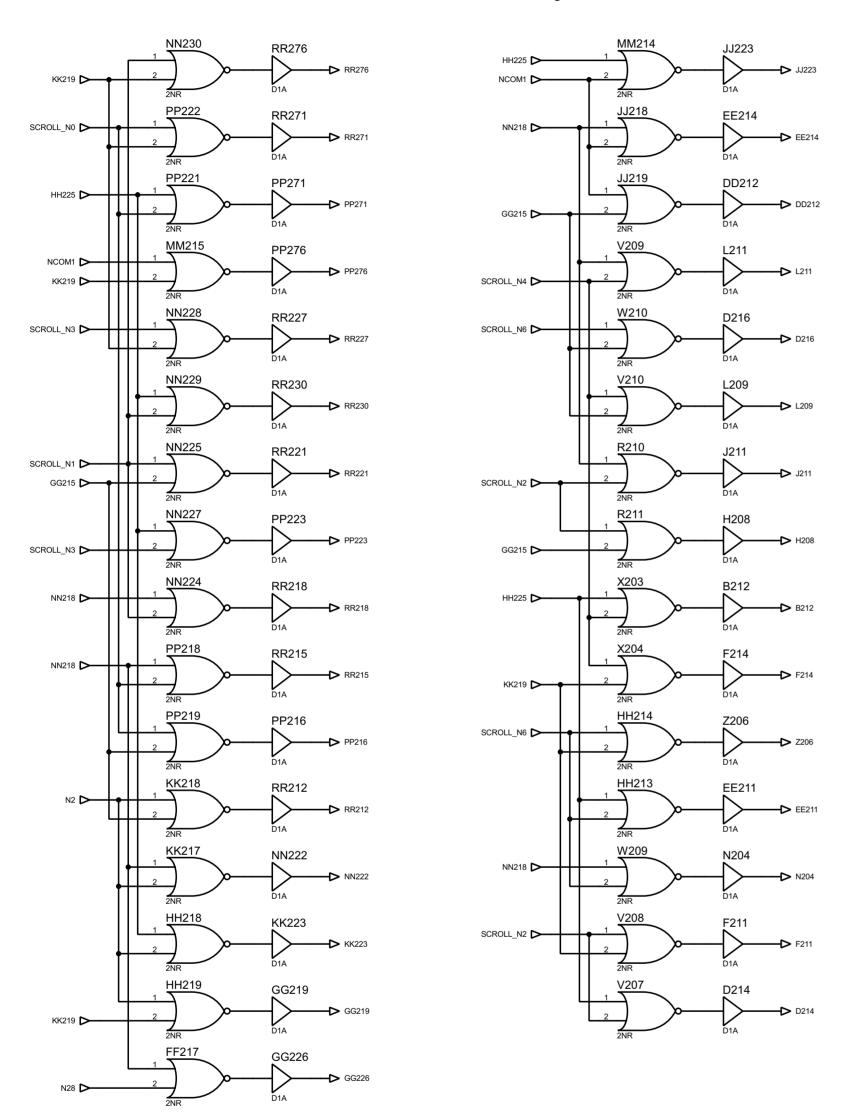
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DESIGN TITLE:	Konami 007121			14/05/2021
	SPR ATTR 3 (X PC	OS)		PAGE: 19 of 61
BY: Sean Gon	salves	REV:	0	

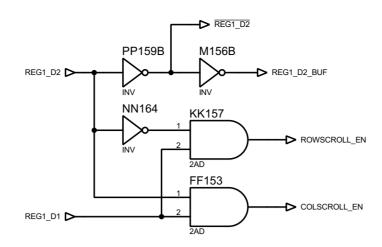


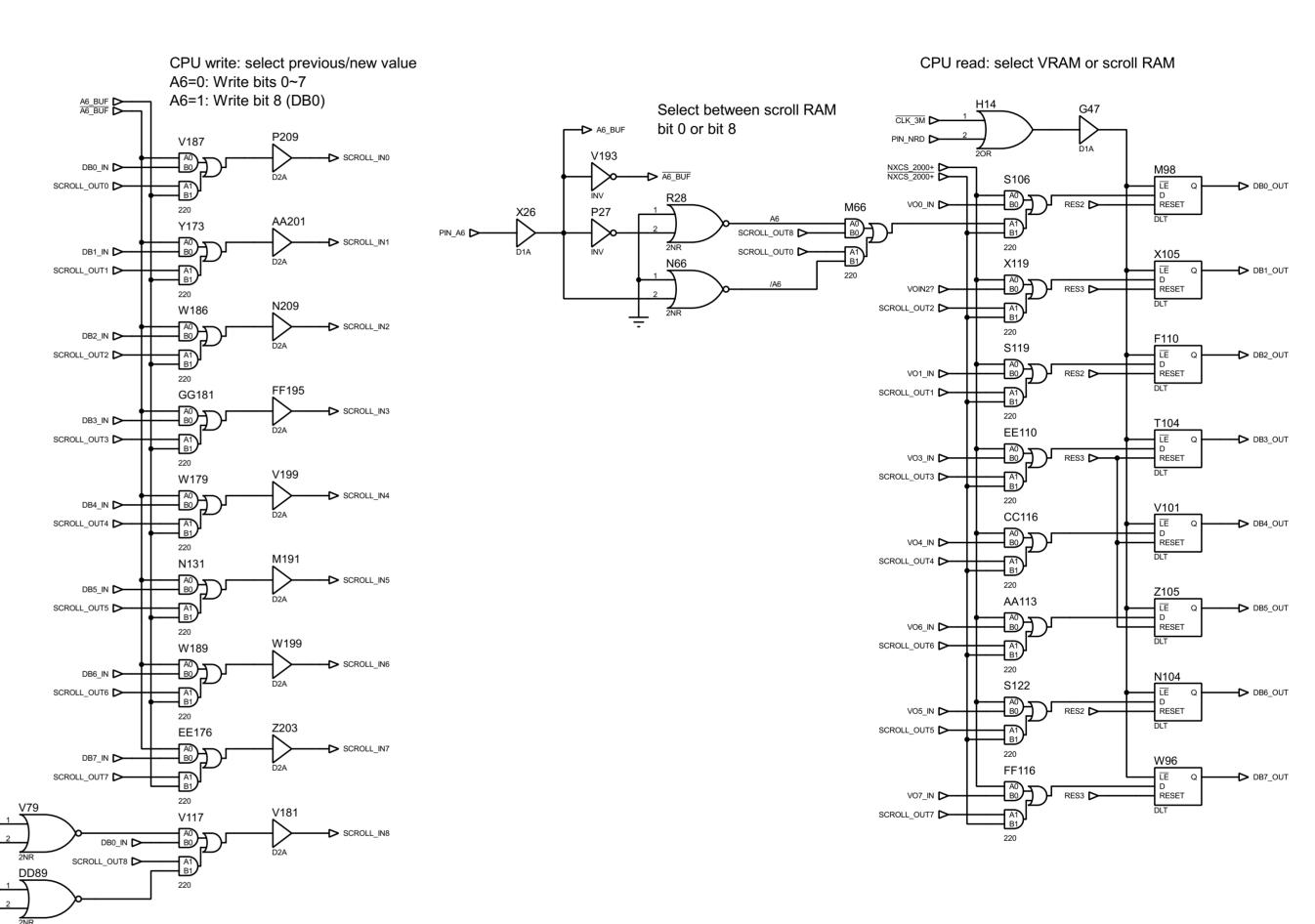


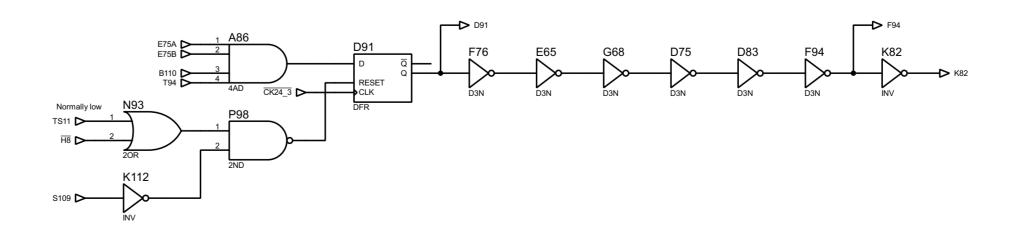


Scroll RAM address decode for writing

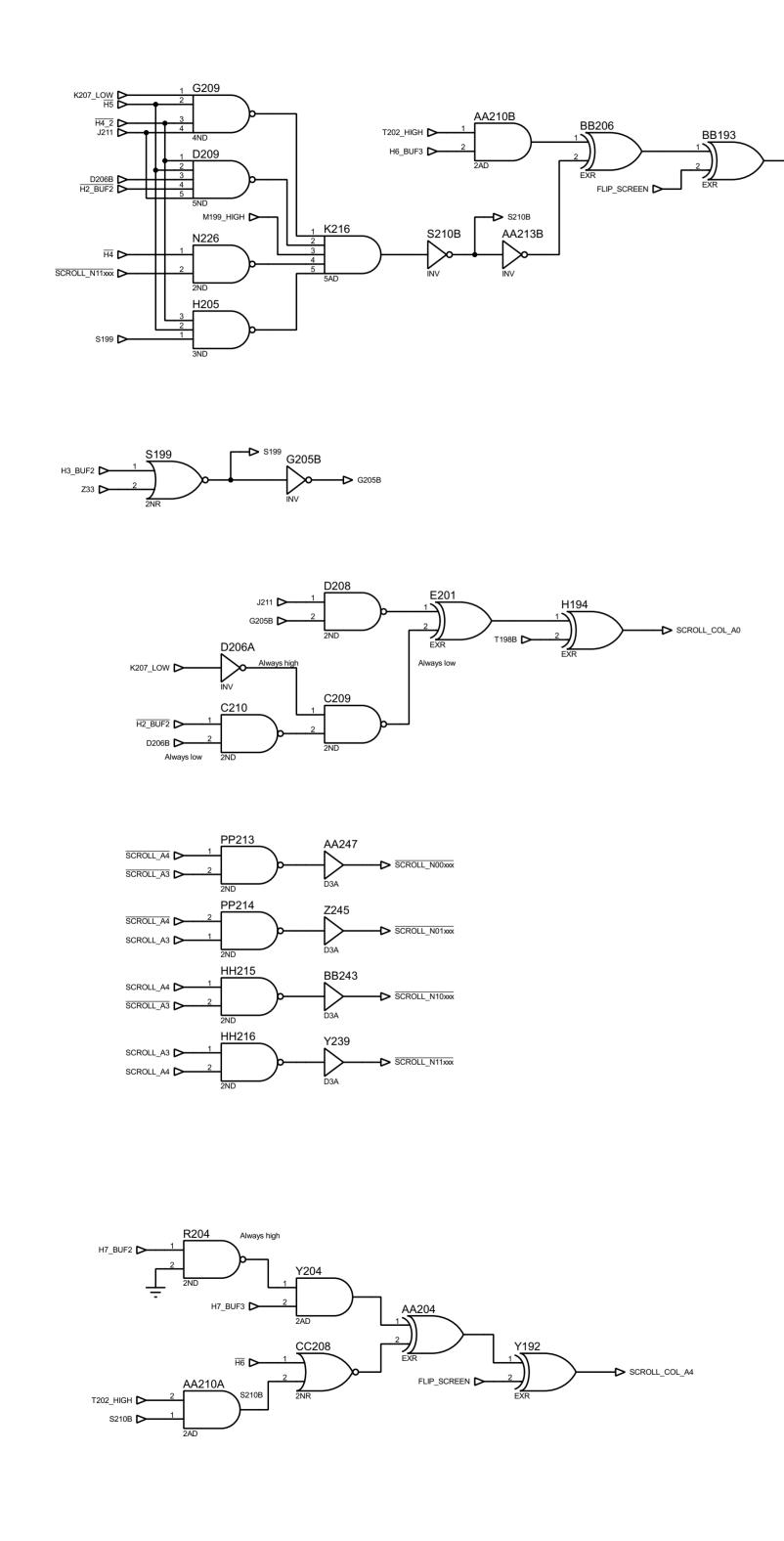


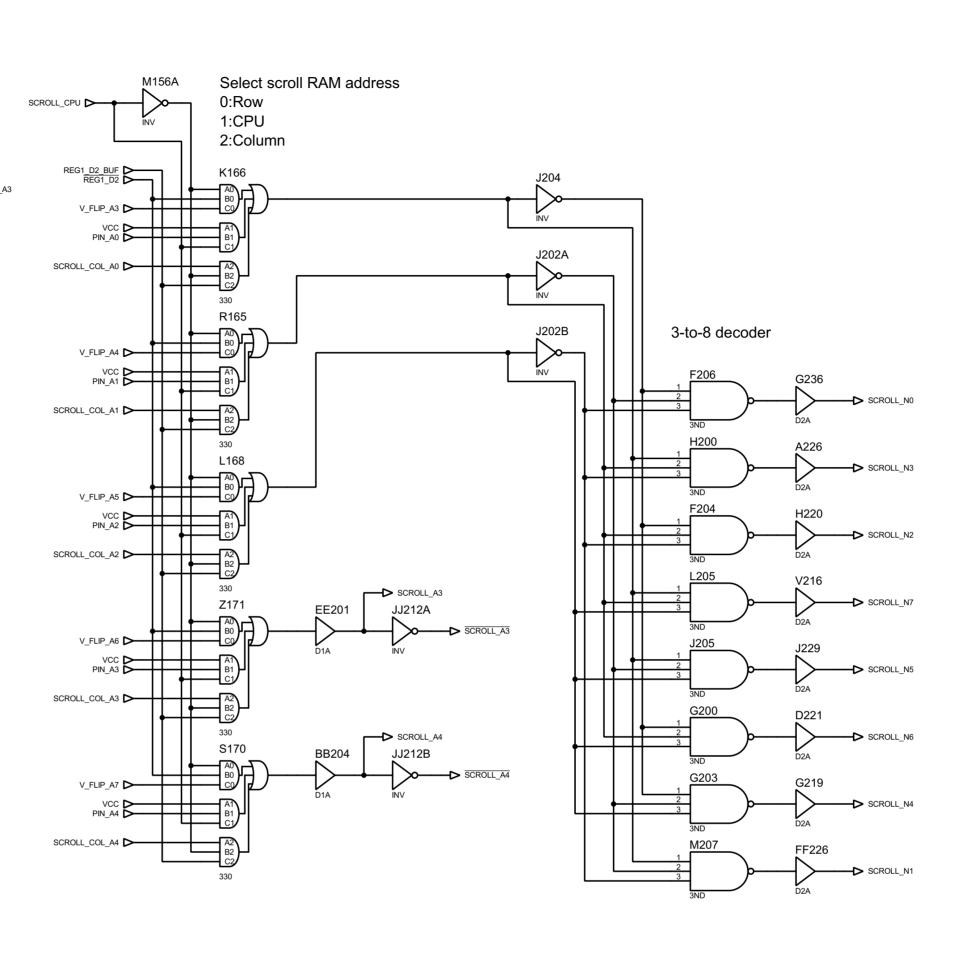


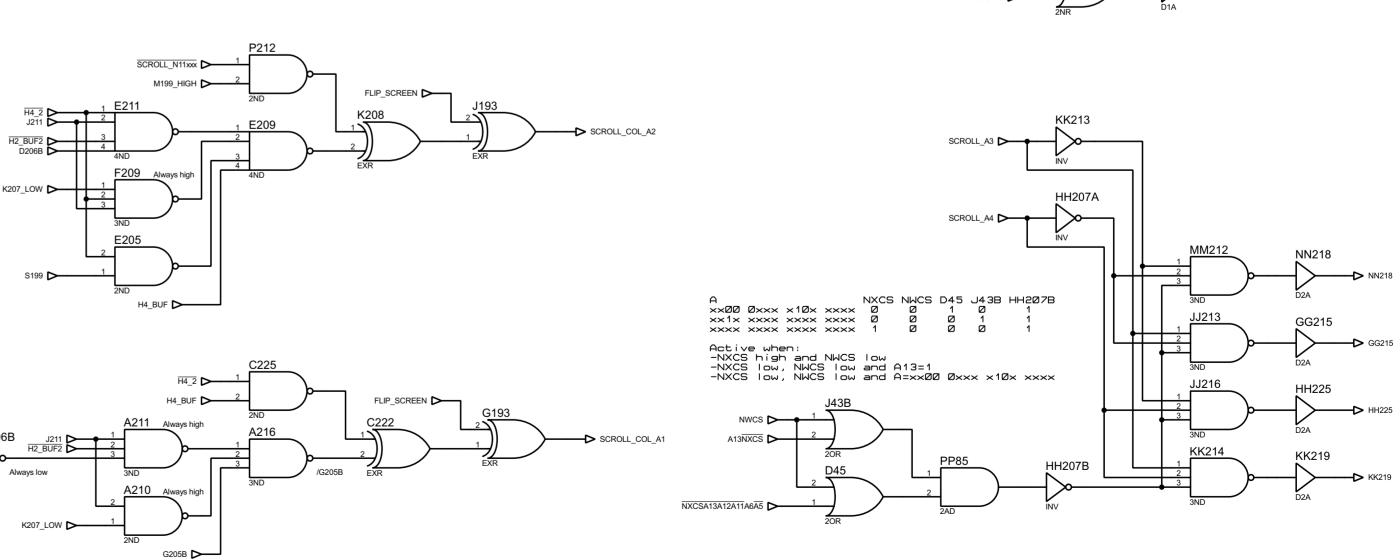




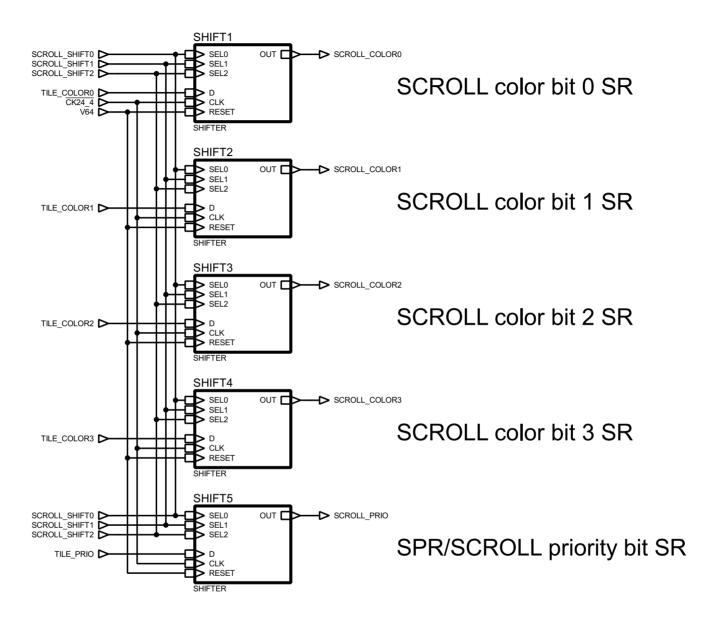
FILE NAME: 007121.pdsprj	İ		DATE:
DESIGN TITLE: Konami 00712	21		14/05/2021
SCROLL RAM ACC	ESS		PAGE: 23 of 61
BY: Sean Gonsalves	REV:	0	TIME: 04:43:34

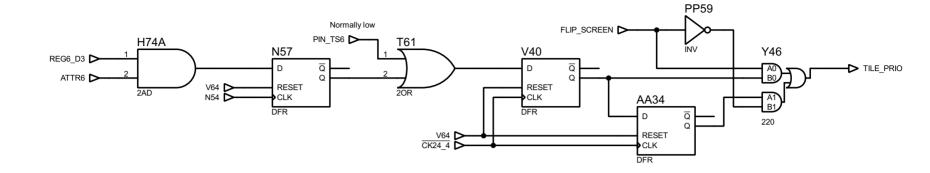


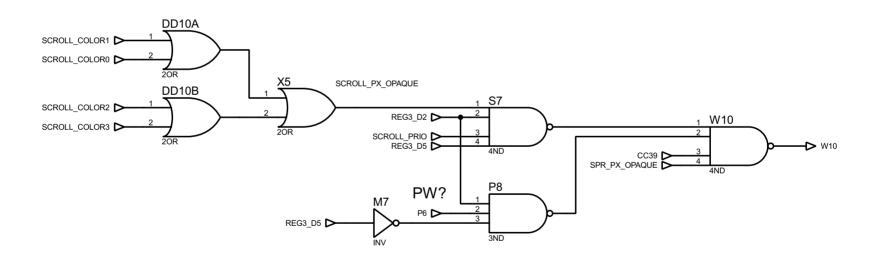




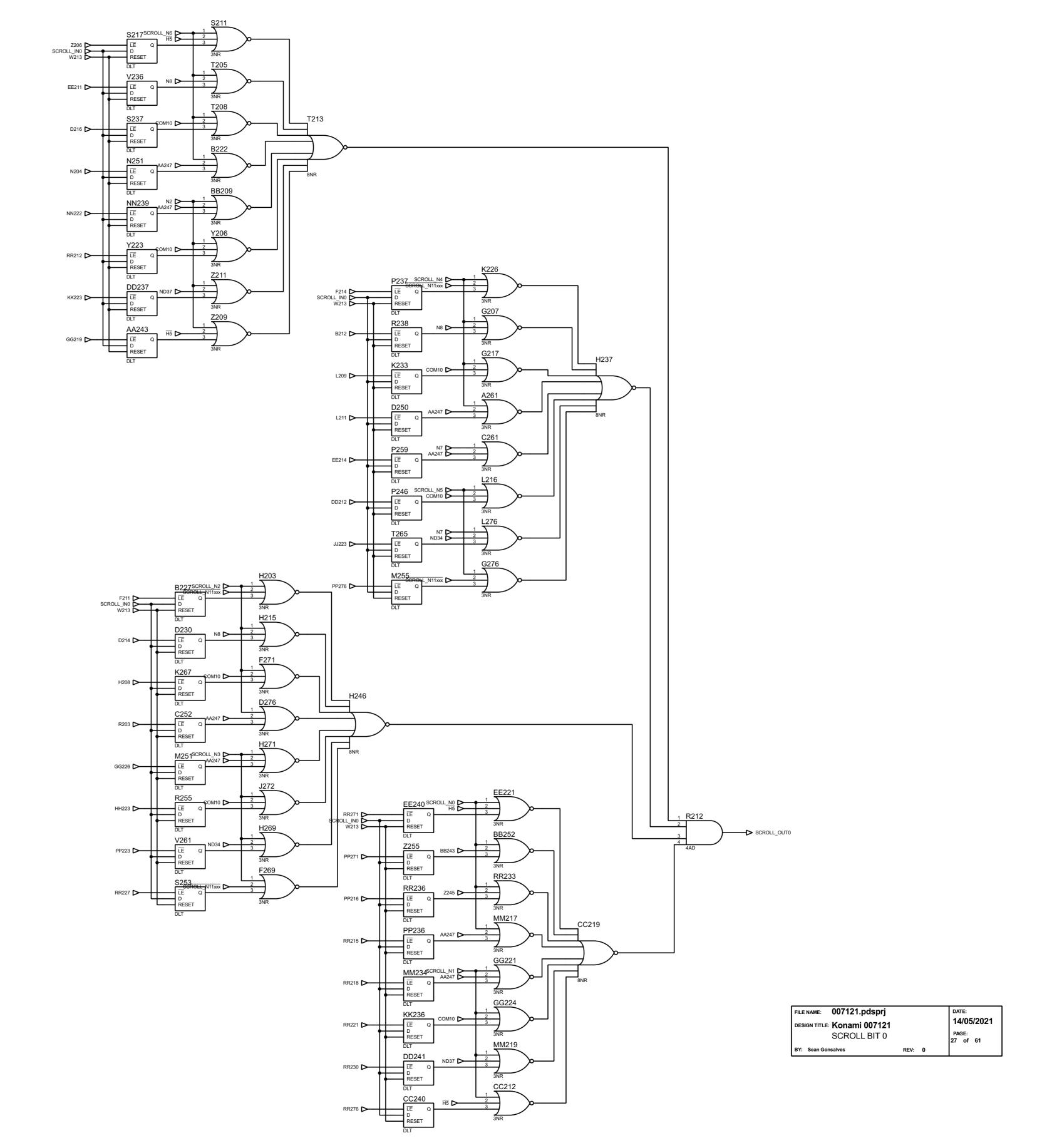
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DESIGN TITLE: Konami 007	121	14/05/2021
SCROLL RA	AM ADDRESS	PAGE: 24 of 61
BY: Sean Gonsalves	REV: 0	

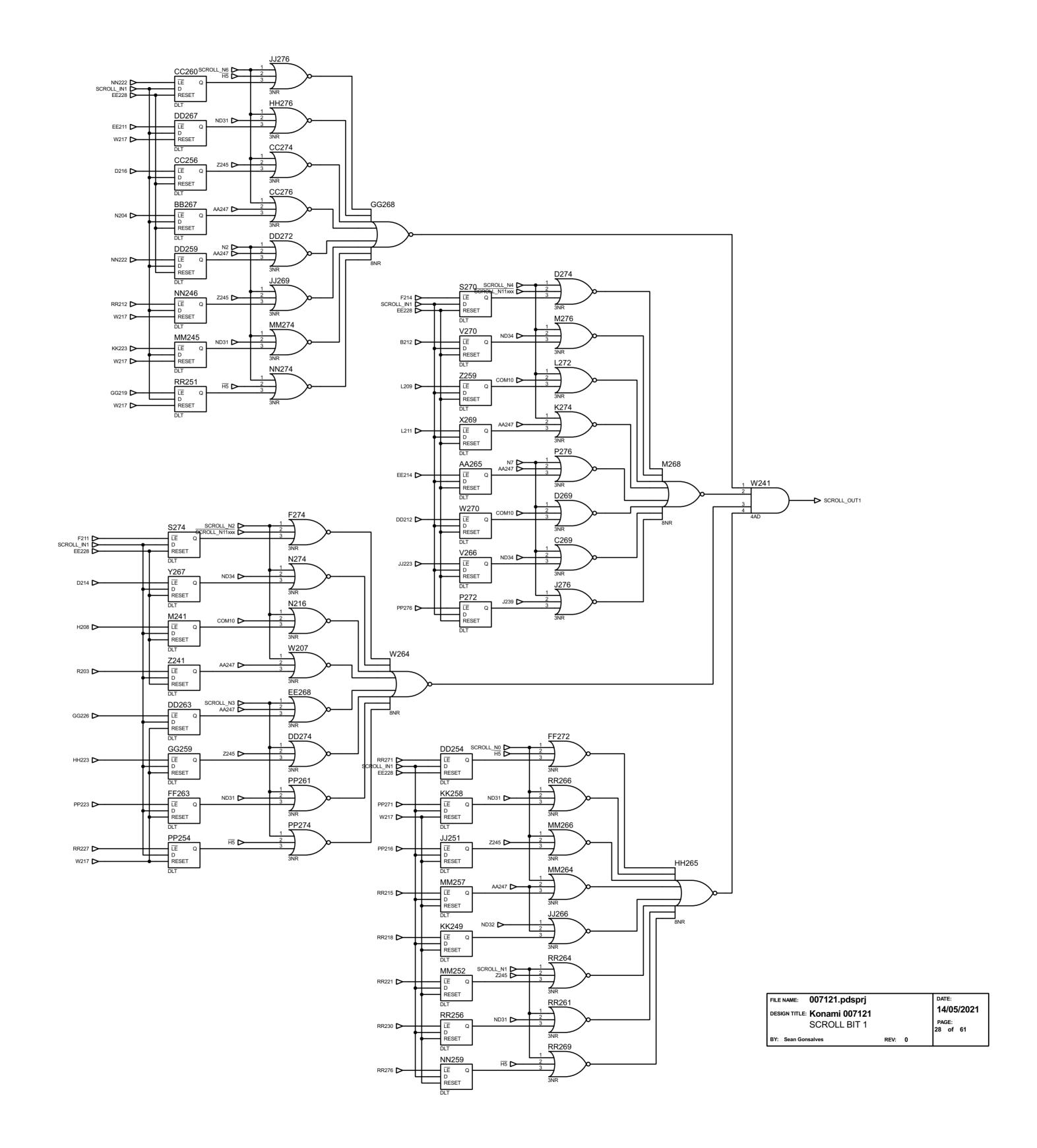


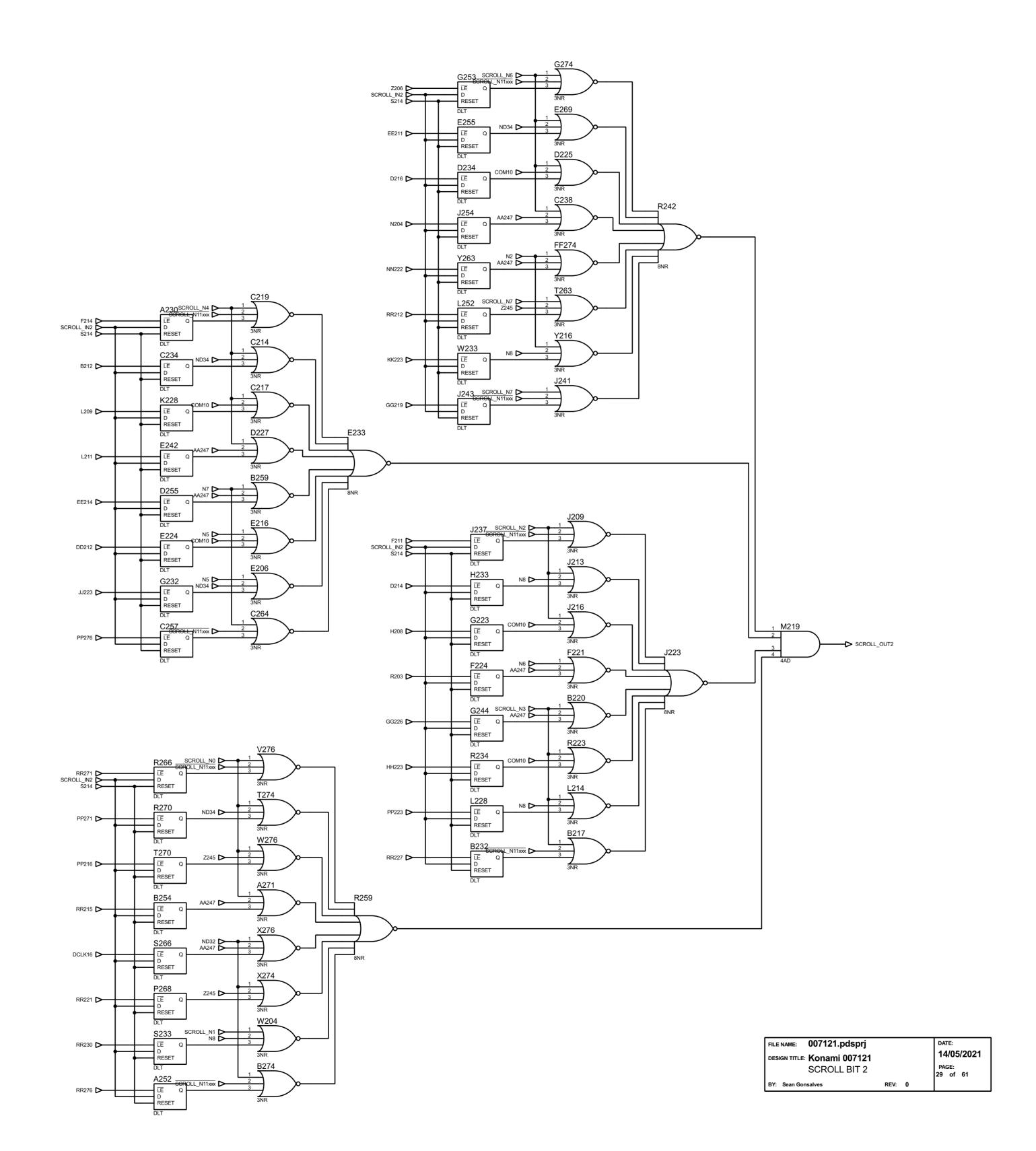
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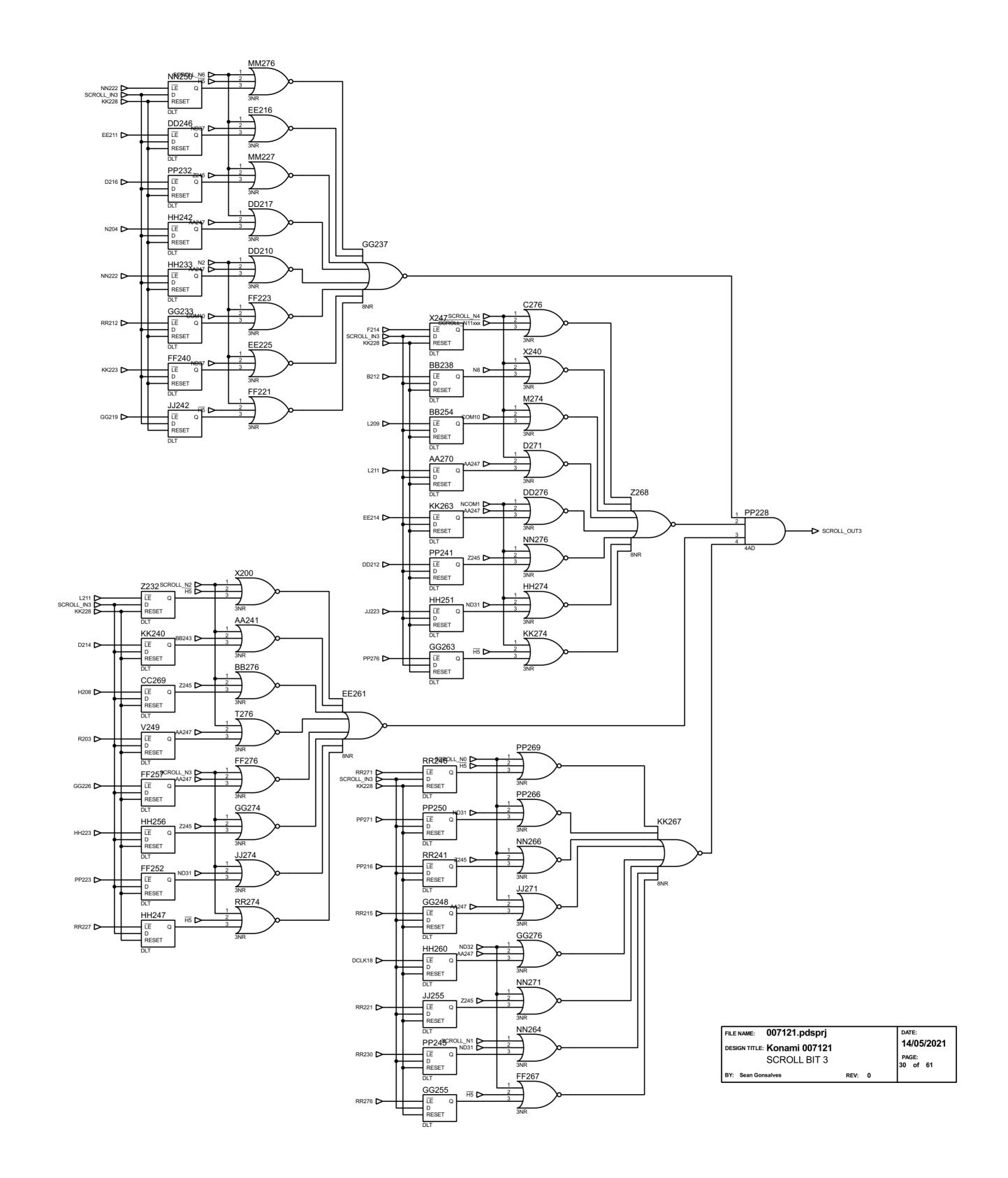


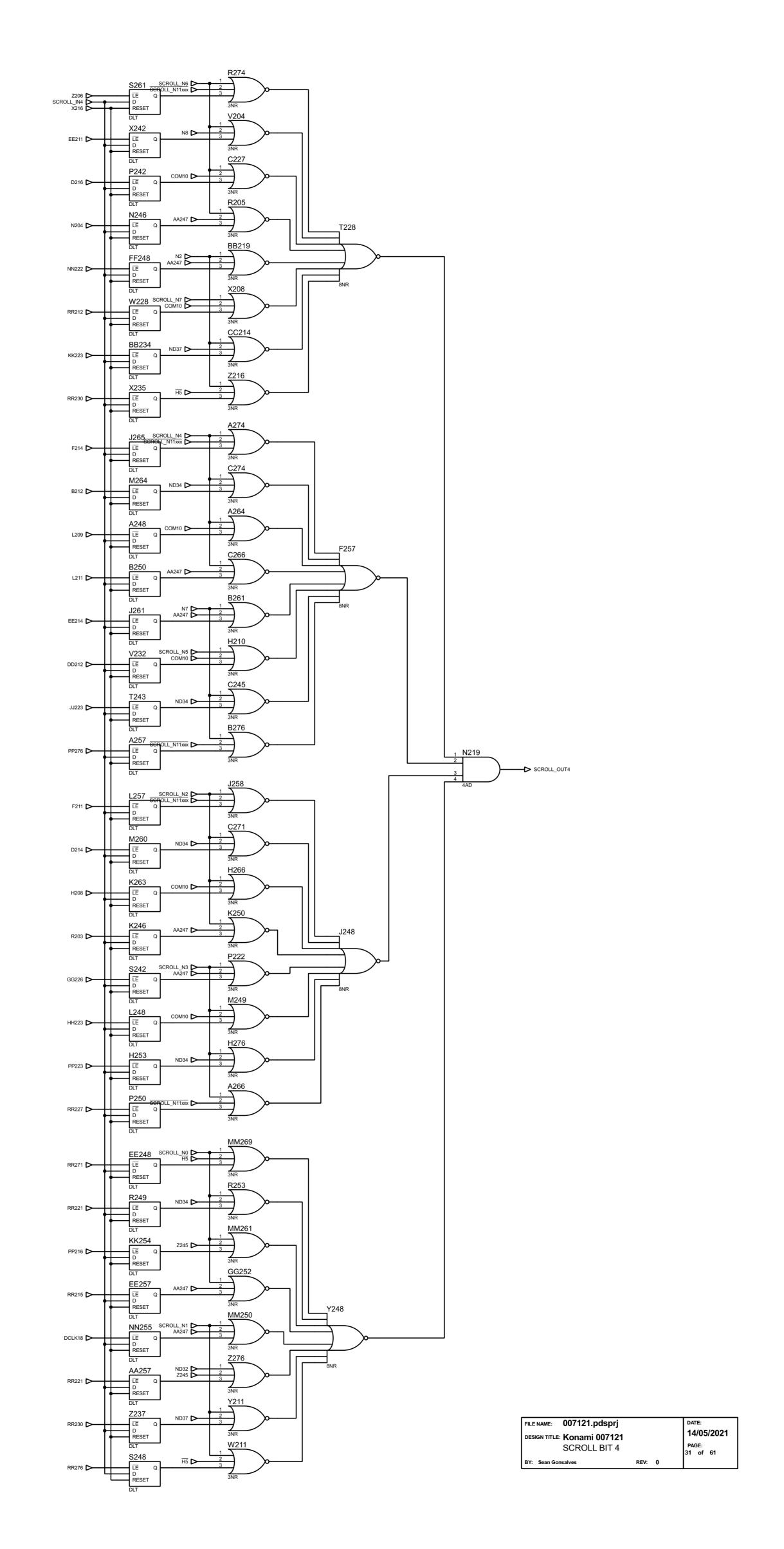
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DESIGN TITLE: Konami 007121			14/05/2021
PRIORITY			PAGE: 26 of 61
BY: Sean Gonsalves	REV:	0	TIME: 04:43:34

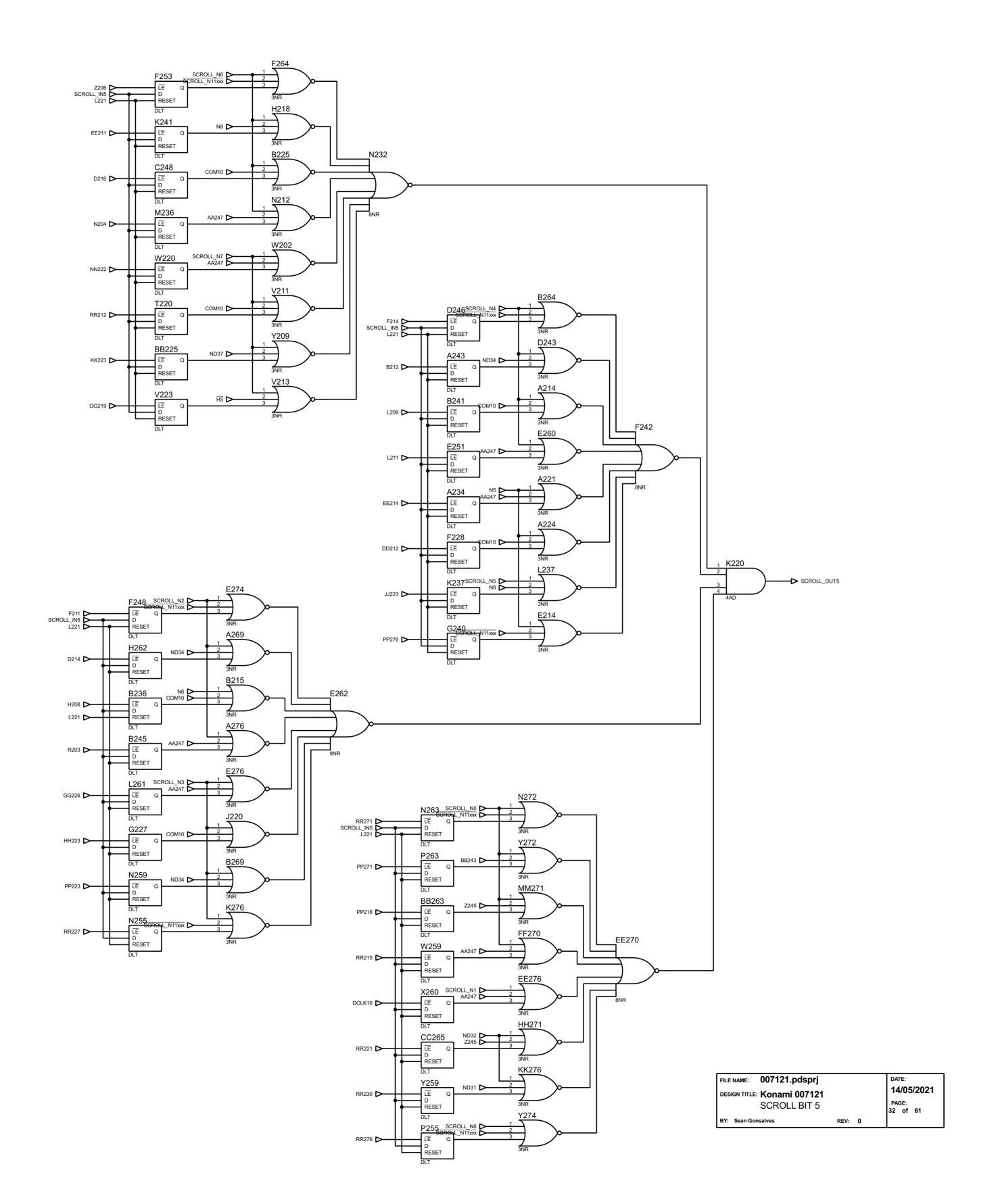


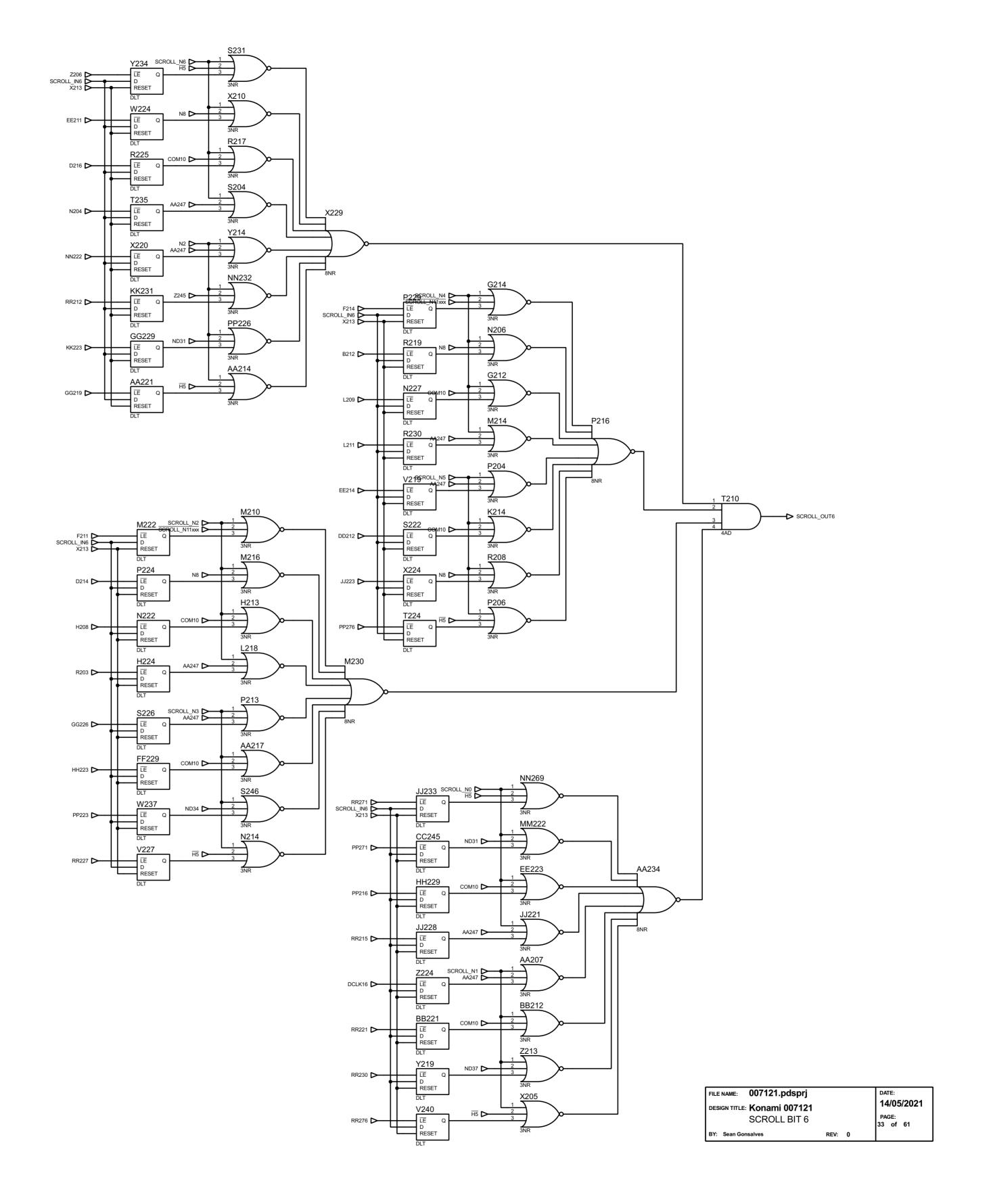


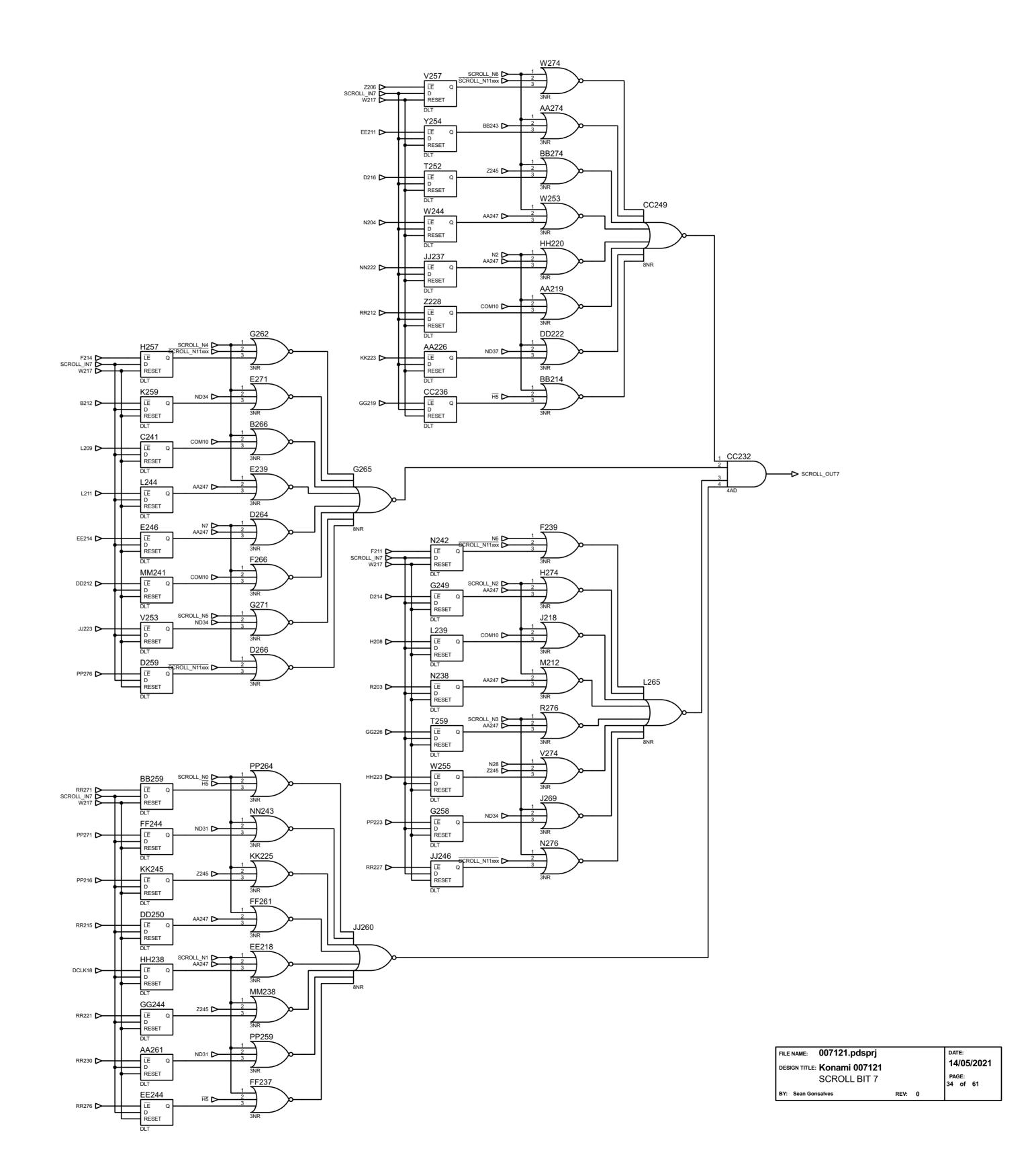


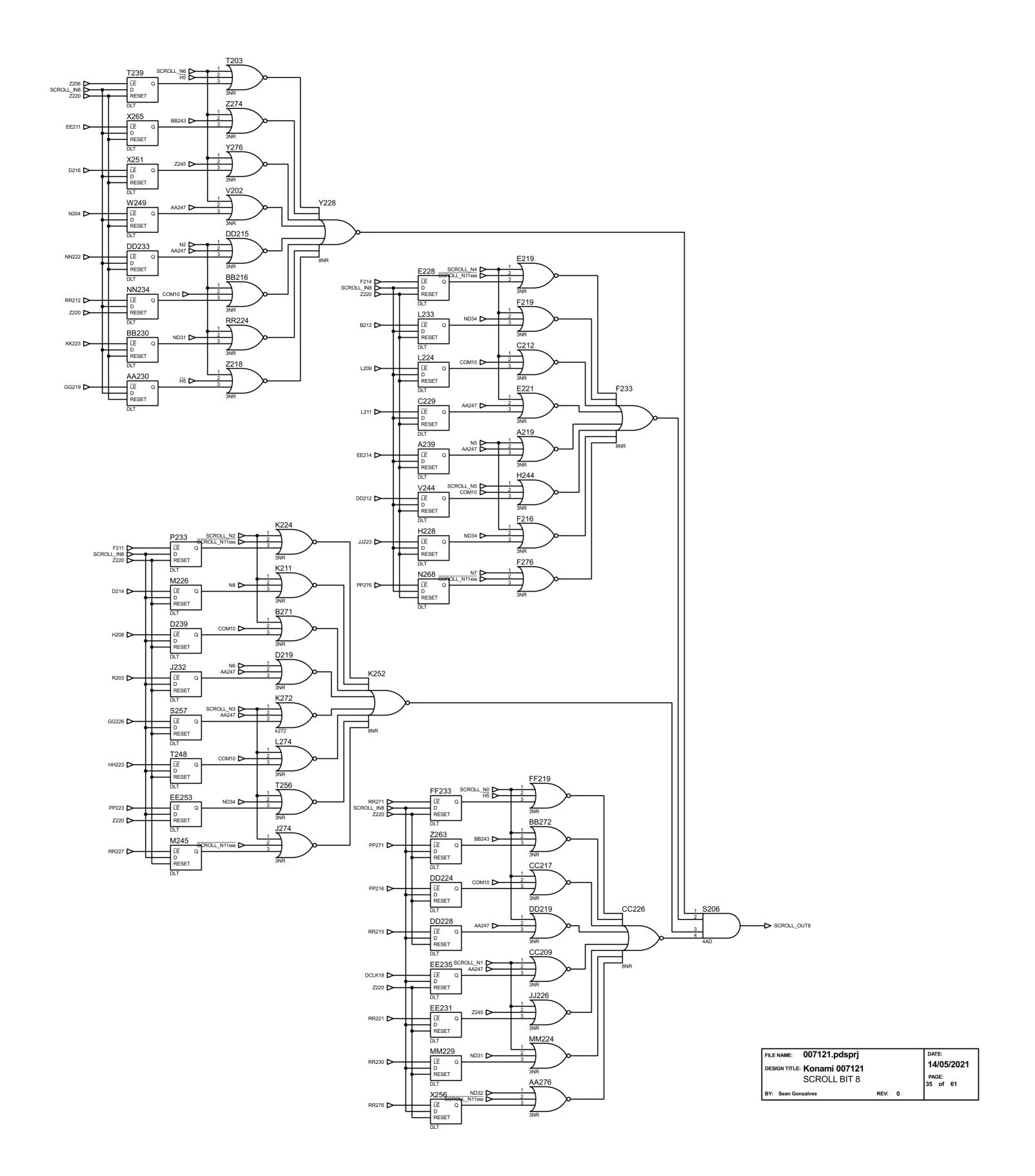


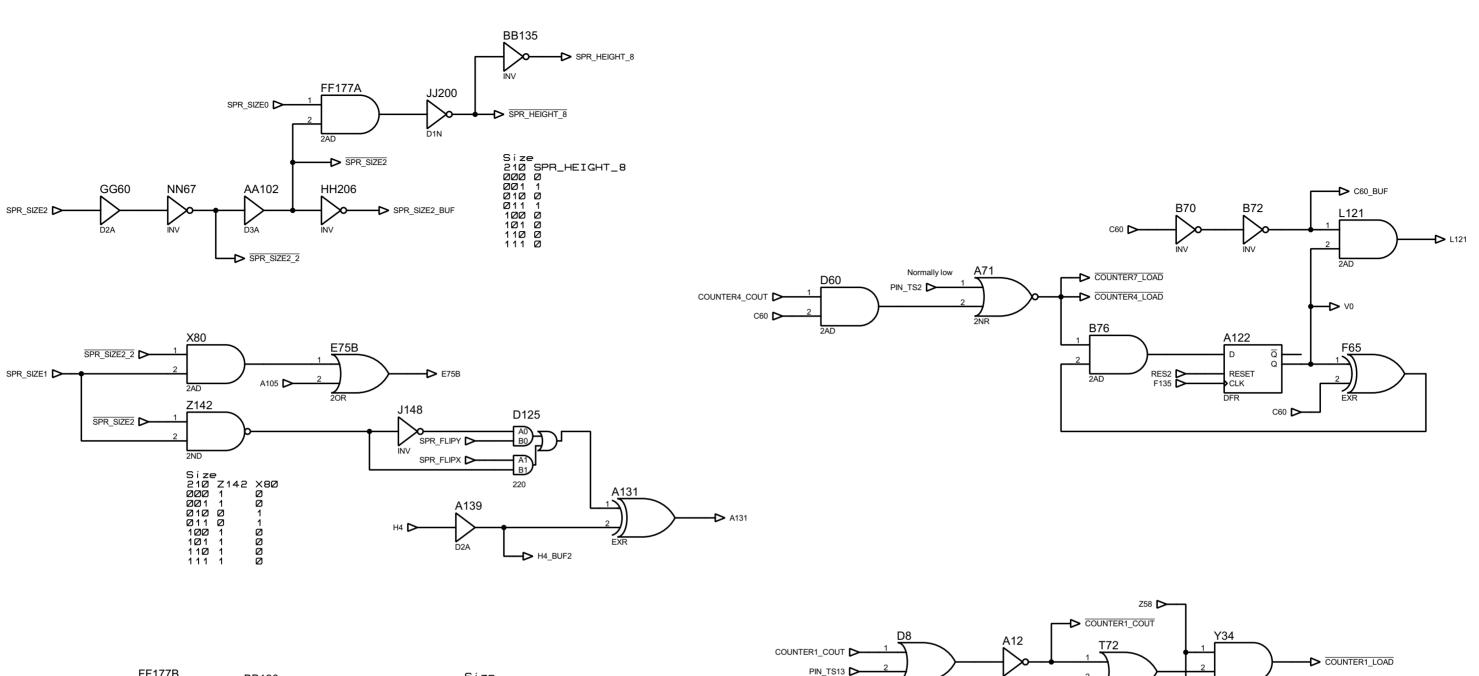


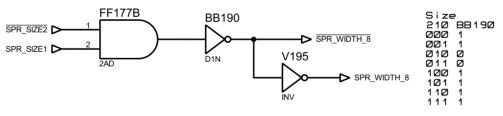


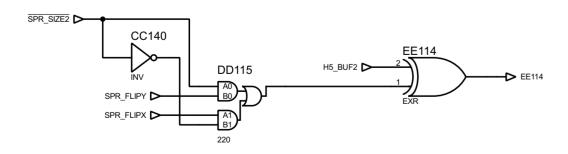


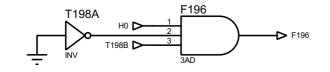


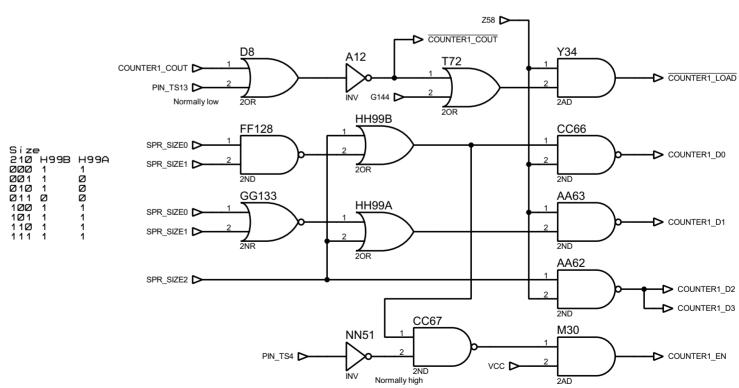




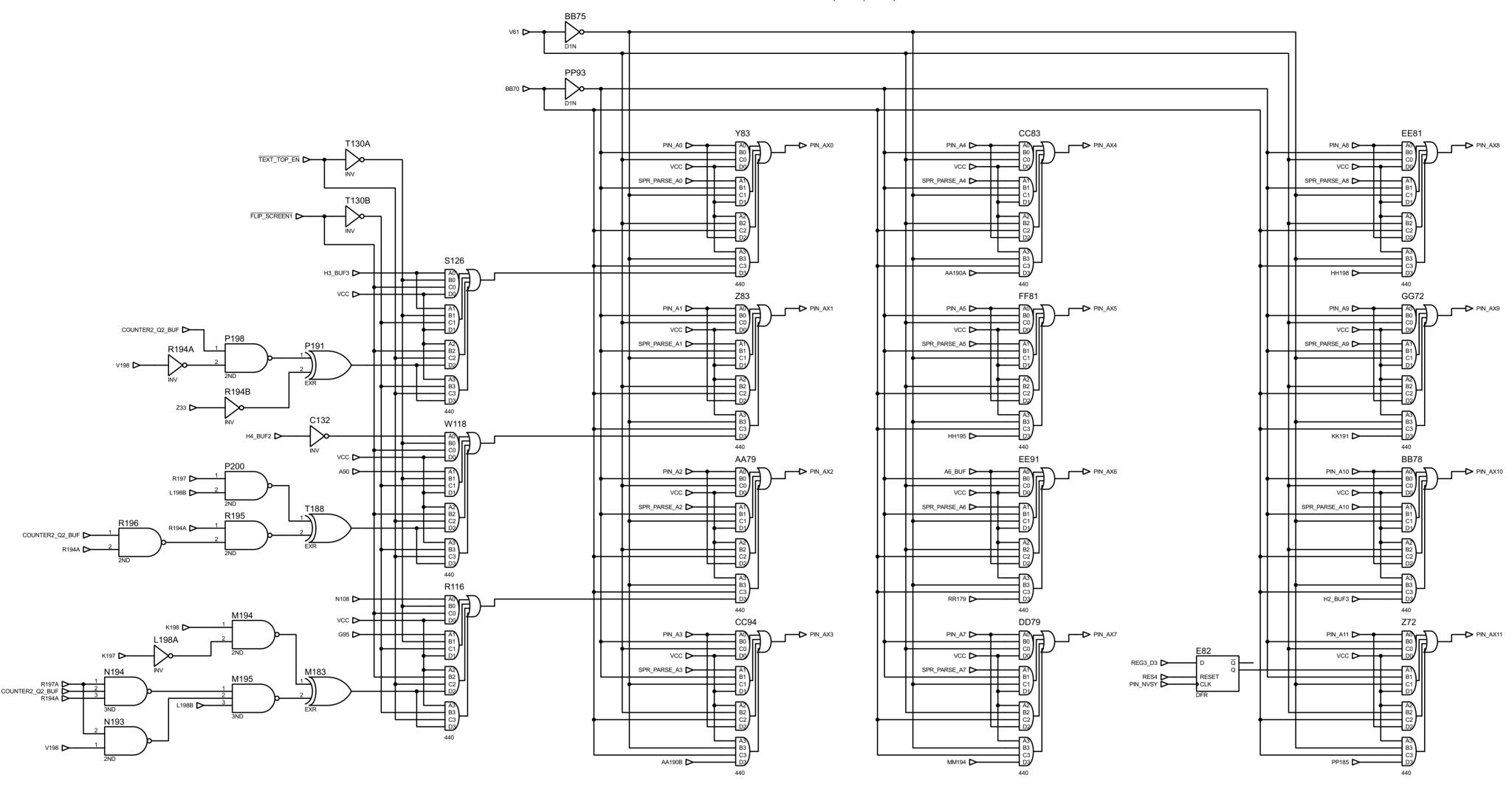


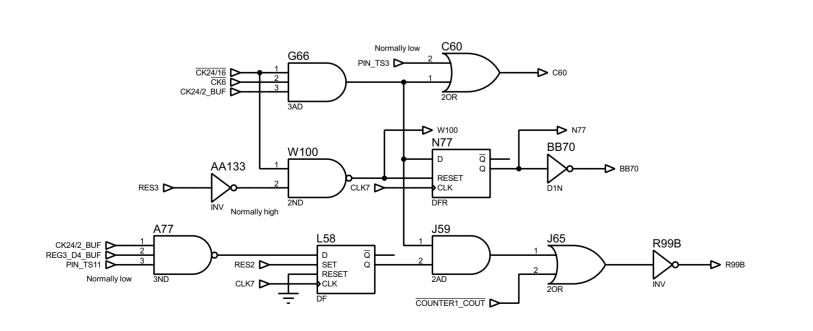


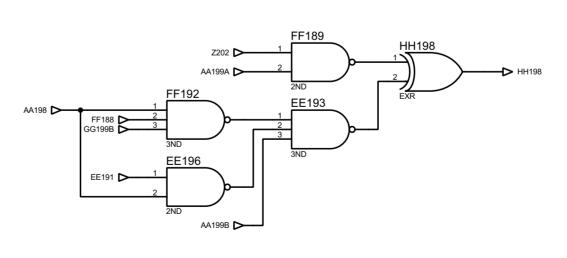




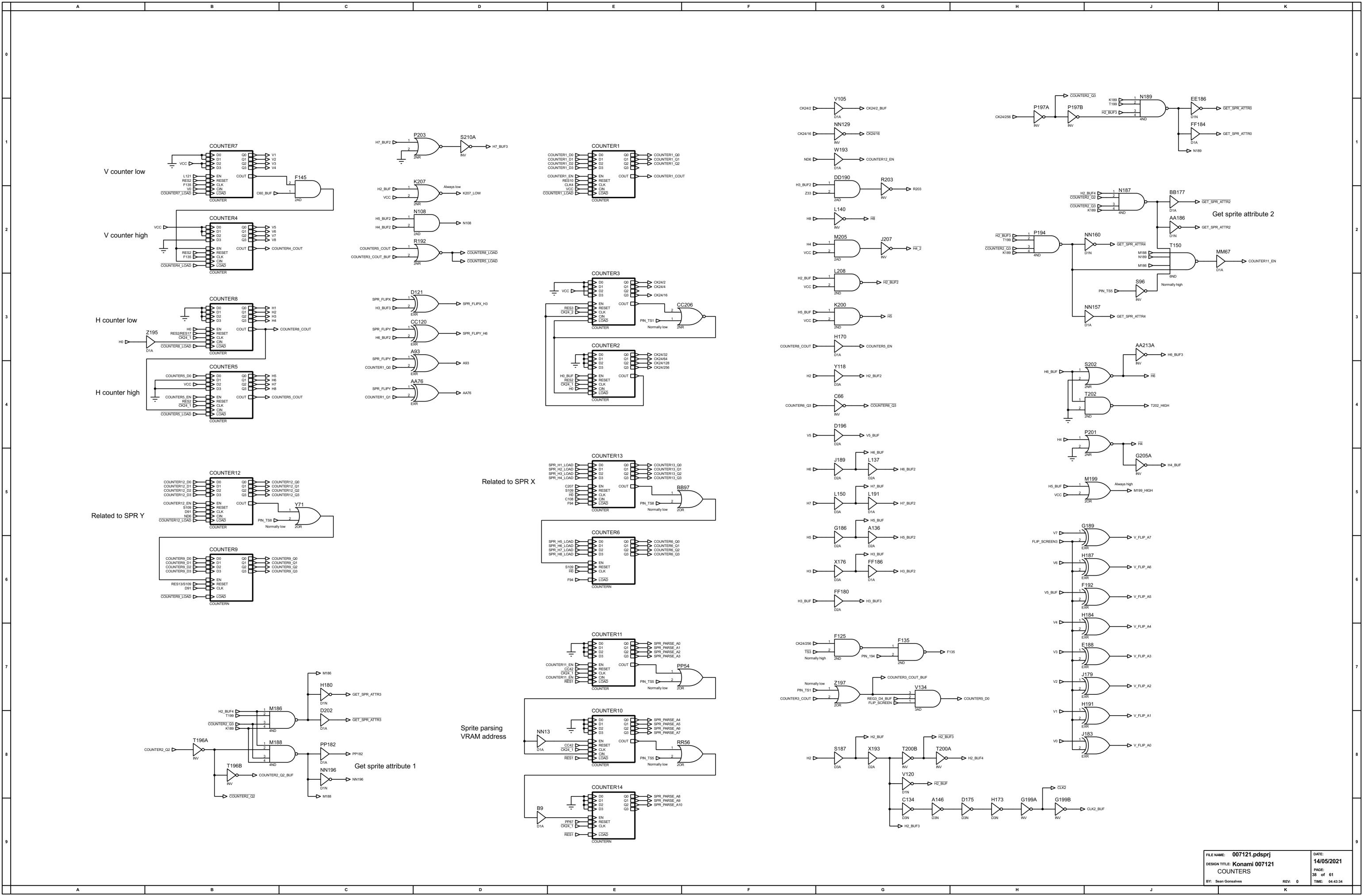
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DESIGN TITLE: Konami 007121			14/05/2021
SPR SIZE			PAGE: 36 of 61
BY: Sean Gonsalves	REV:	0	TIME: 04:43:34

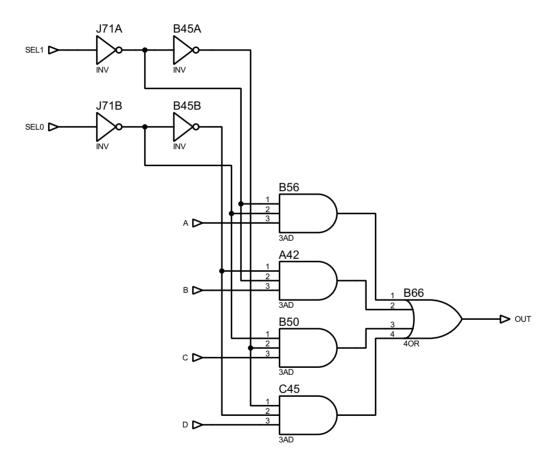




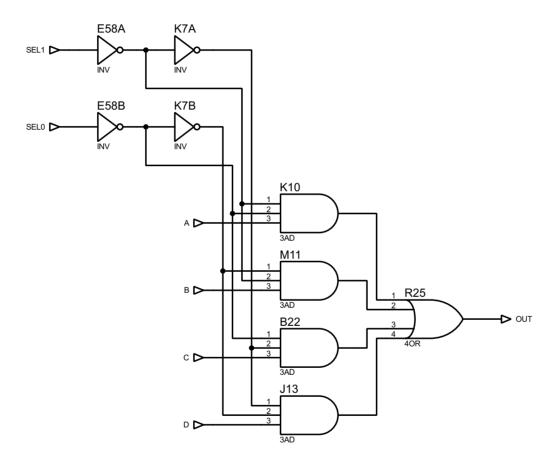


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DESIGN TITLE: Konami 007121			14/05/2021
SRAM ADDRESS			PAGE: 37 of 61
BY: Sean Gonsalves	REV:	0	

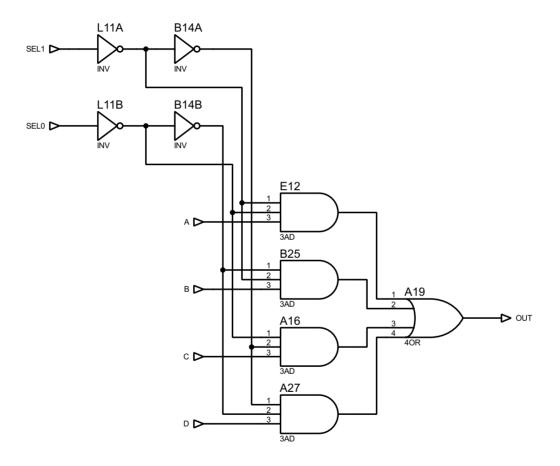




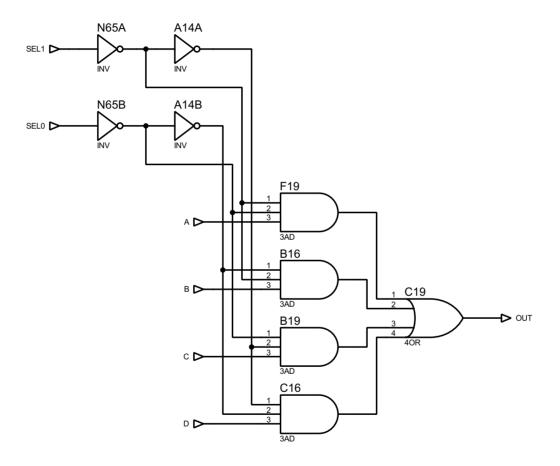
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			PAGE: 39 of 61
BY: Sean Gonsalves	REV:	0	



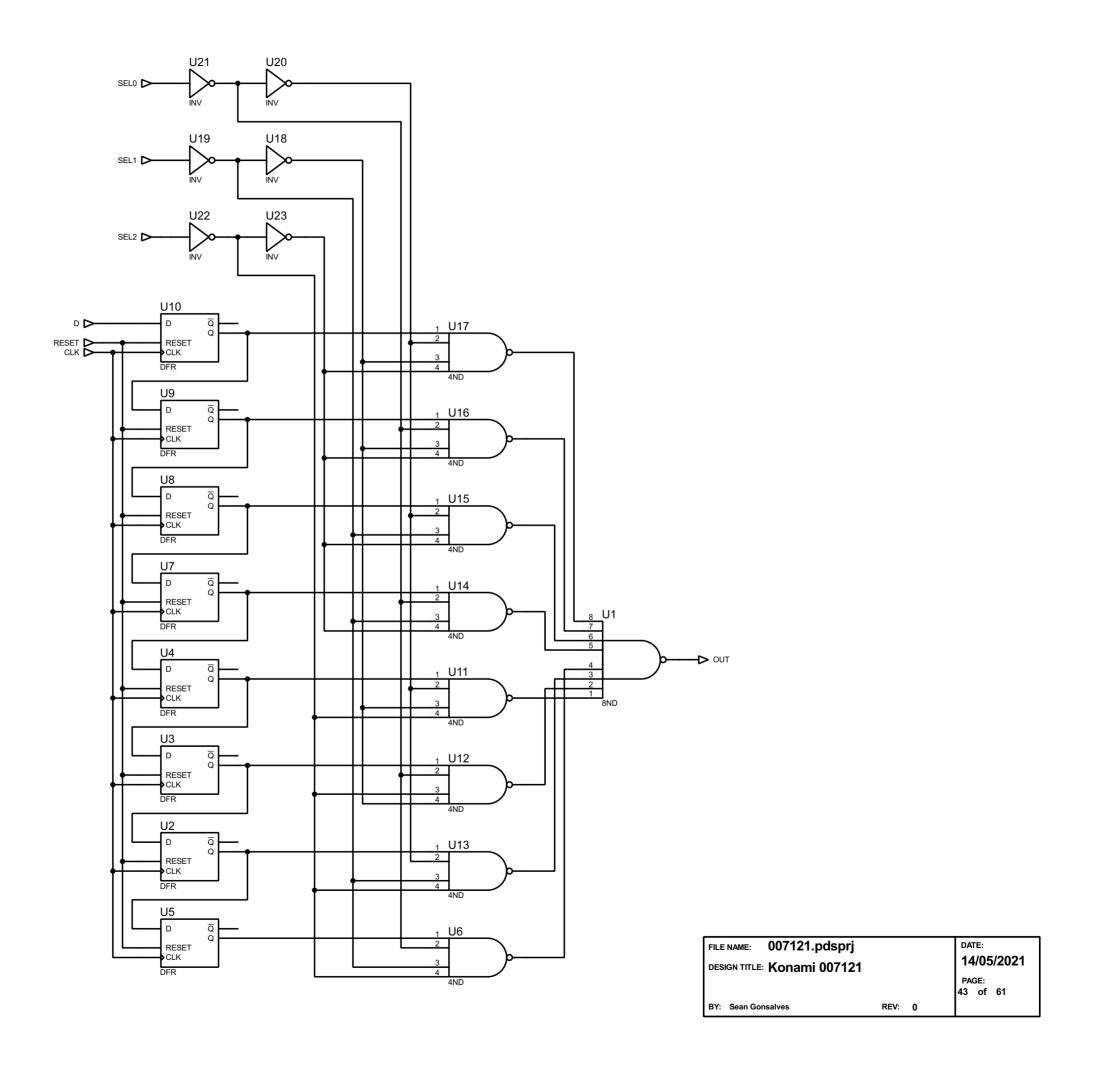
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			PAGE: 40 of 61
BY: Sean Gonsalves	REV:	0	

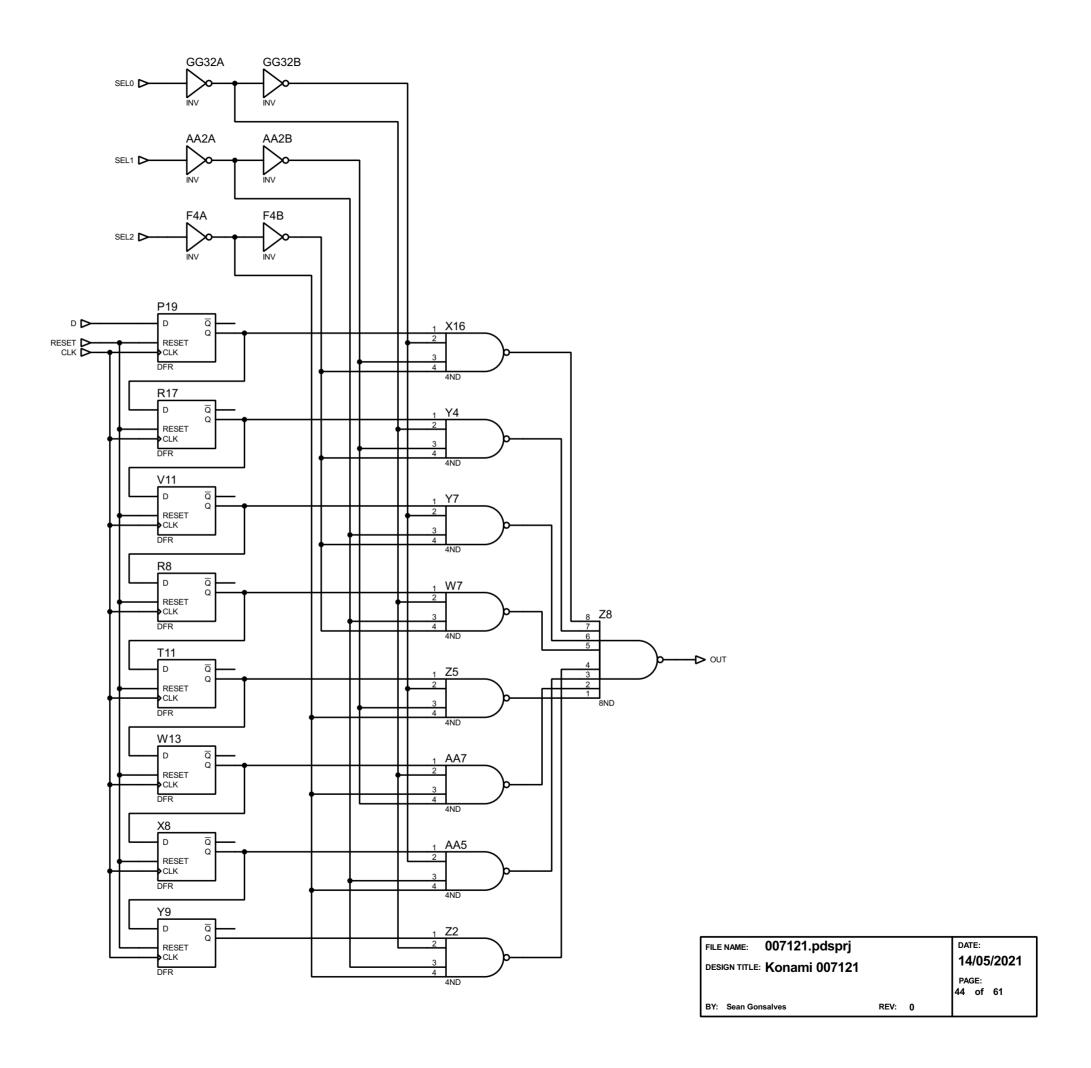


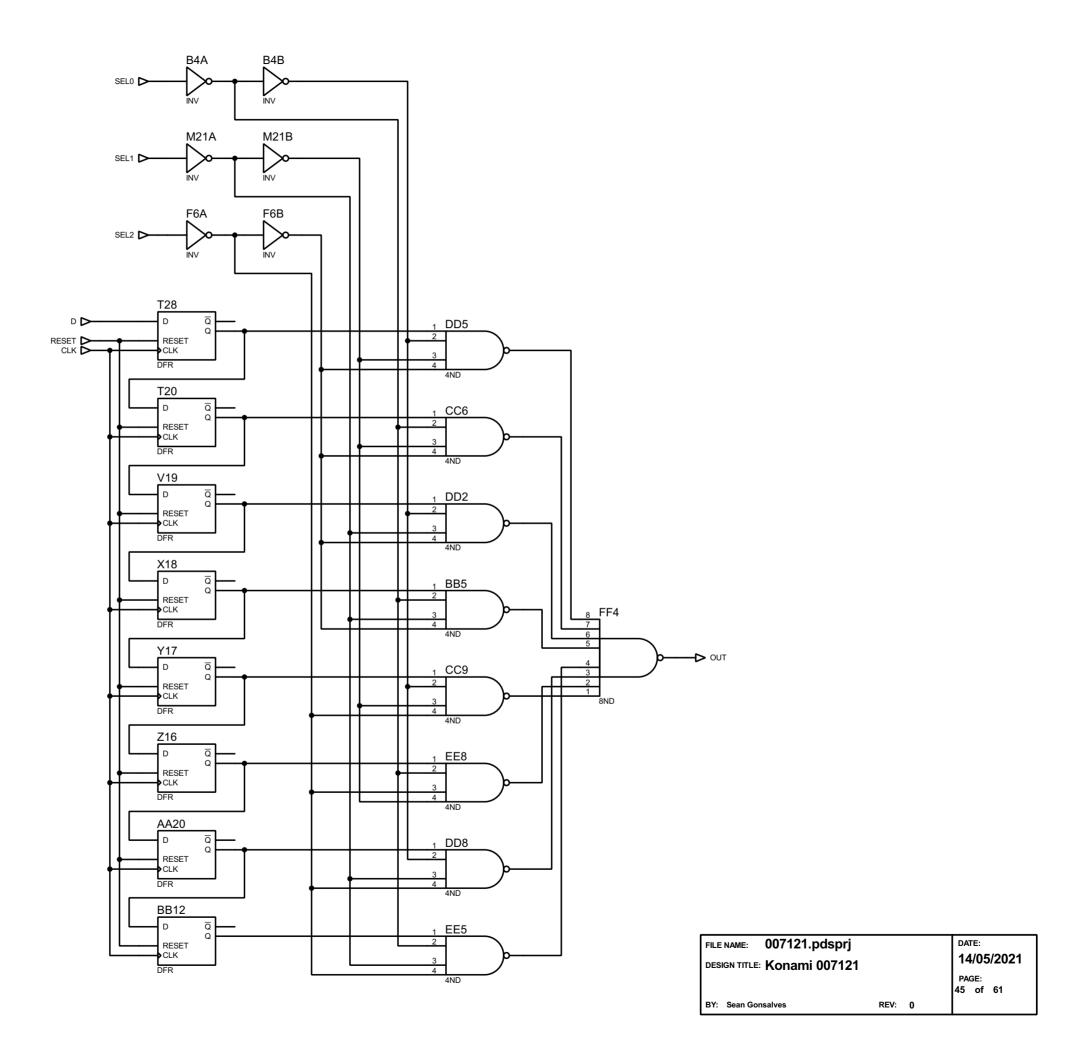
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			PAGE: 41 of 61
BY: Sean Gonsalves	REV:	0	

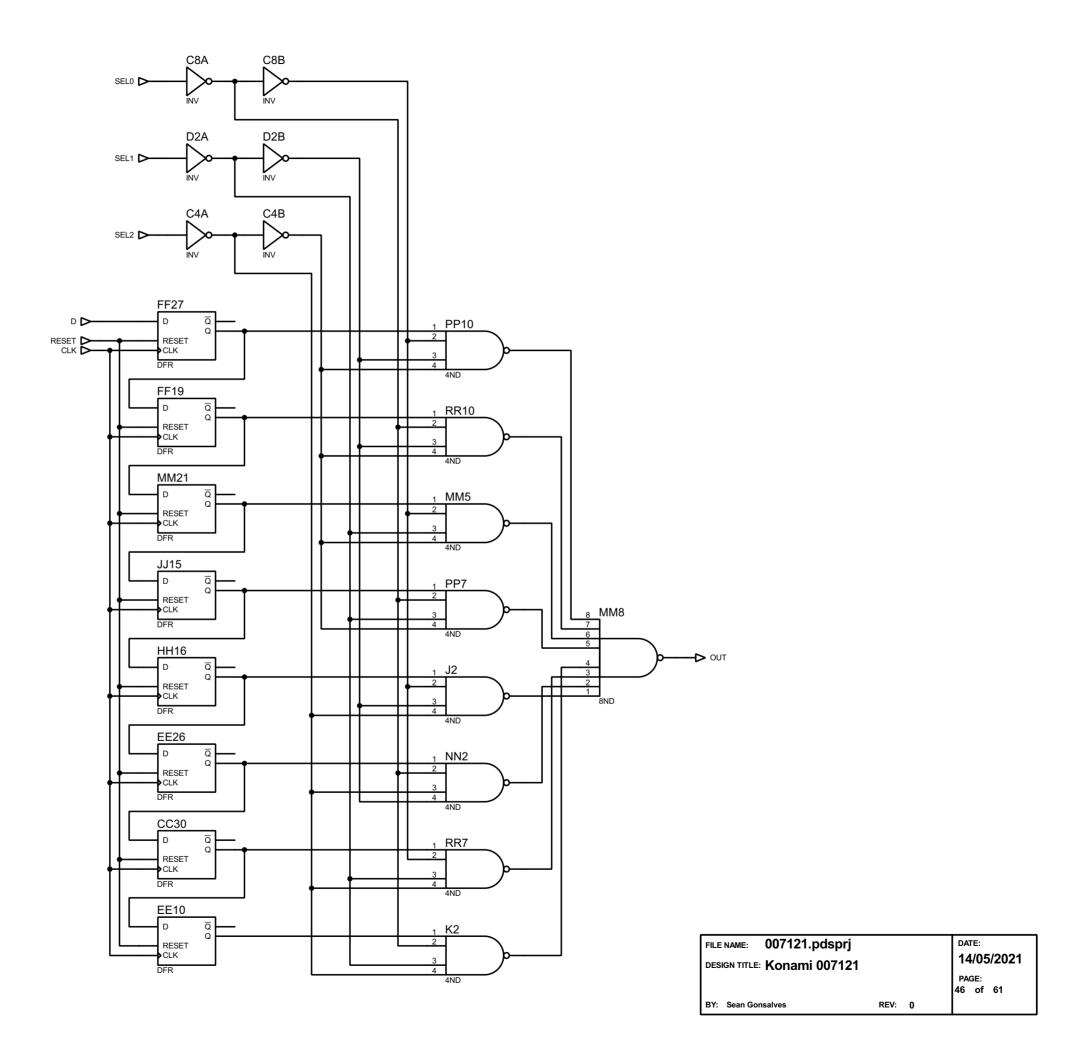


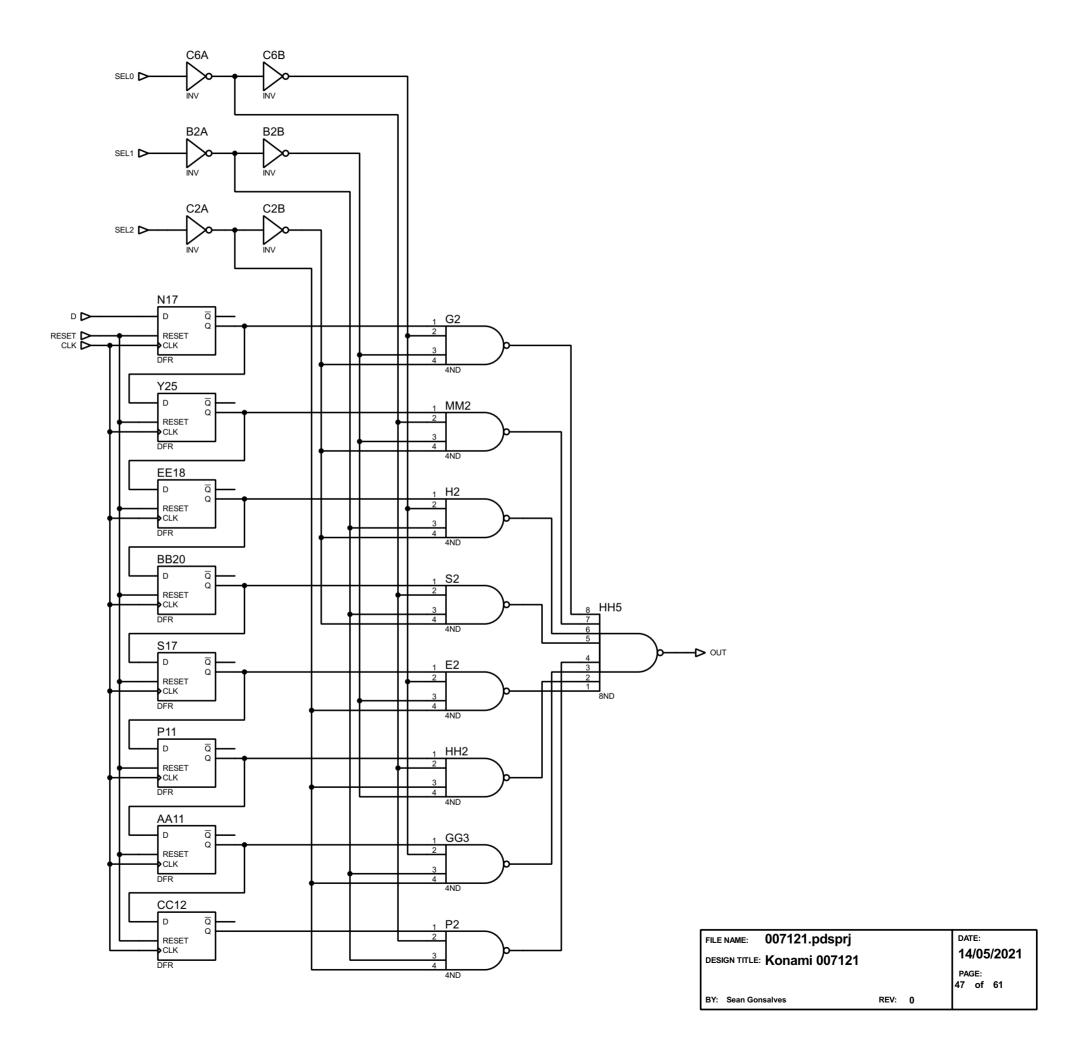
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			PAGE: 42 of 61
BY: Sean Gonsalves	REV:	0	

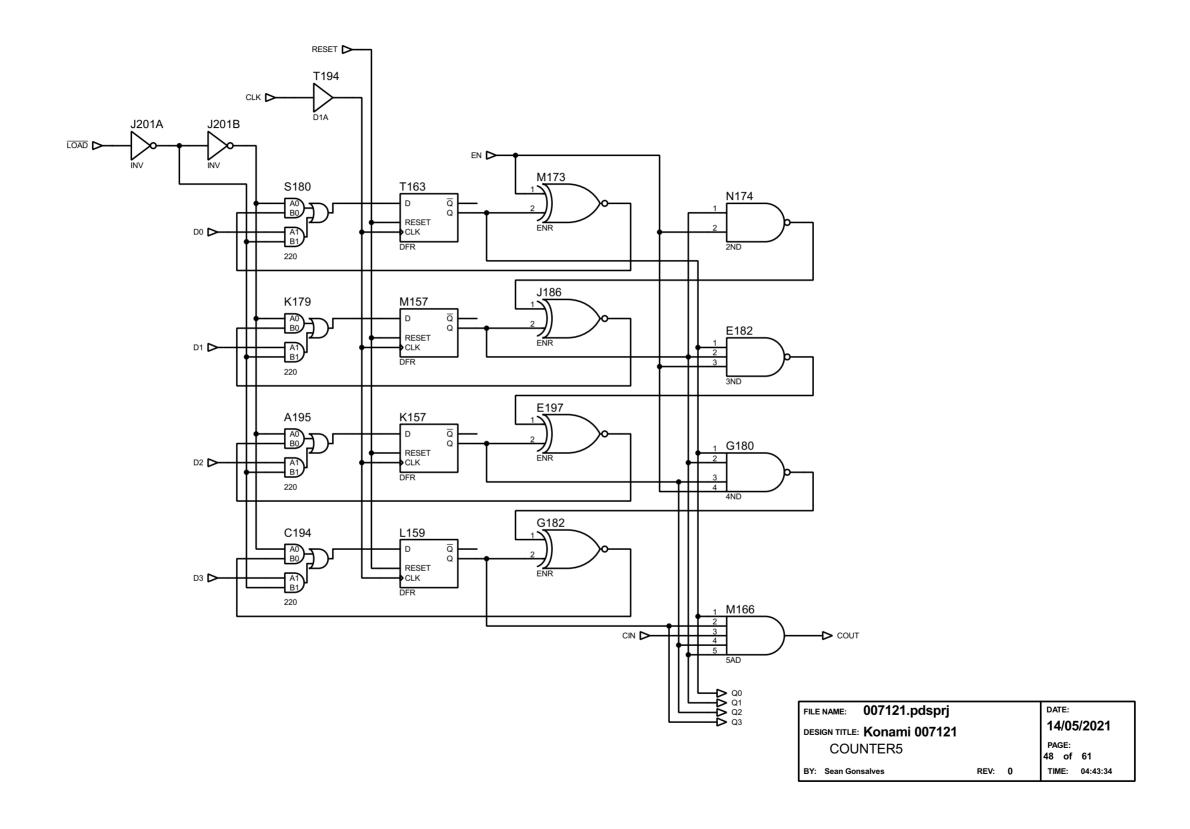


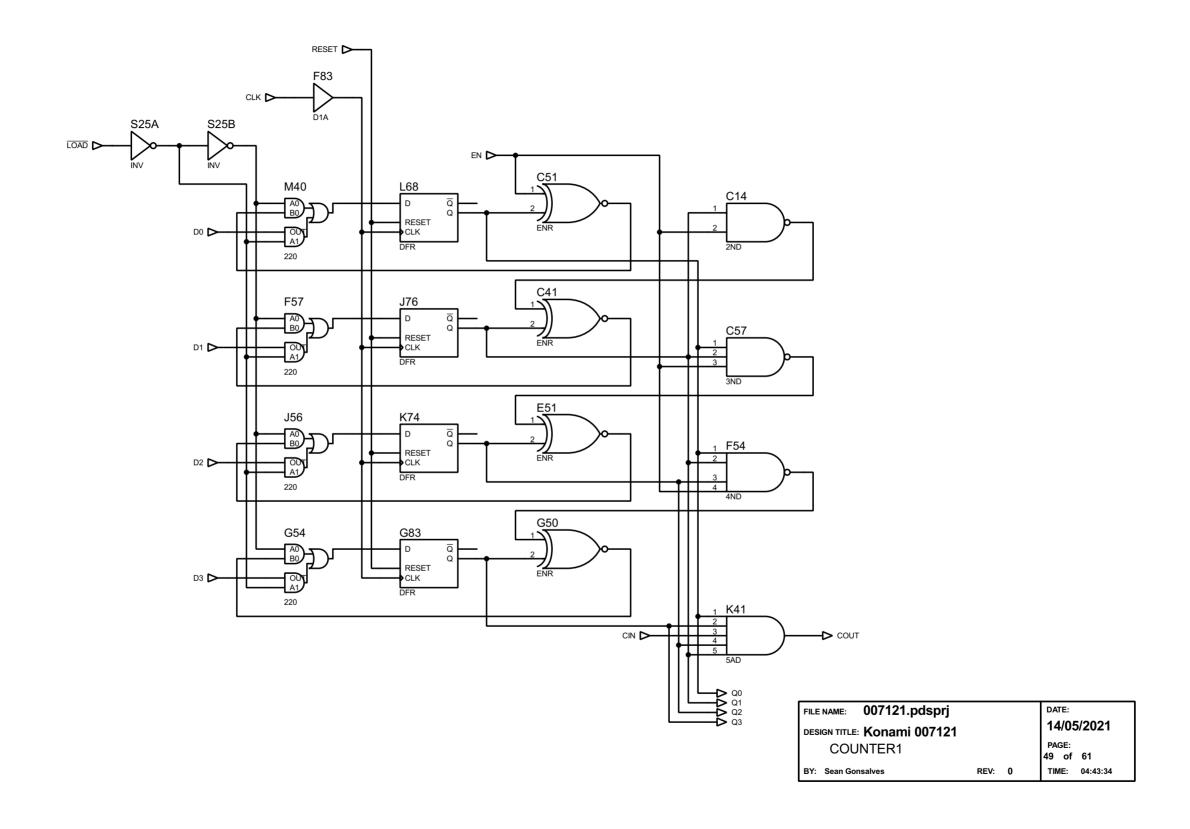


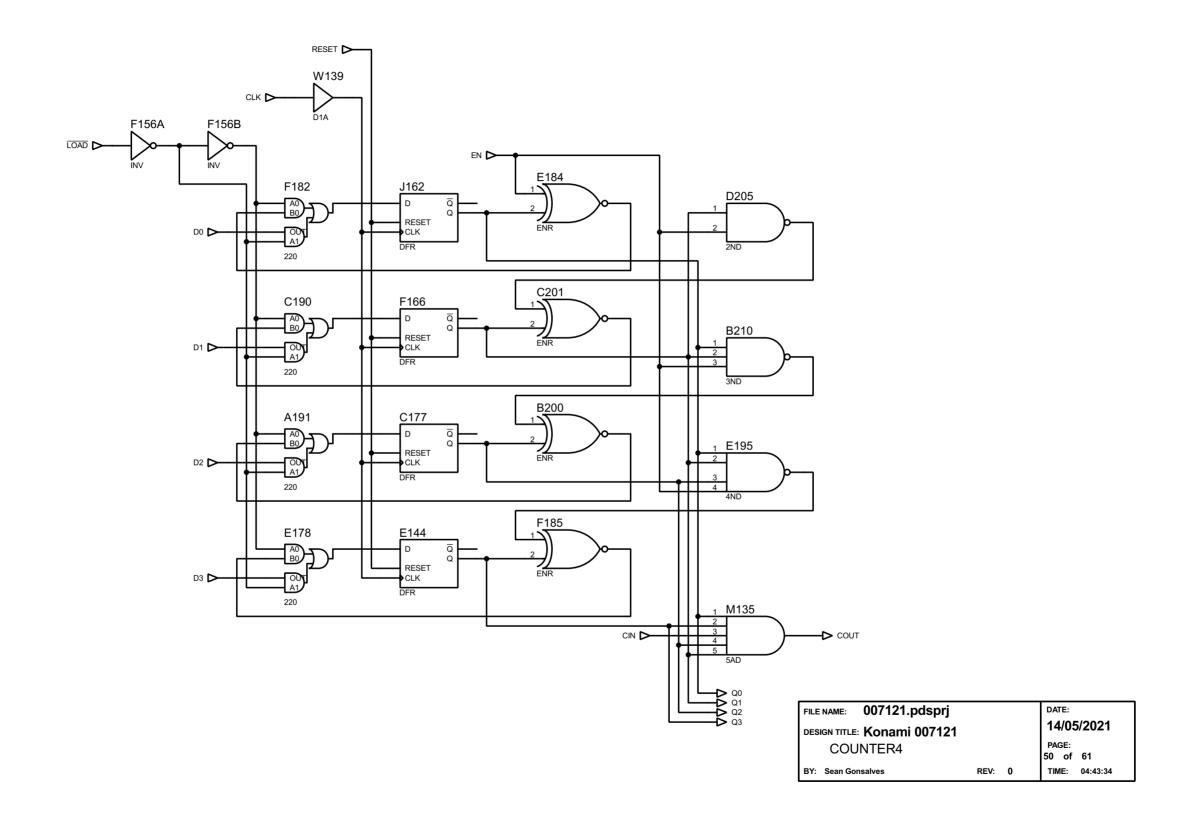


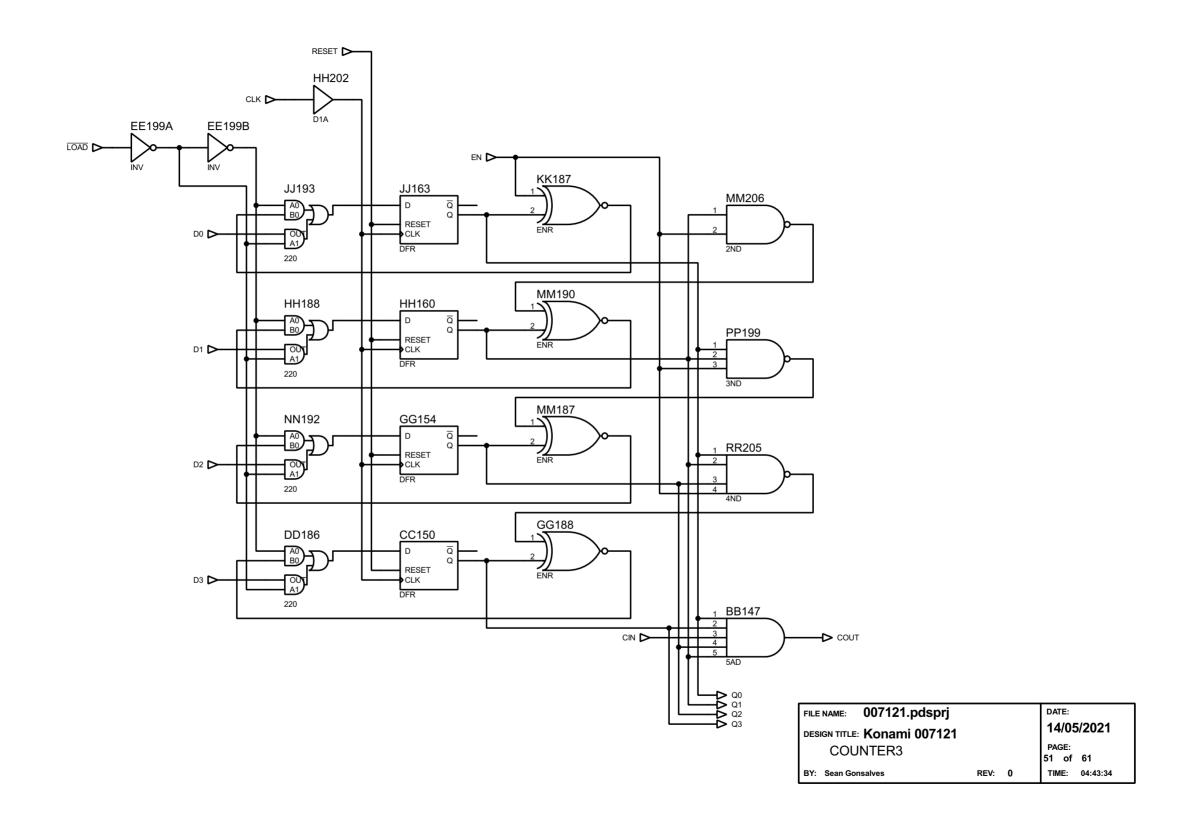


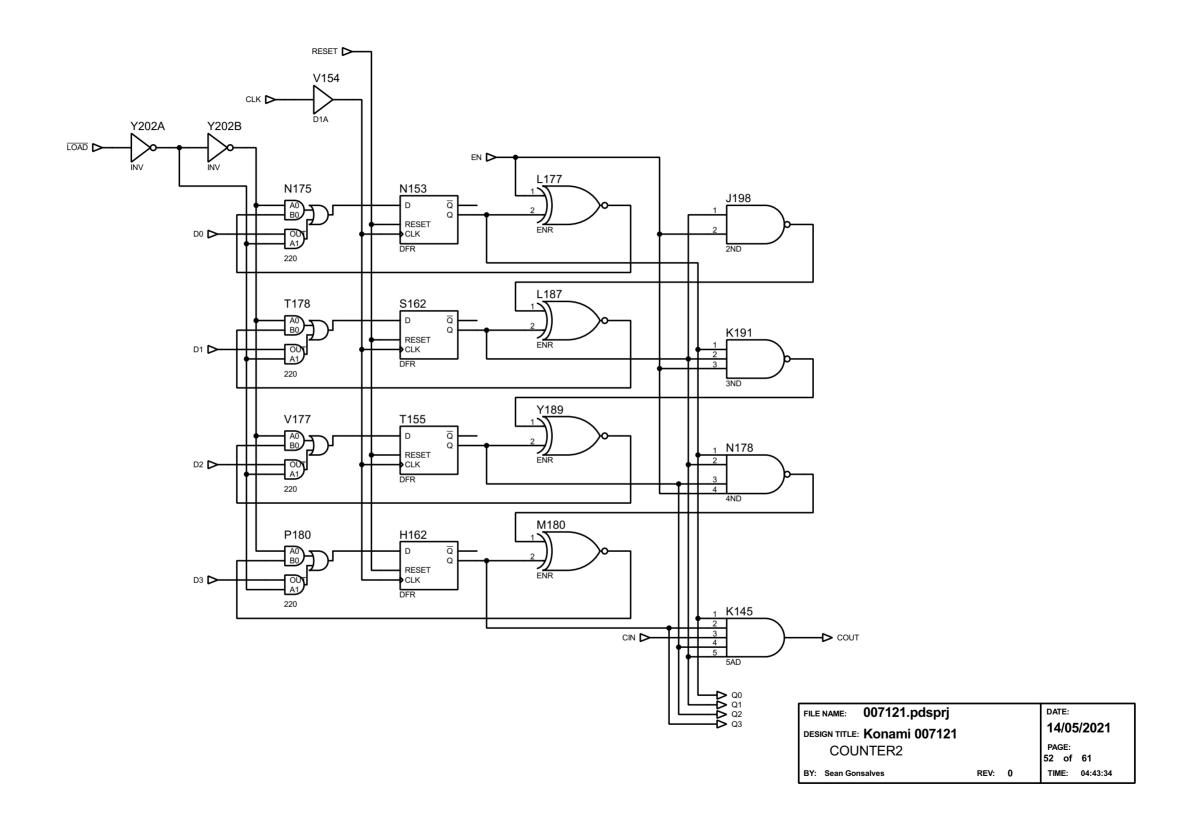


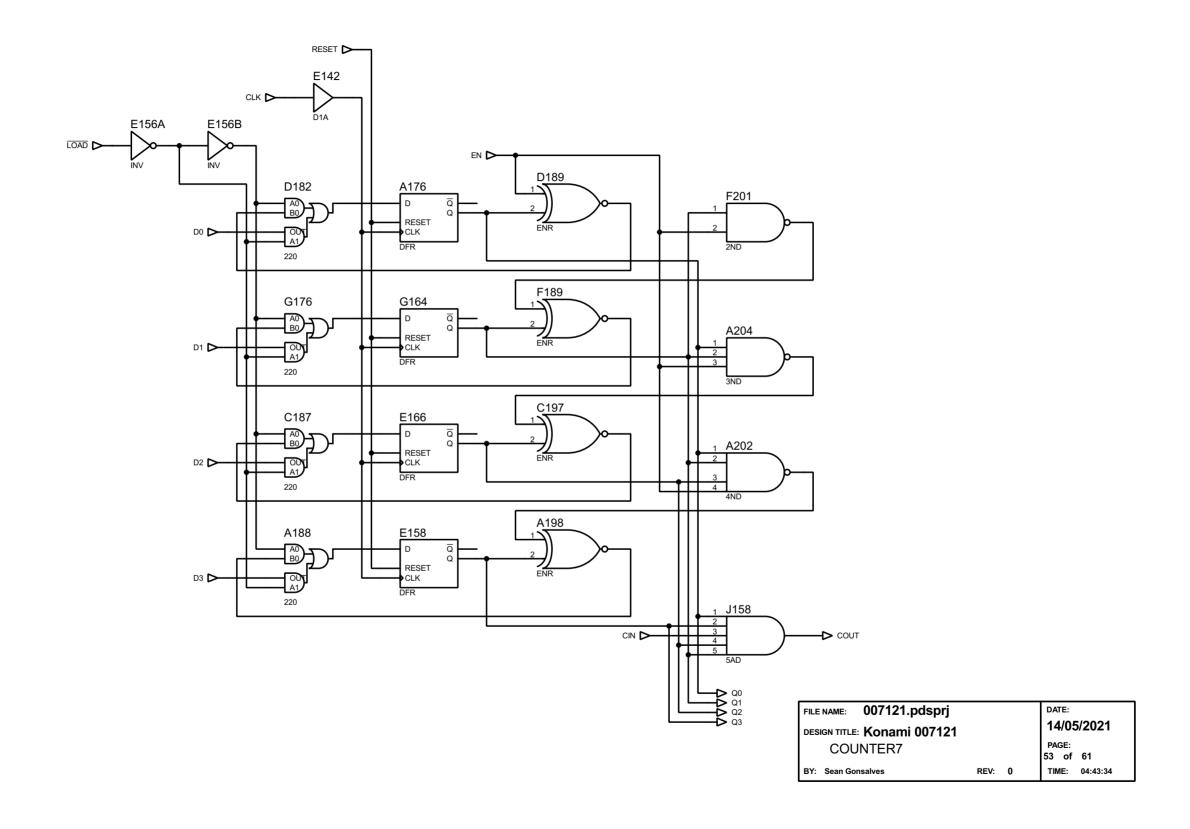


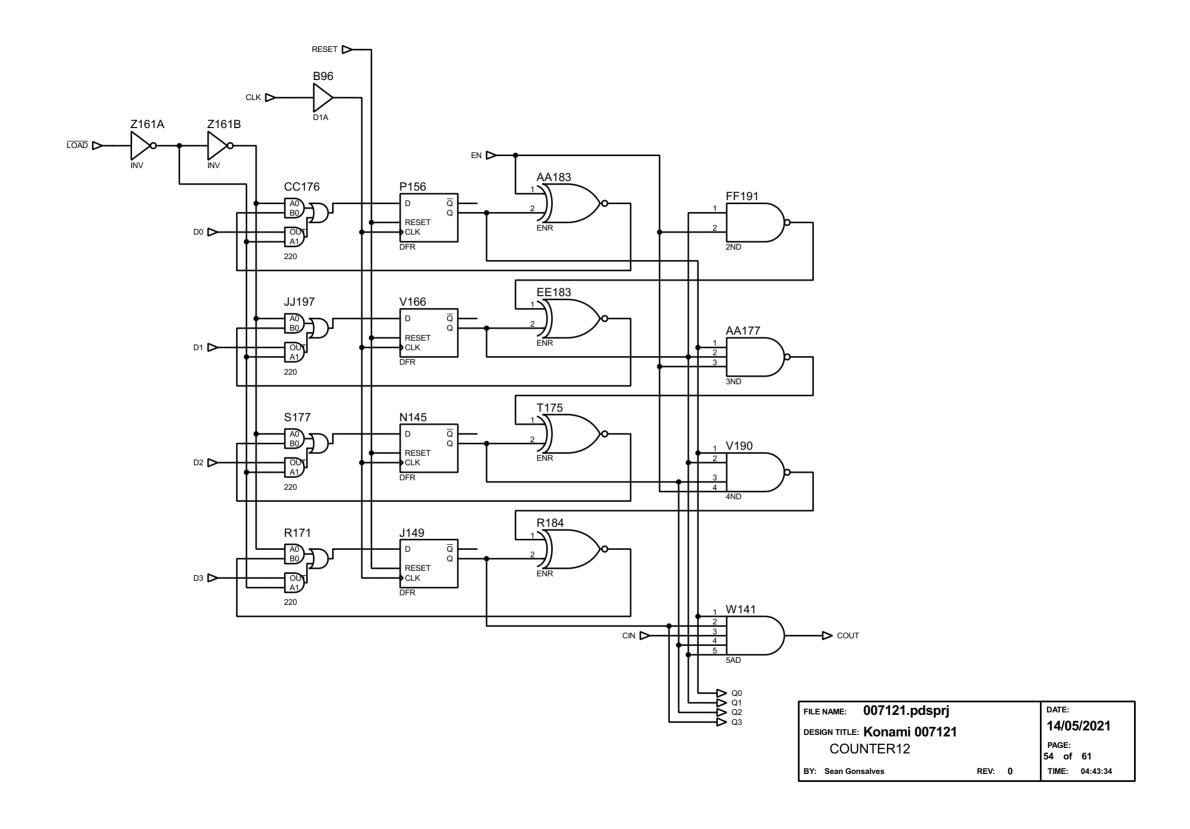


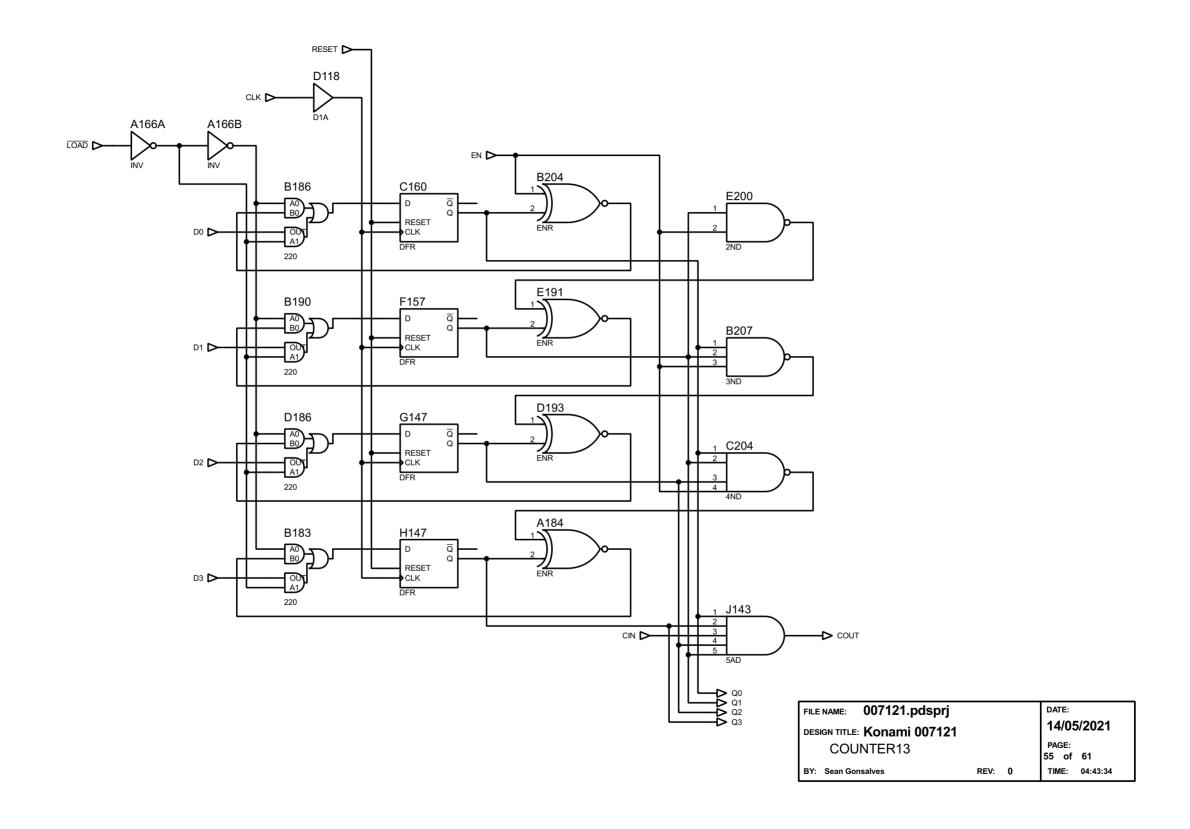


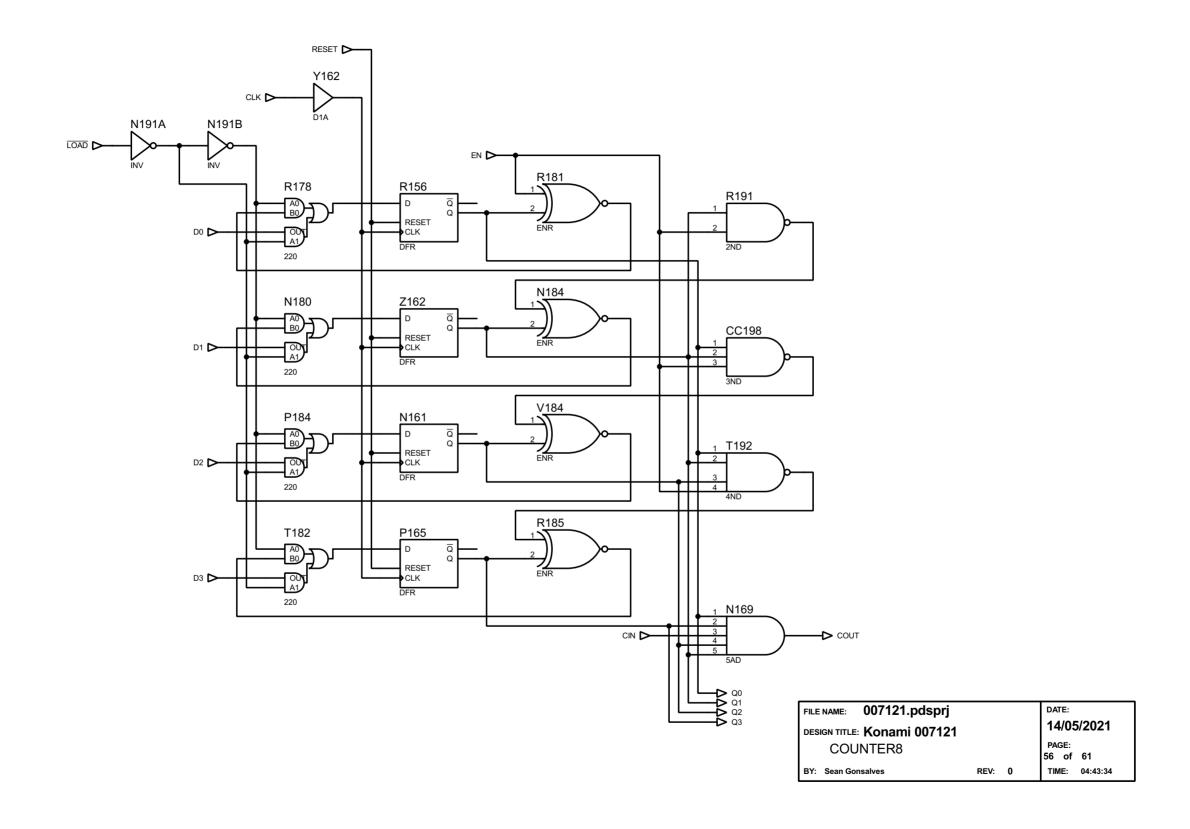


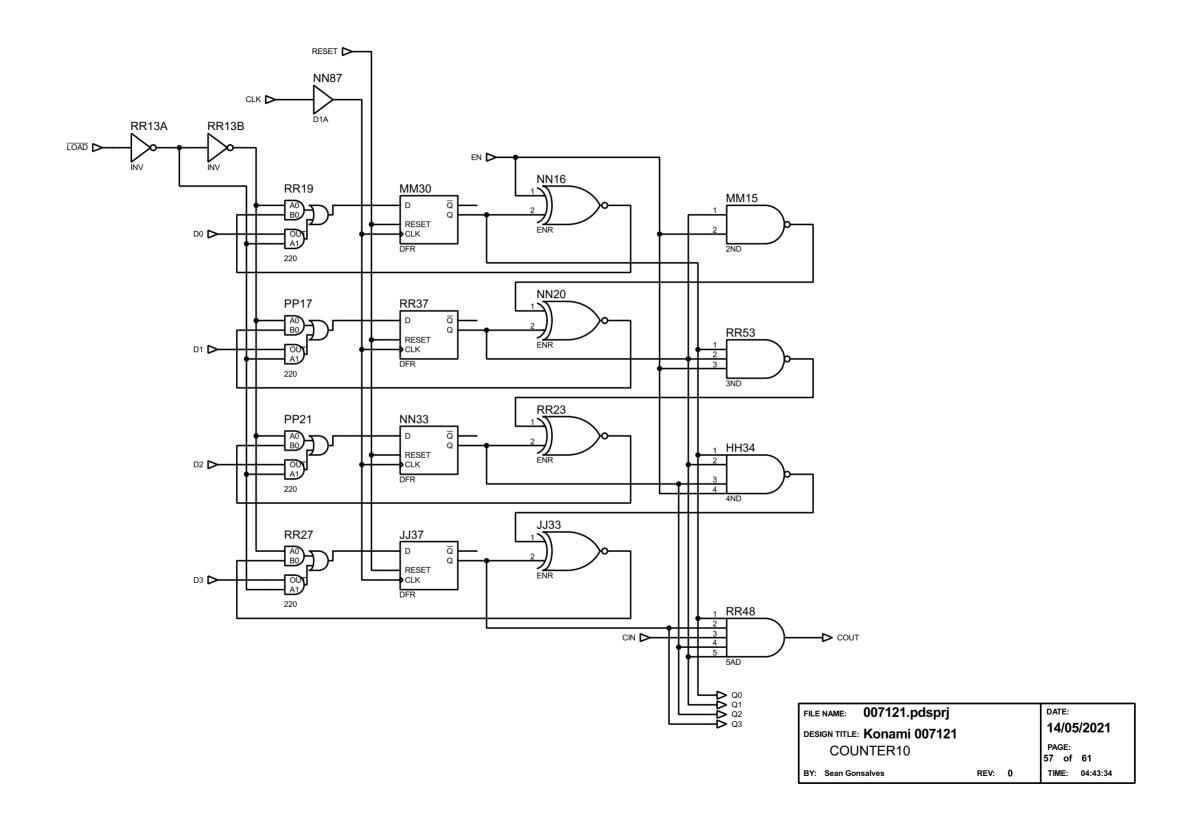


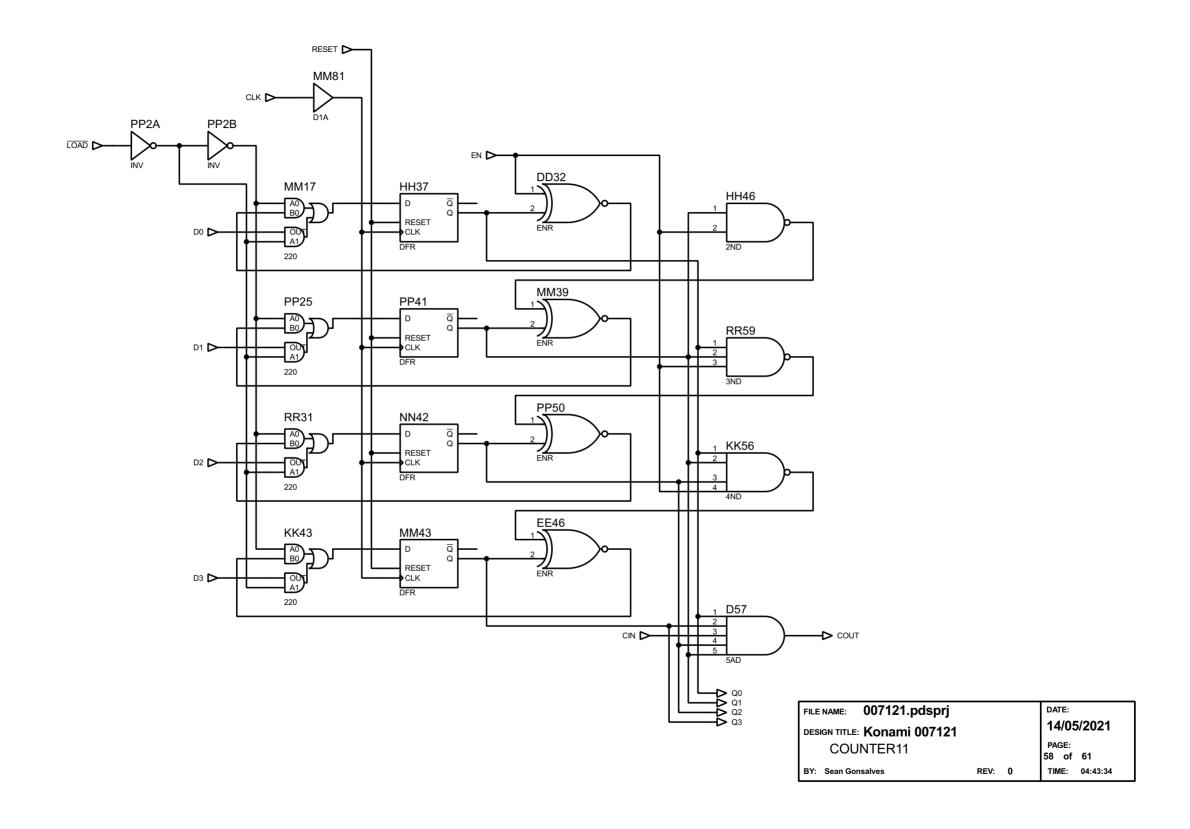


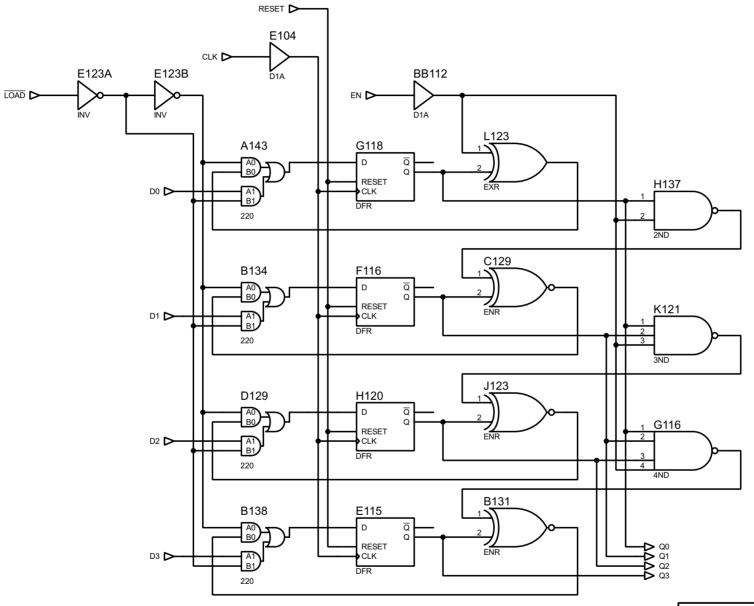


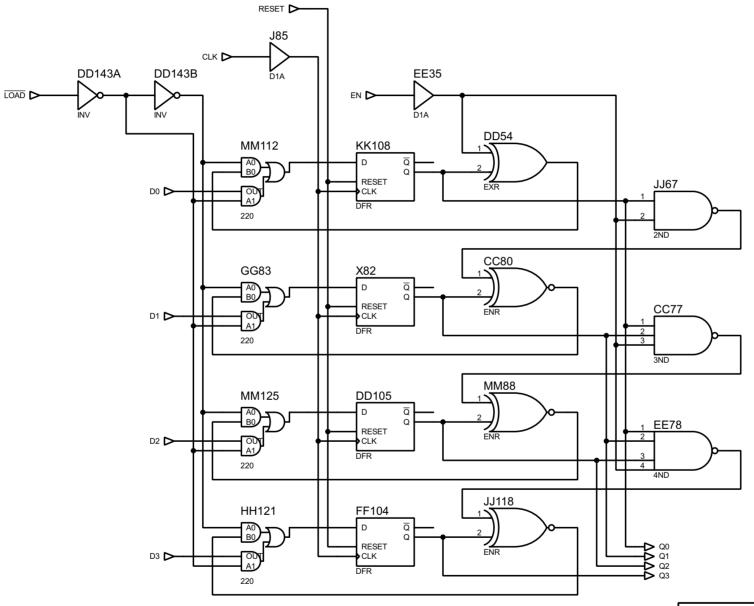




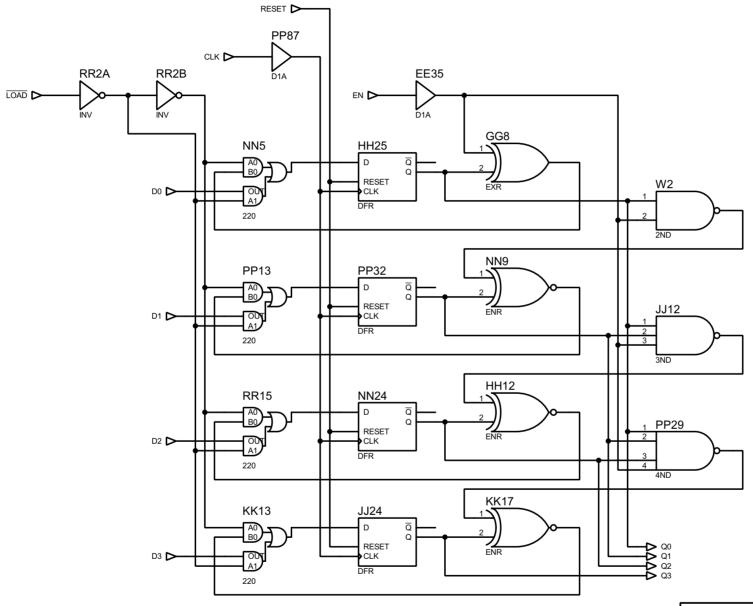






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