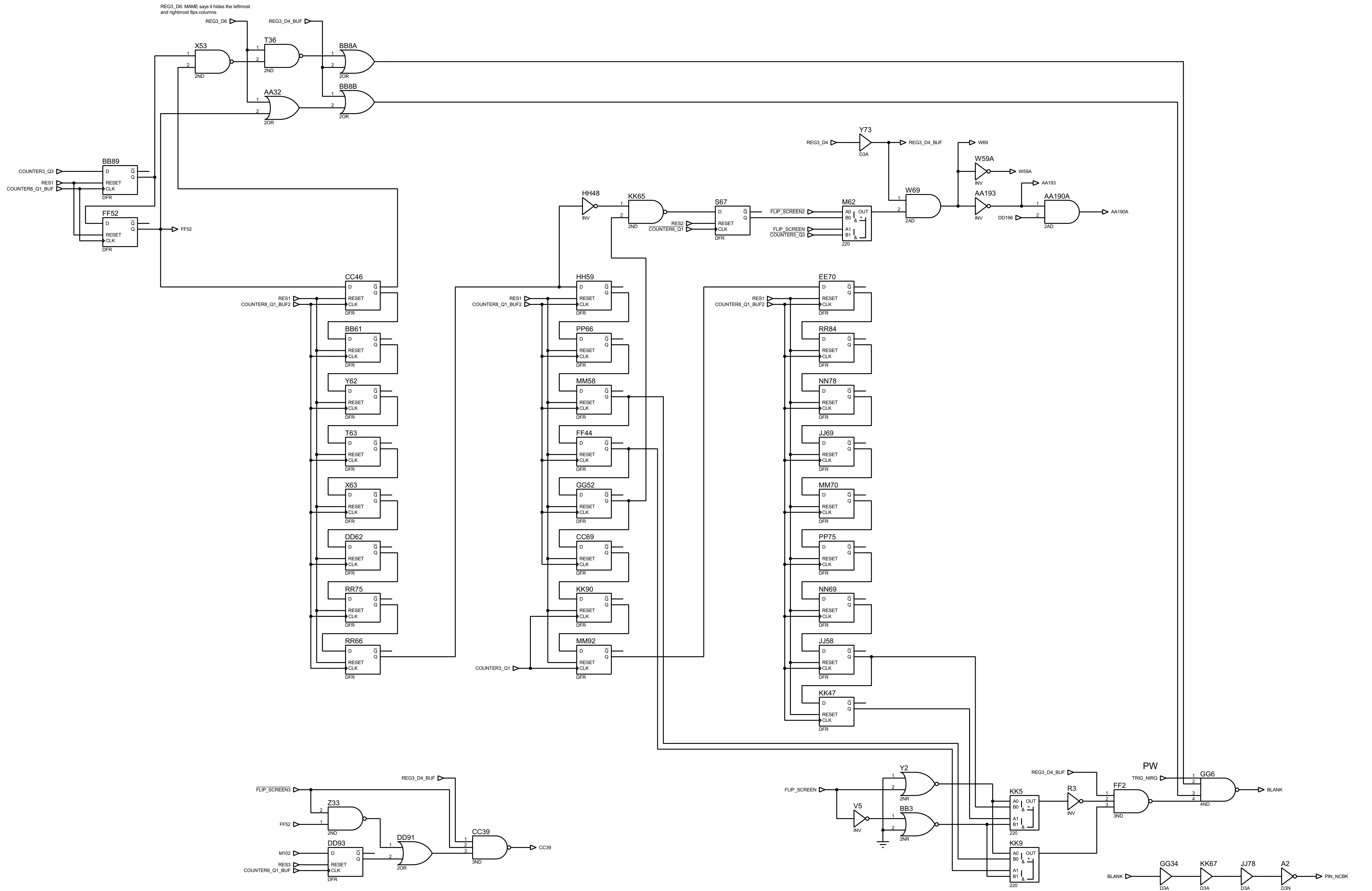
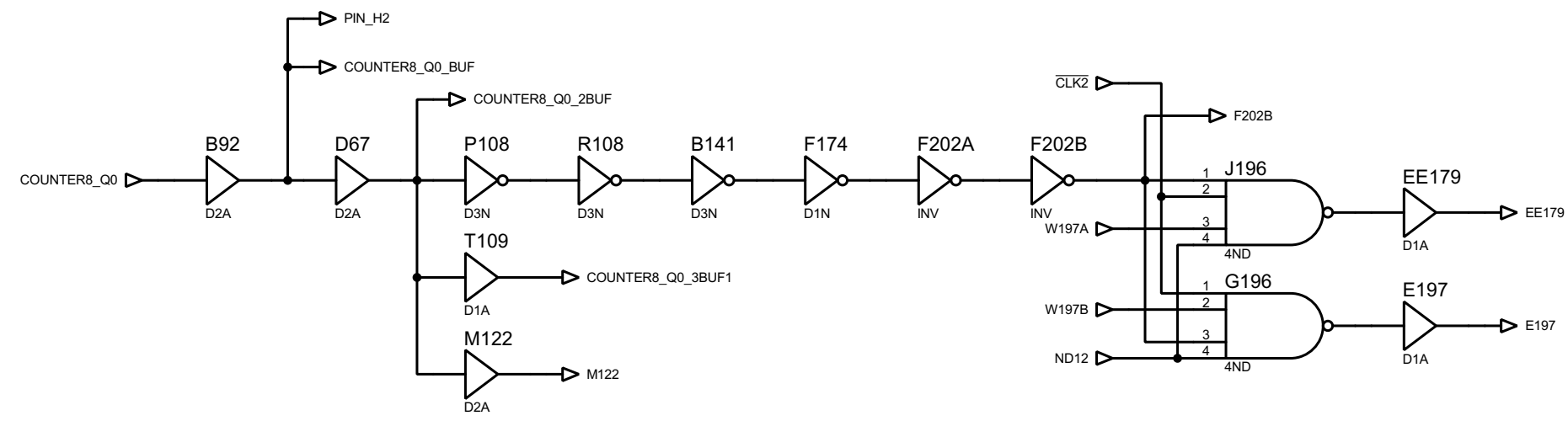
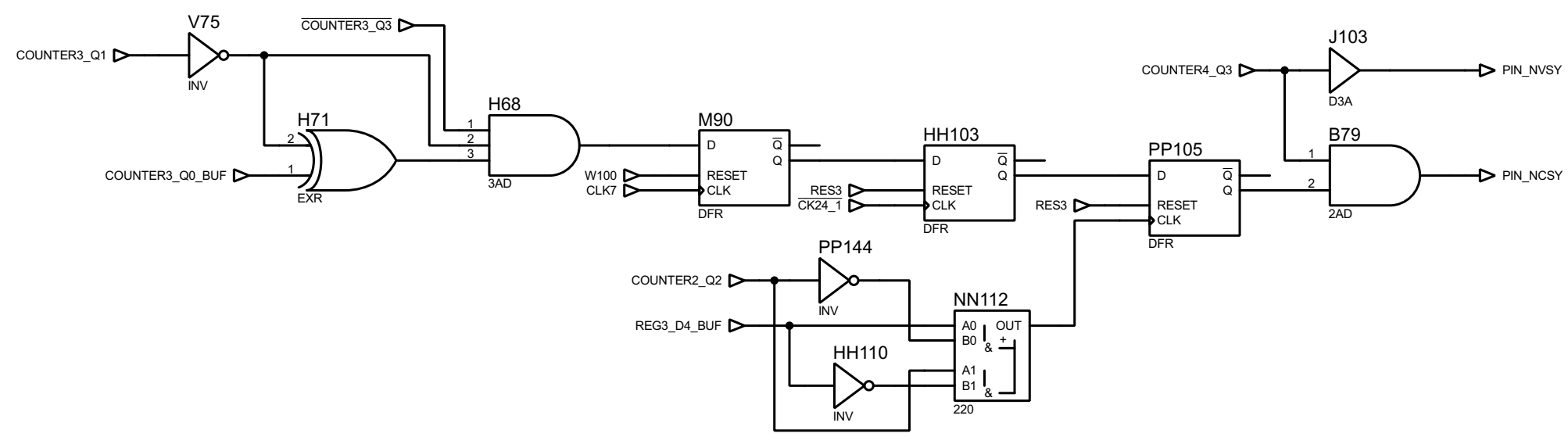


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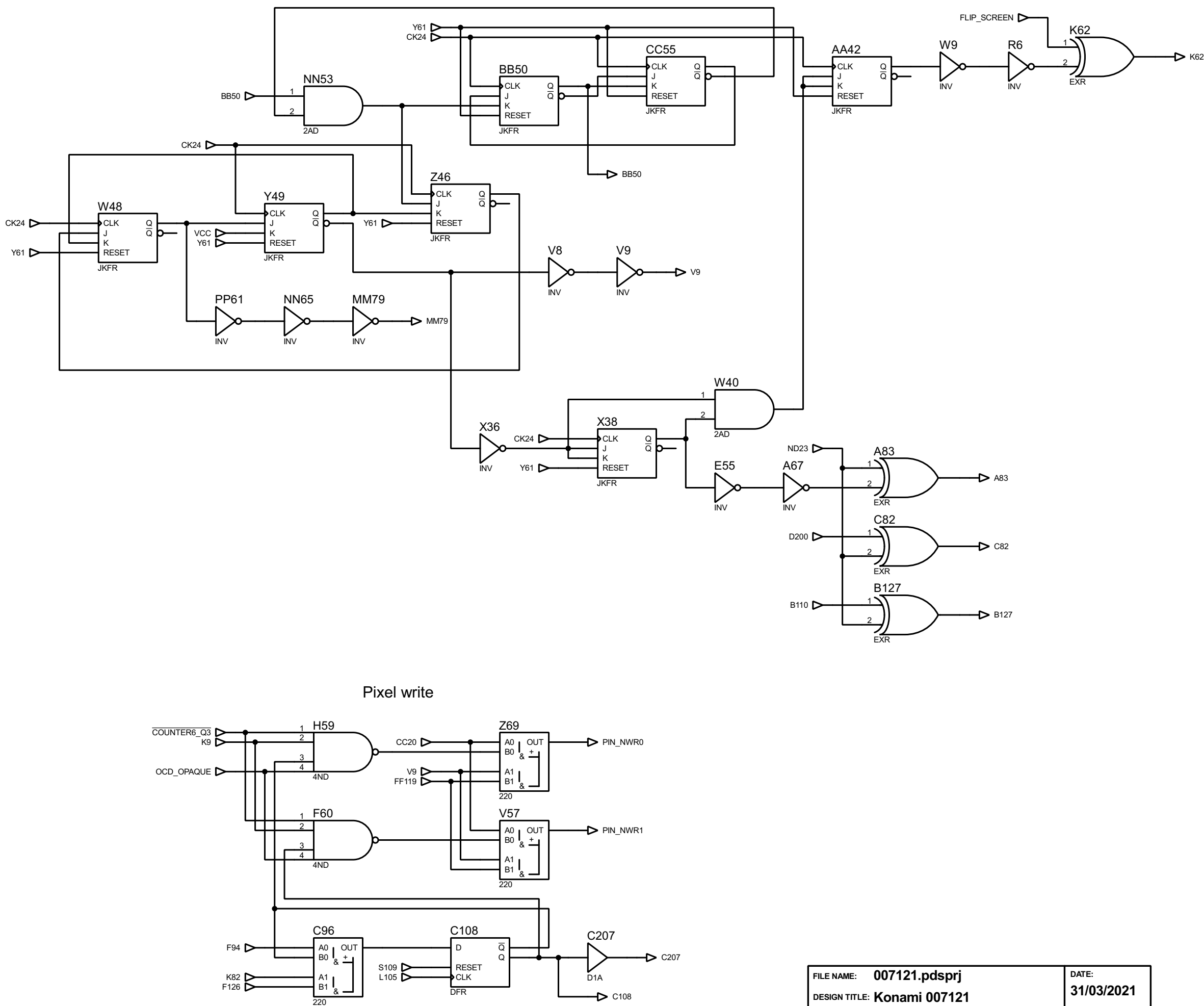


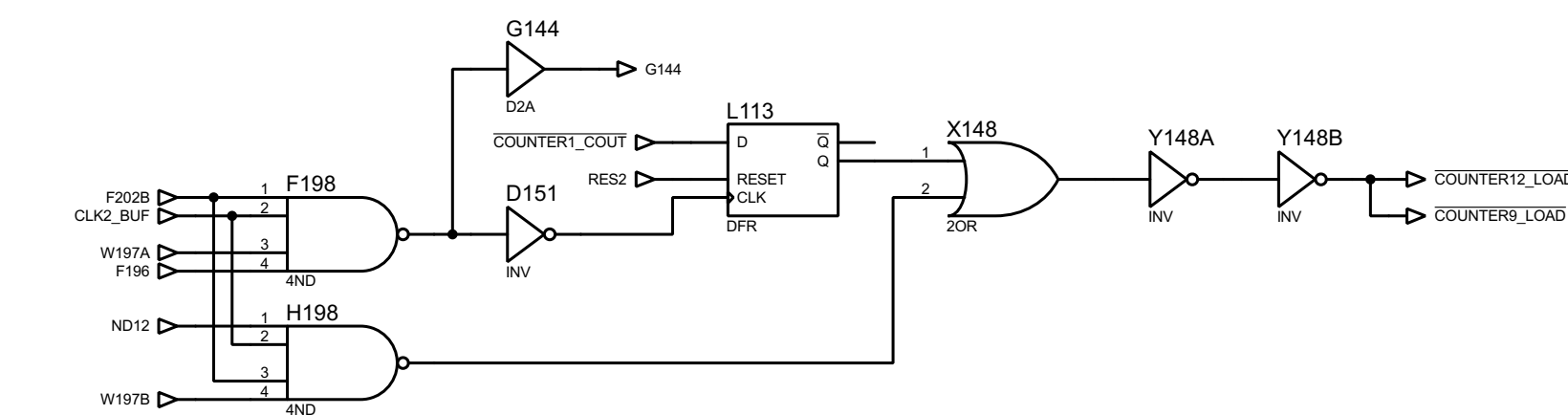
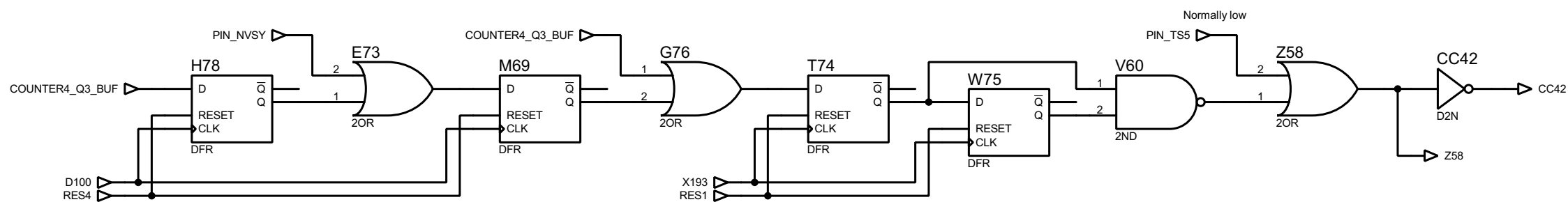
T109 or M112 must be an inverter ?

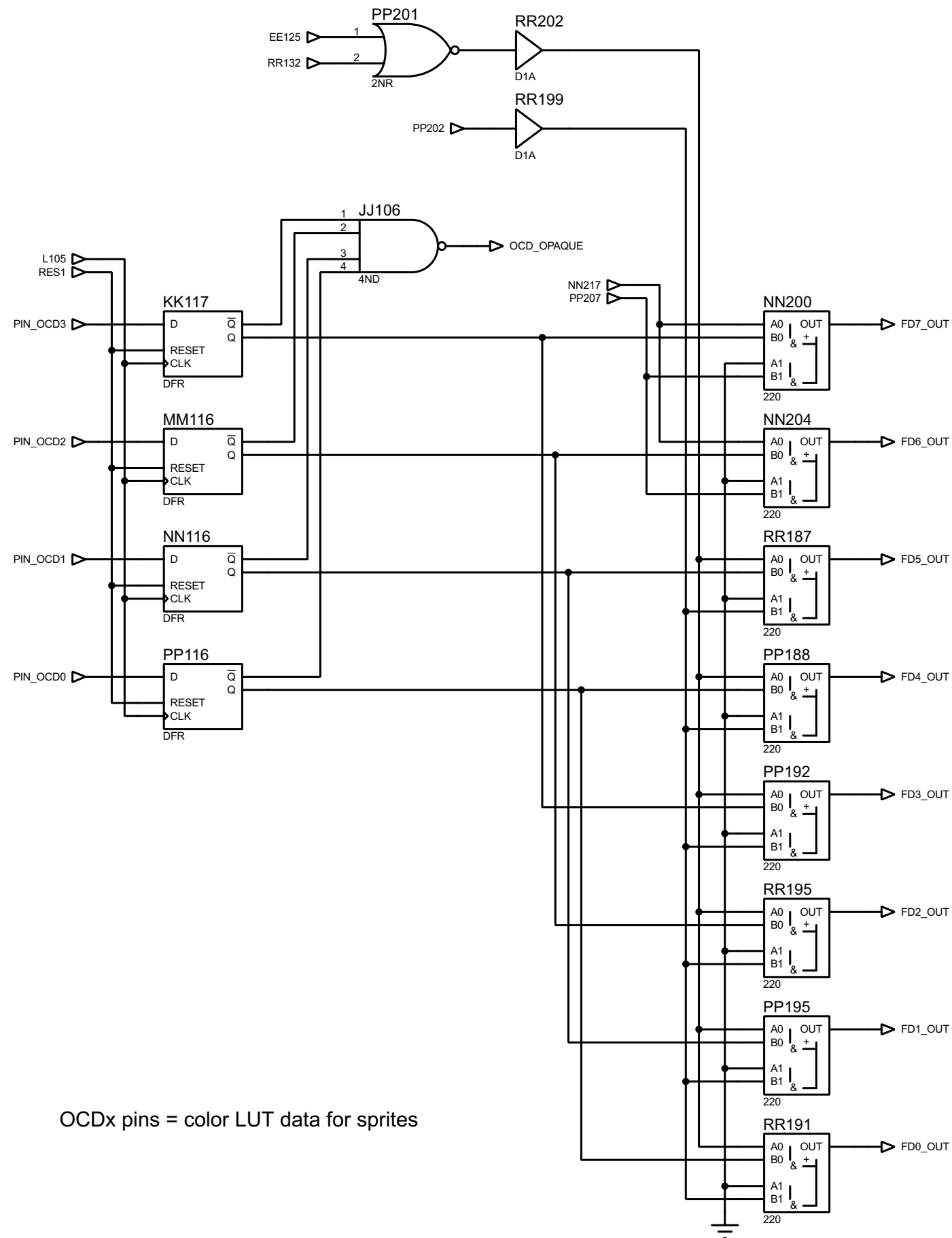


Polarity select

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OCDx pins = color LUT data for sprites

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REV: 0	

MAME says (video/k007121.cpp):

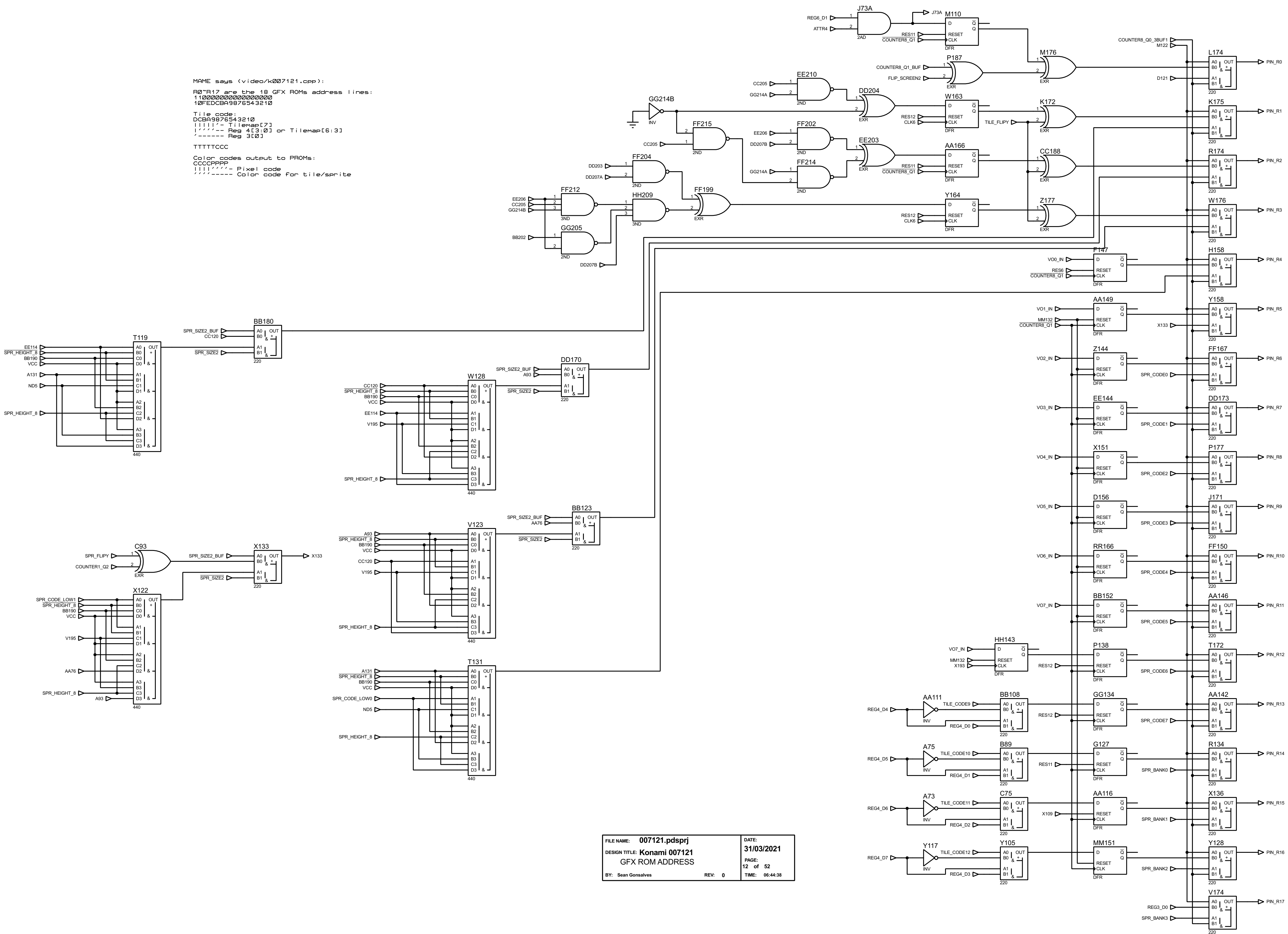
R0~R17 are the 18 GFX ROMs address lines:
1100000000000000
10FEDCBA9876543210

Tile code:
DCBA9876543210
| | | | | Tilemap[2]
| | | | | Reg 4[3:0] or Tilemap[6:3]
| | | | | Reg 3[0]

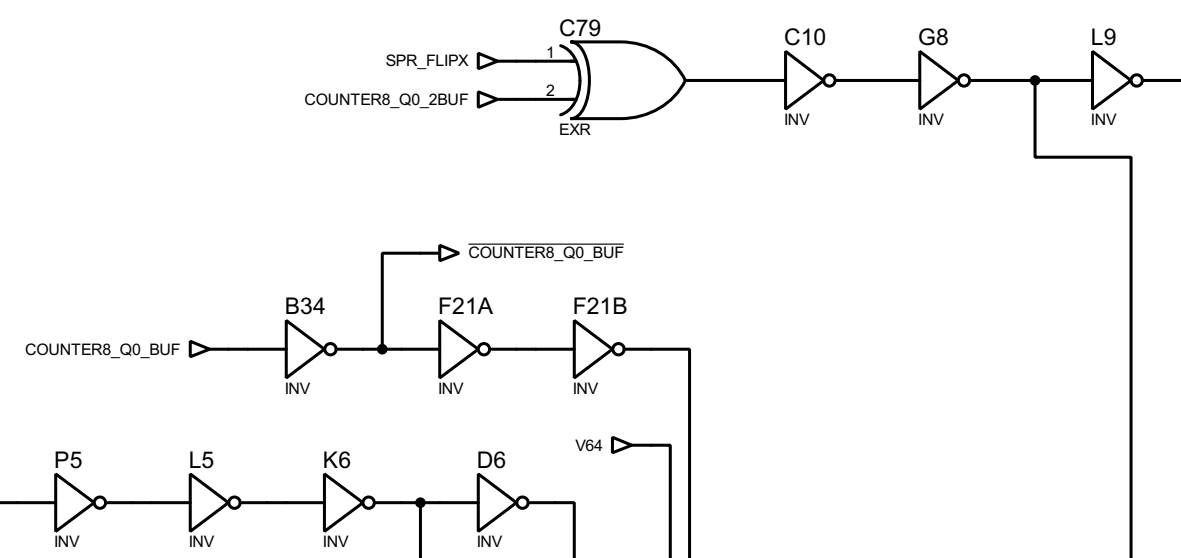
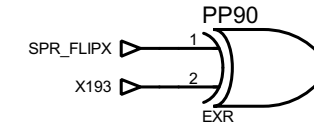
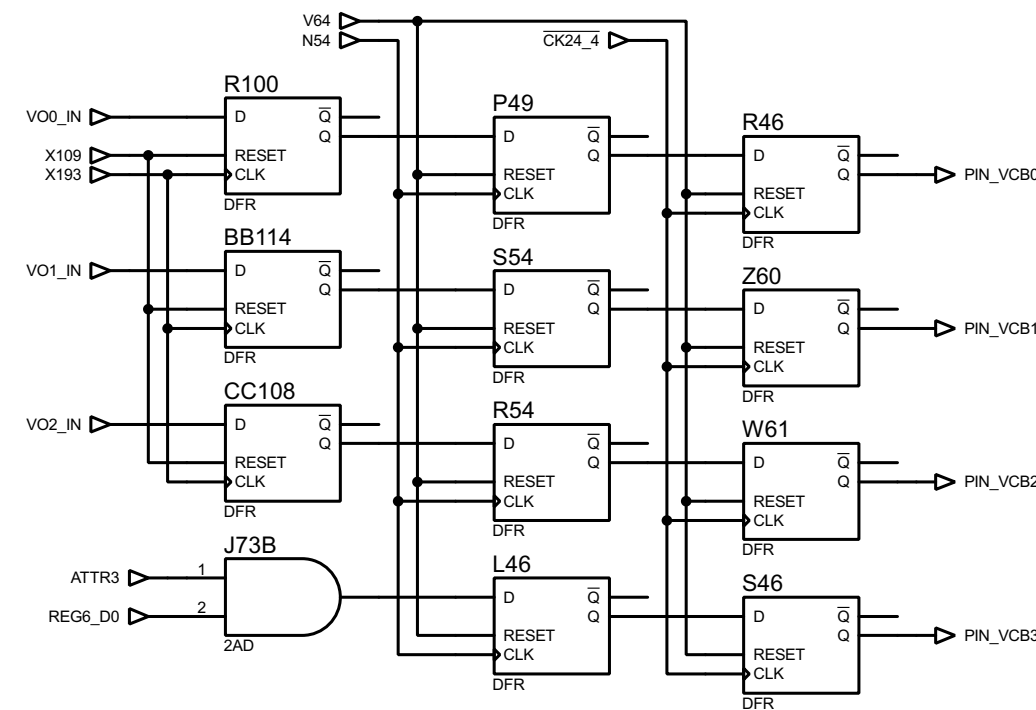
TTTTTCCC

Color codes output to PROMs:
CCCCPPPP

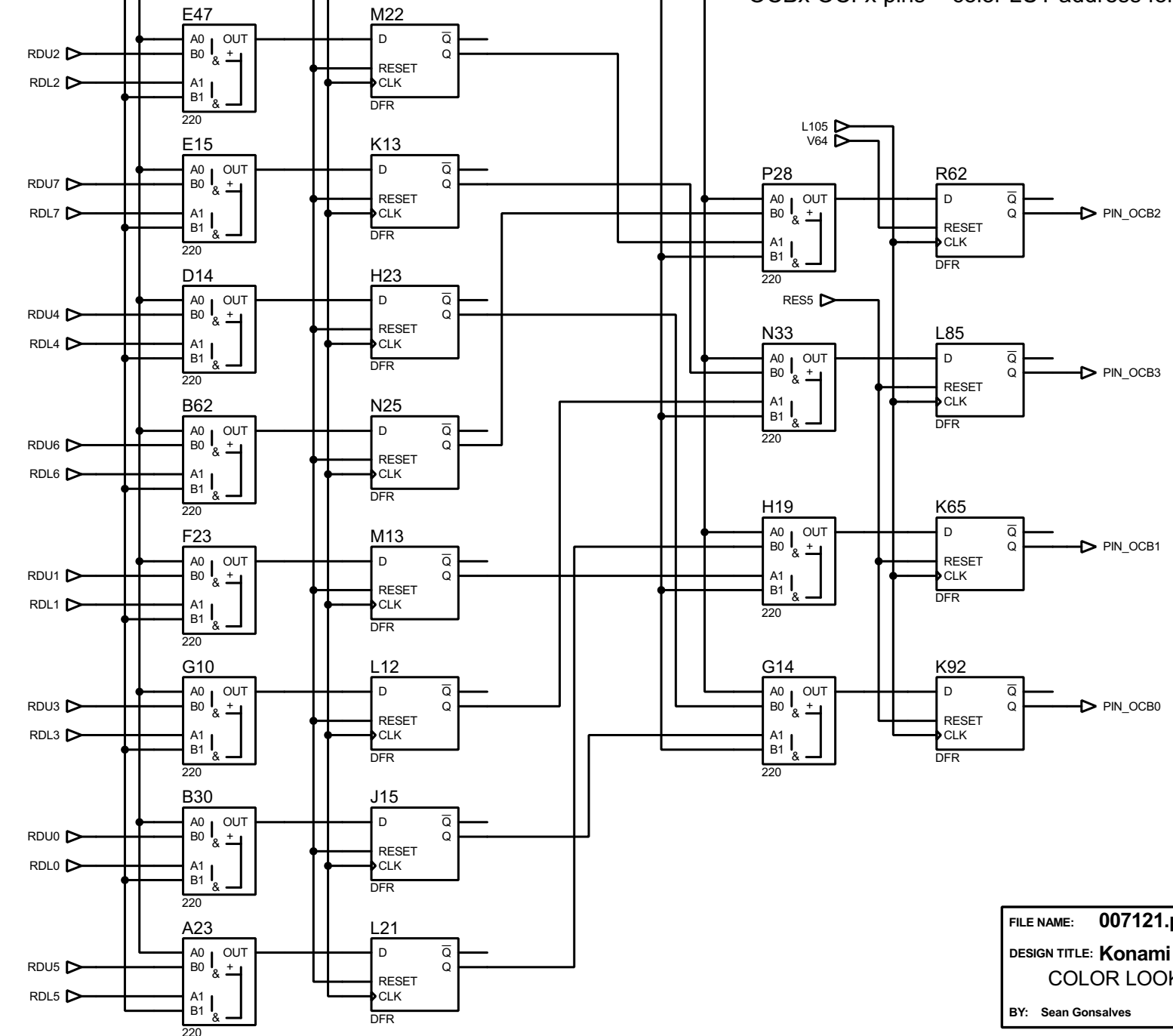
| | | | | Pixel code
| | | | | Color code for tile/sprite



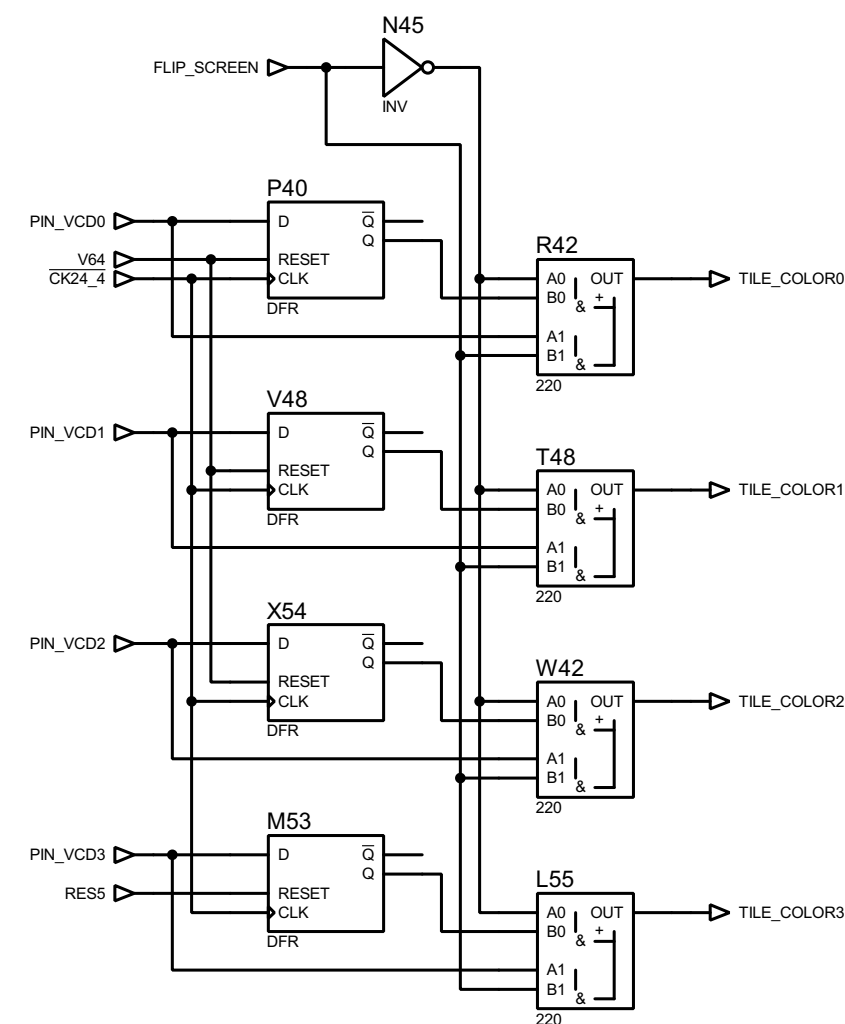
VCBx VCFx pins = color LUT address for tiles

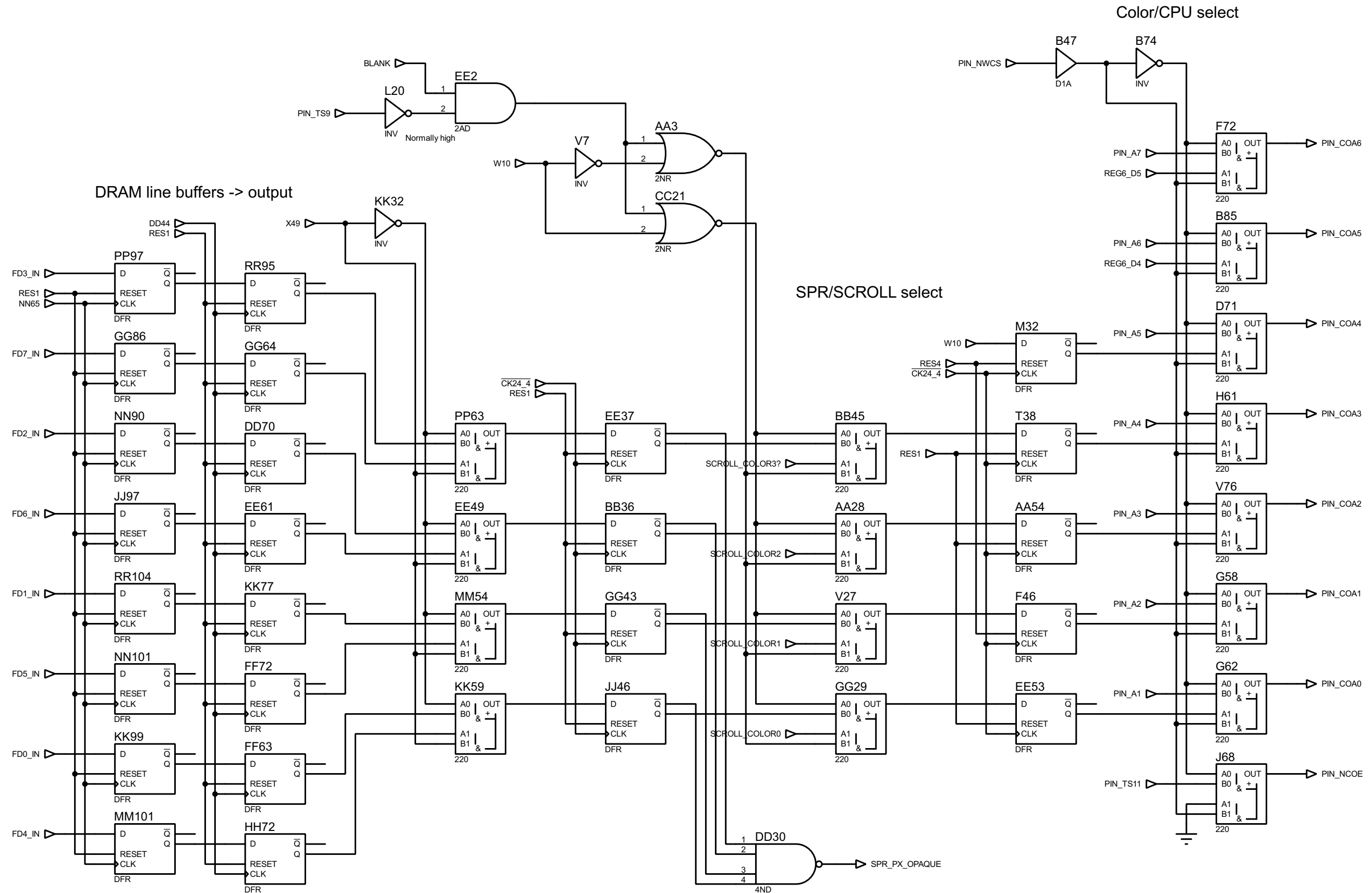


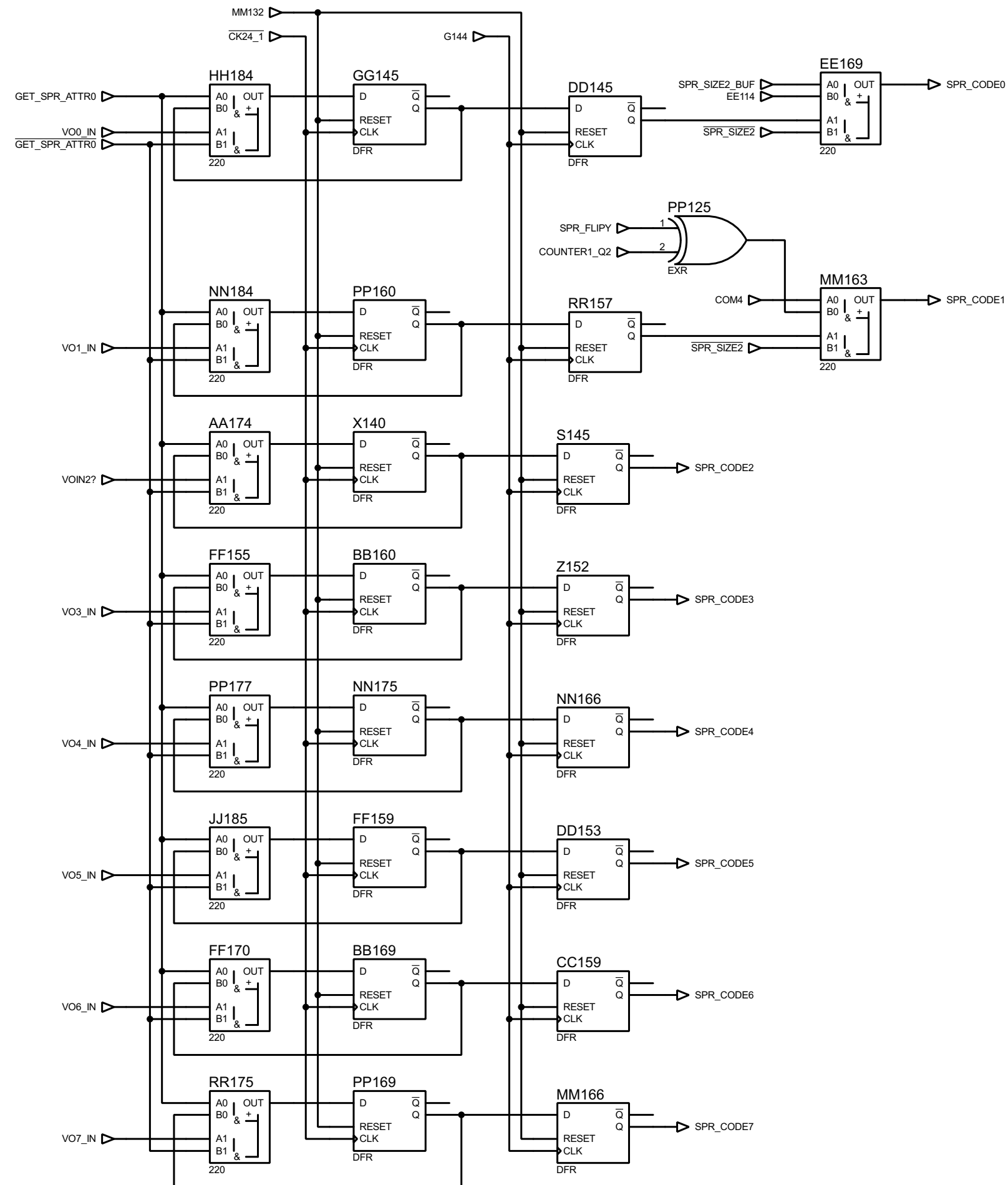
OCBx OCFx pins = color LUT address for sprites

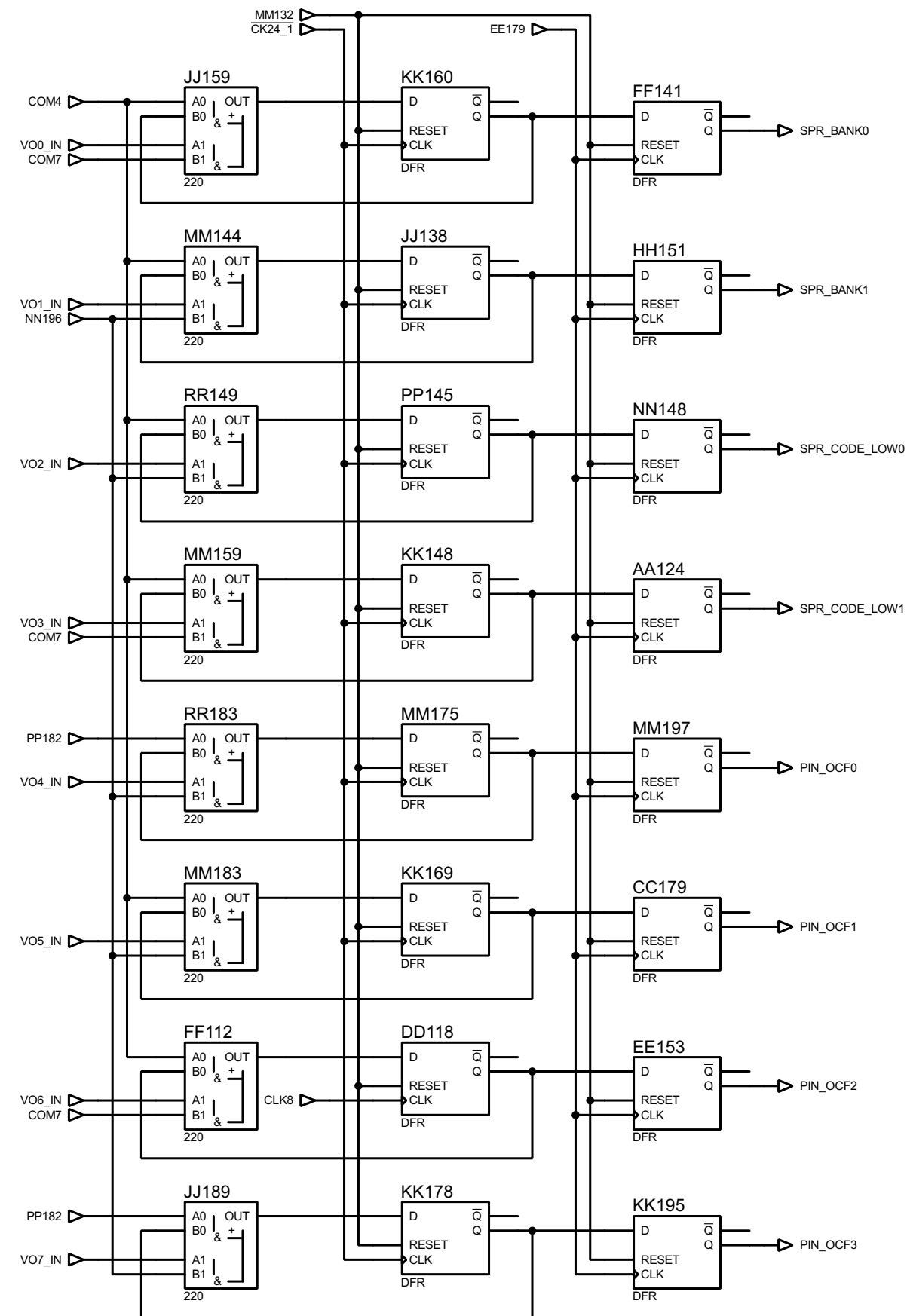


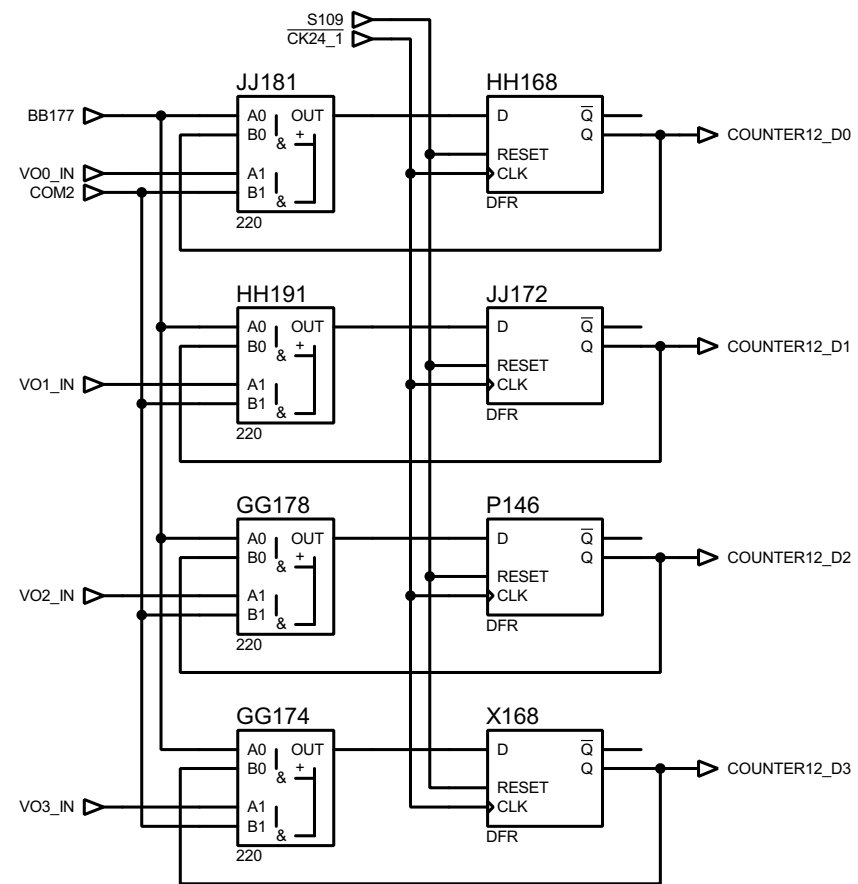
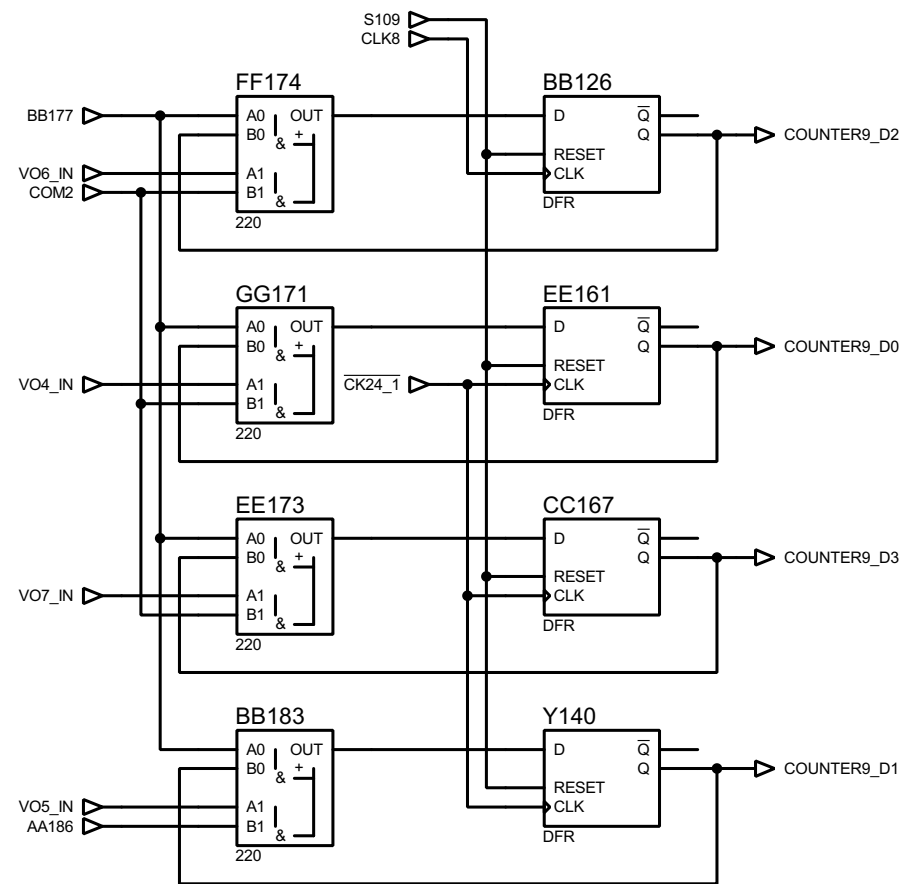
VCDx pins = color LUT data for tiles



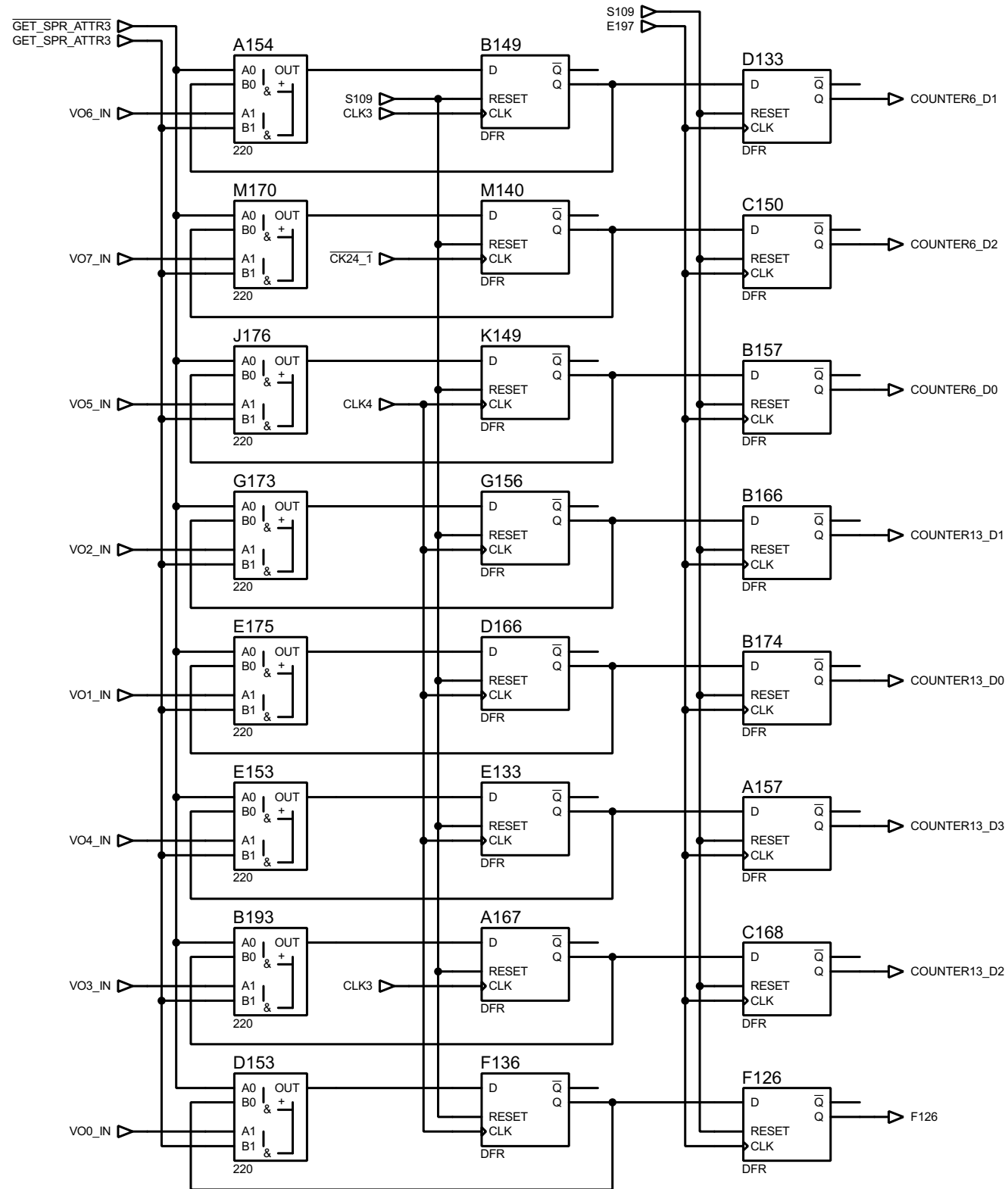


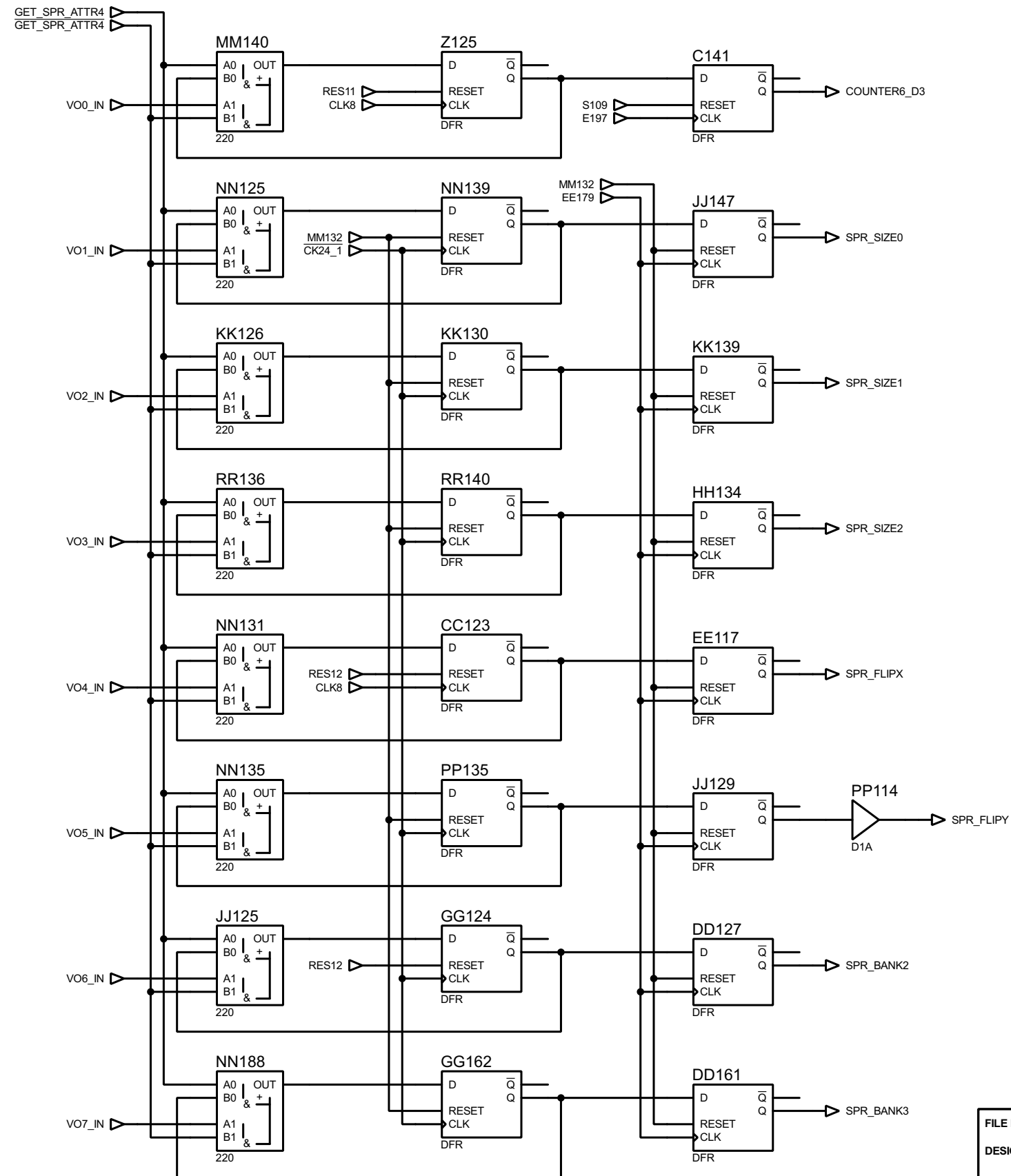


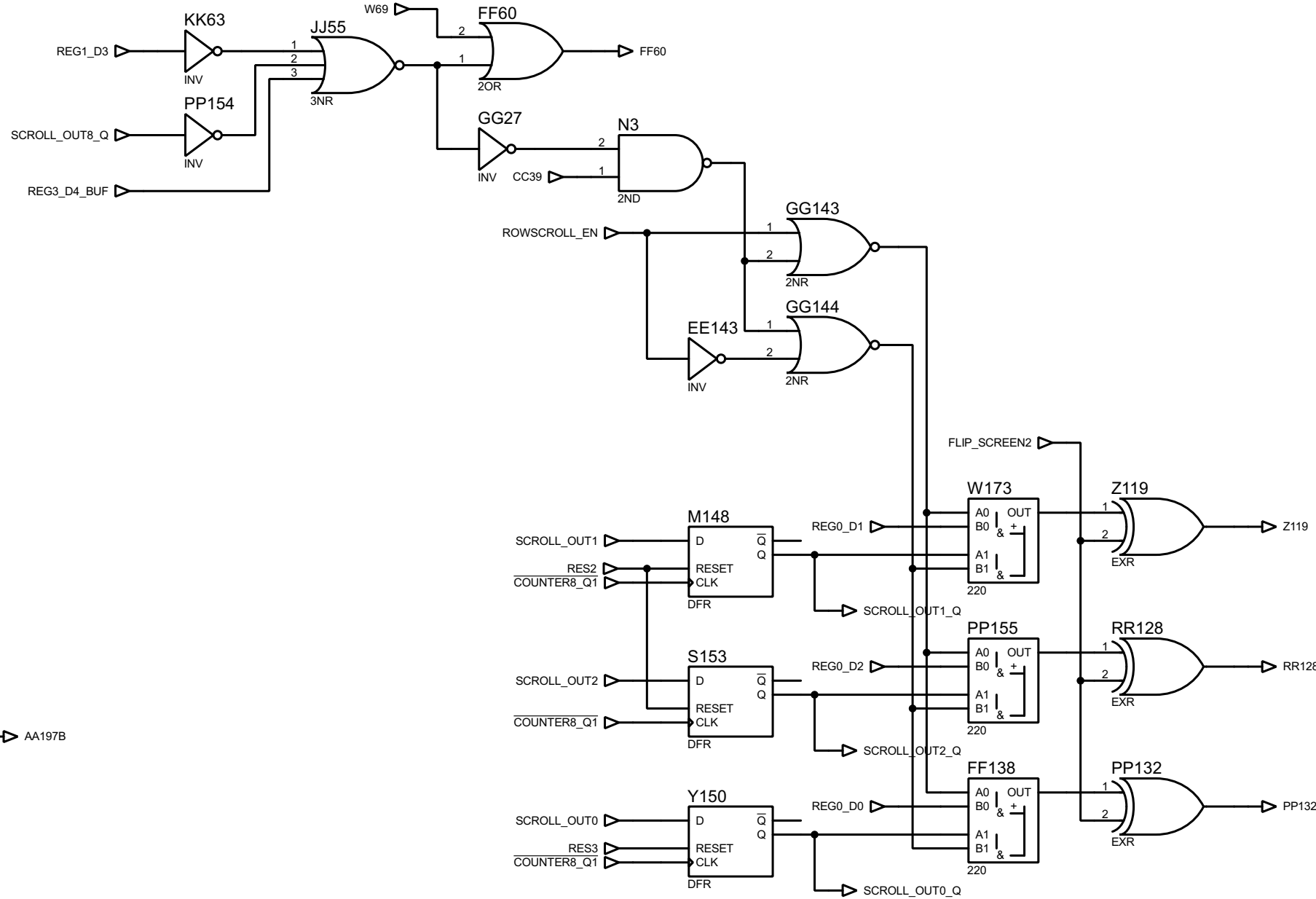


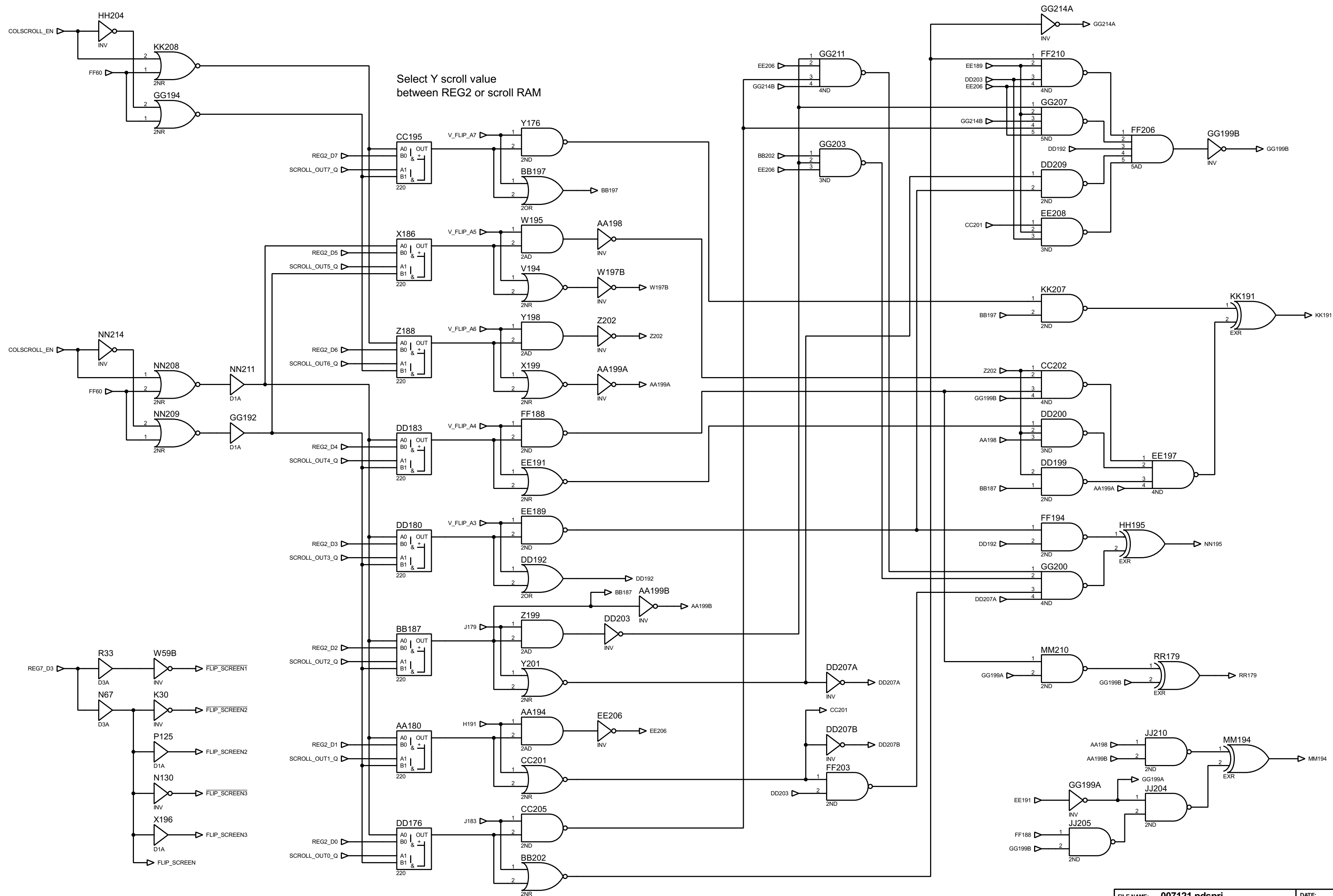


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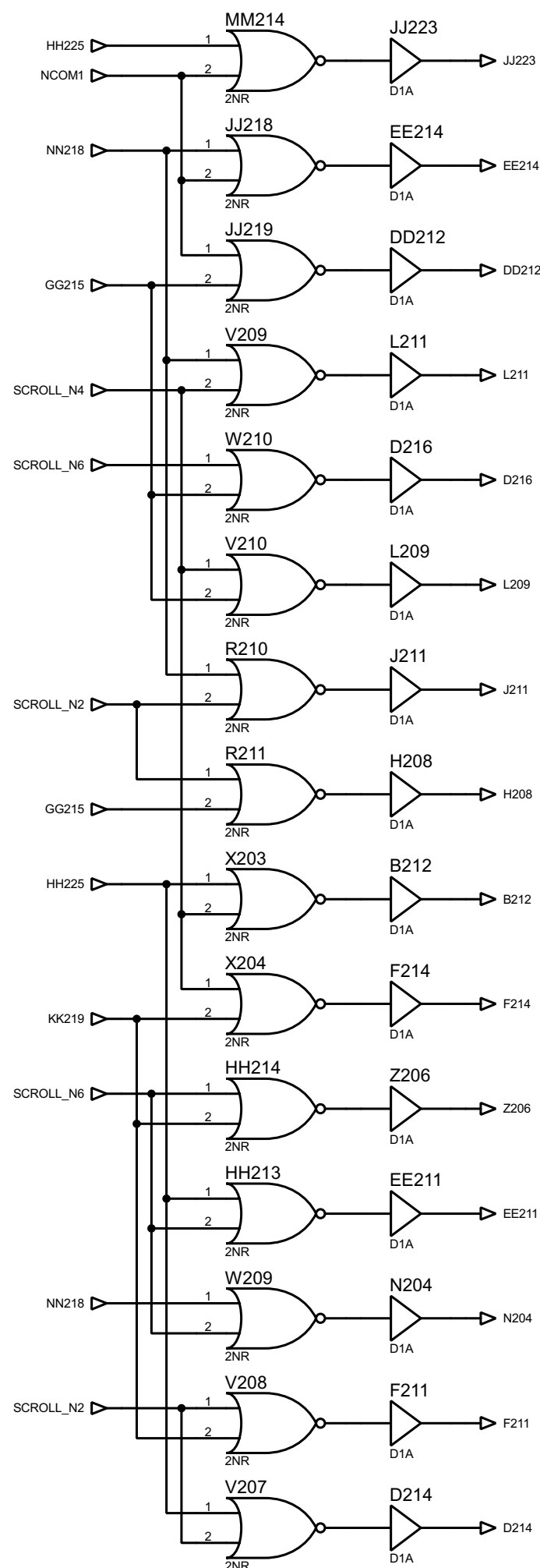
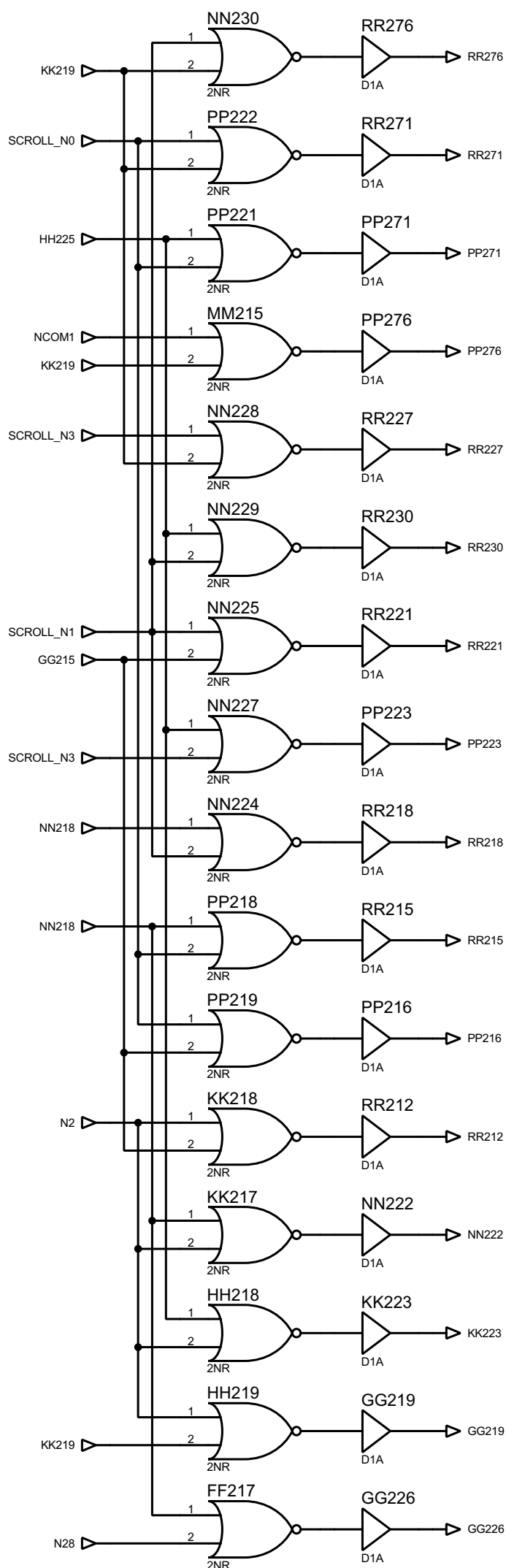




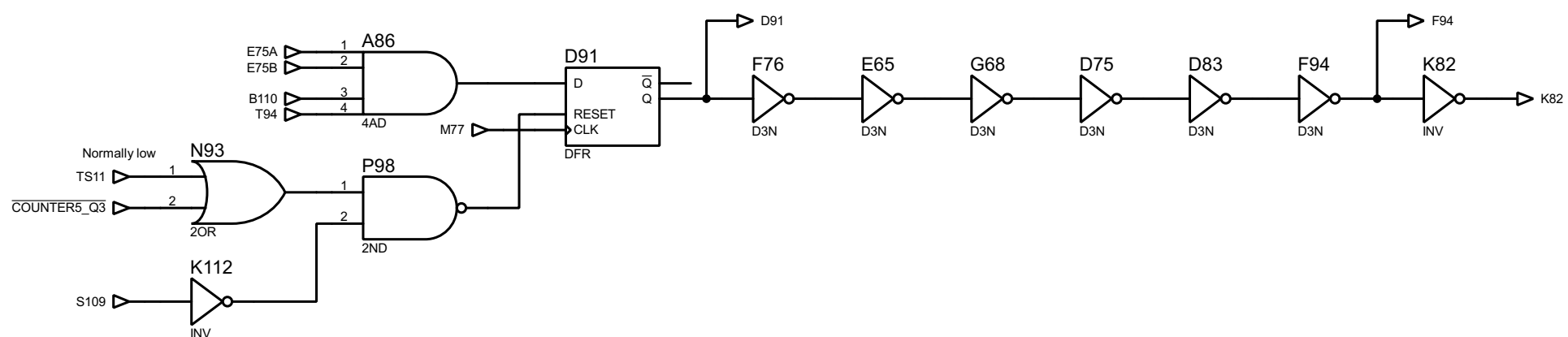
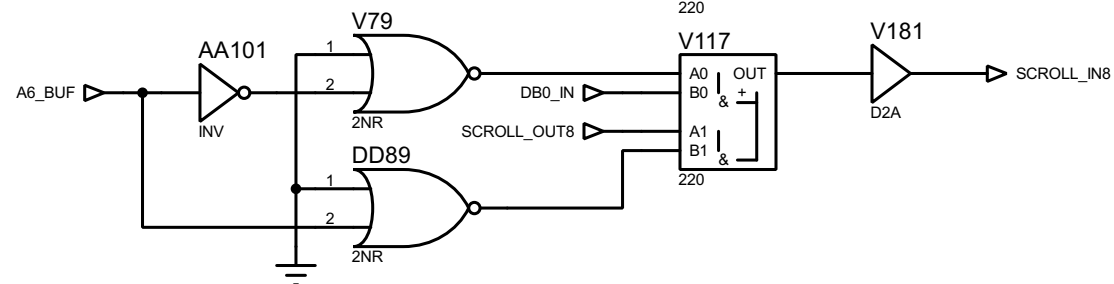


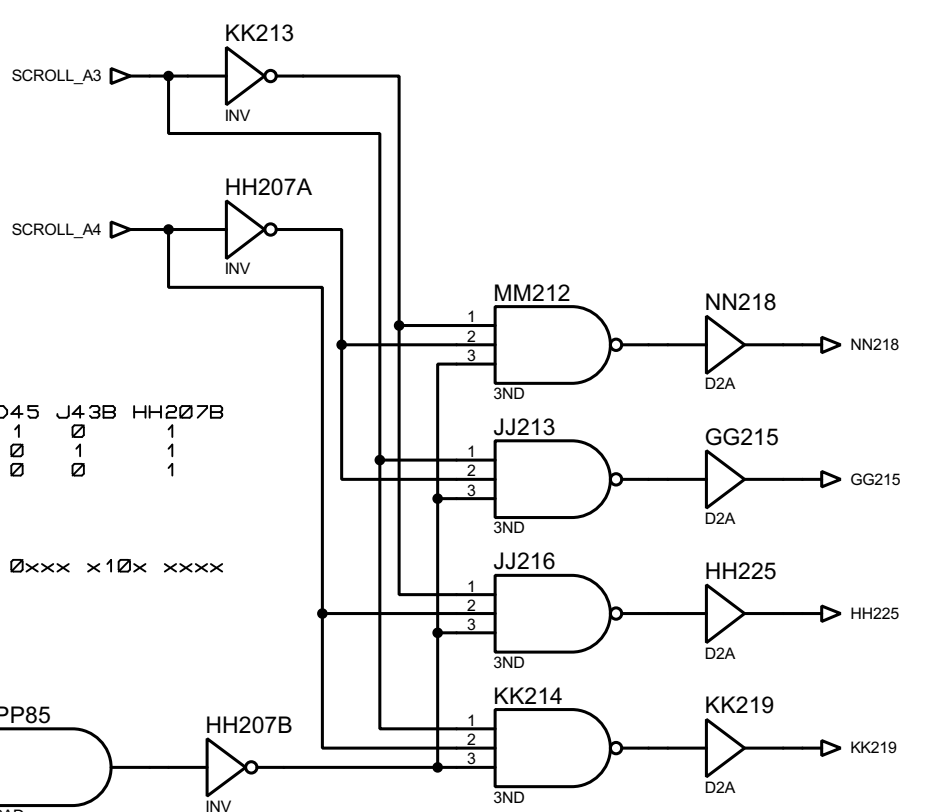
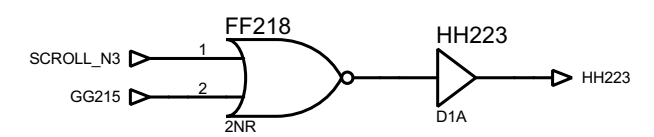
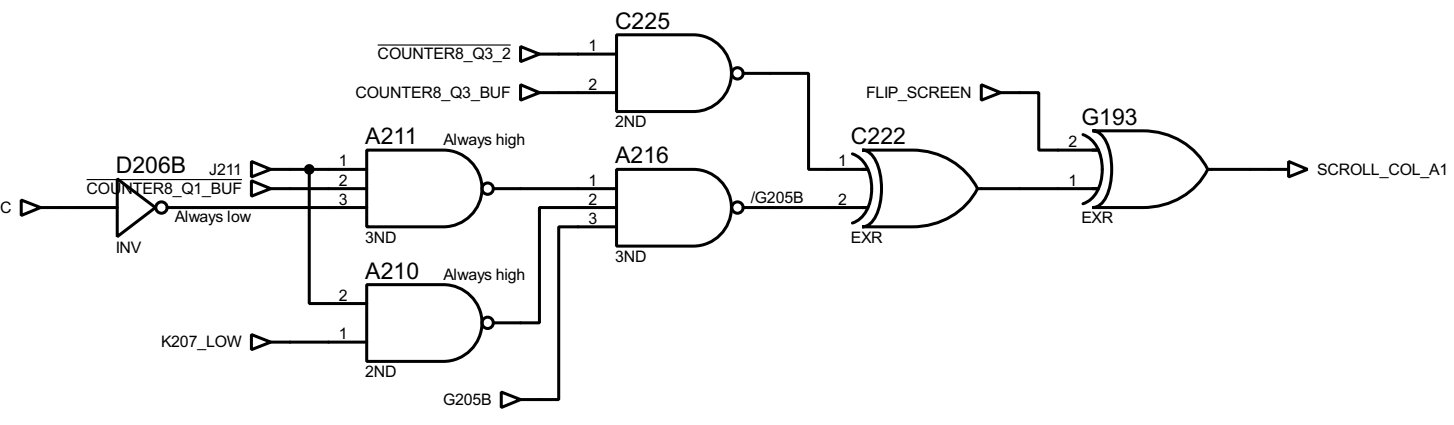
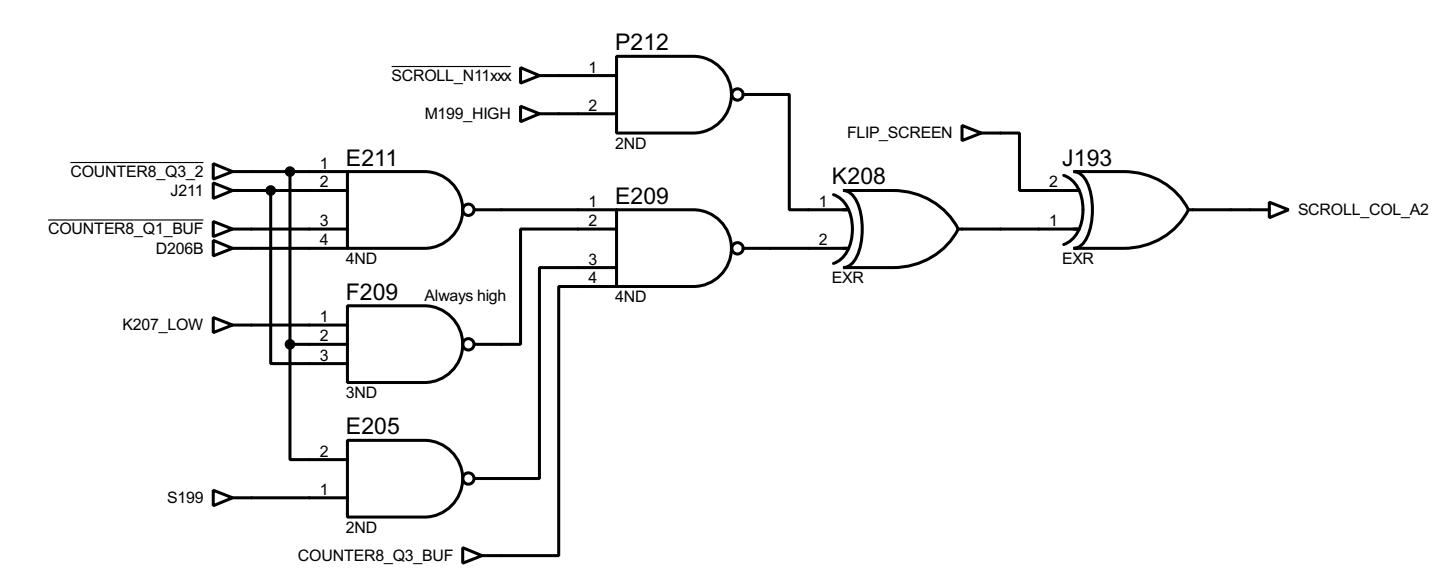
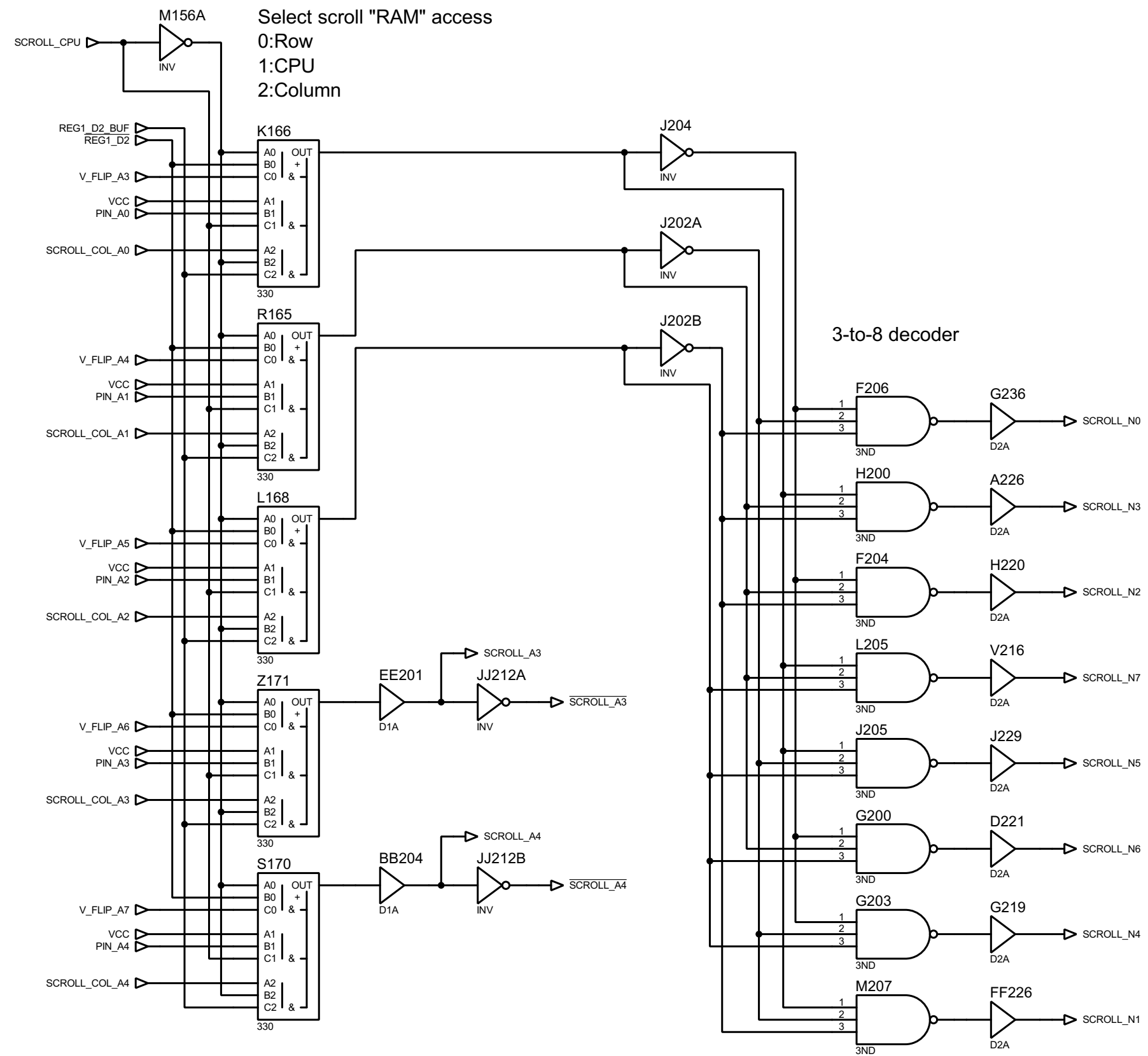
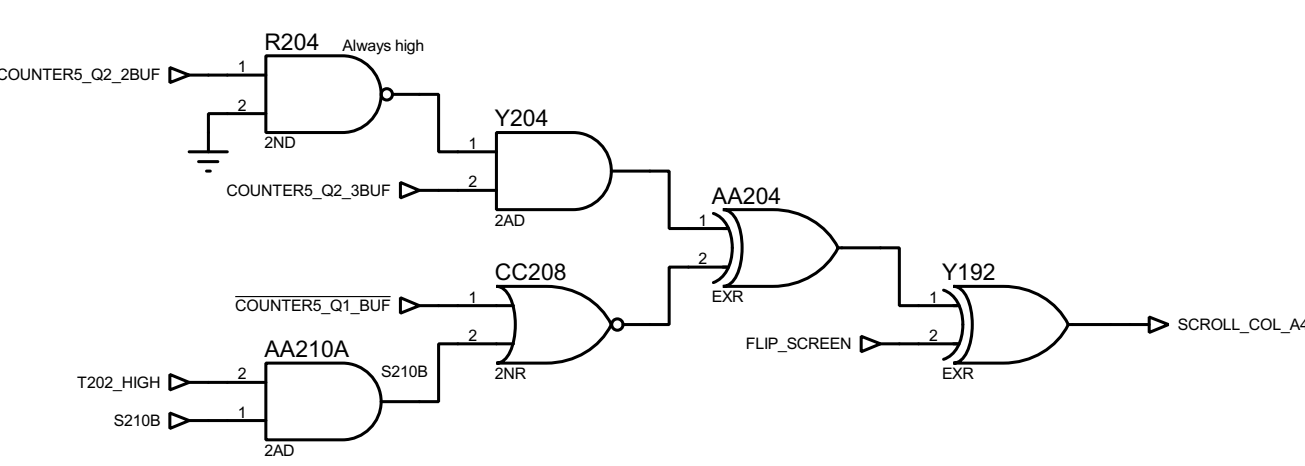
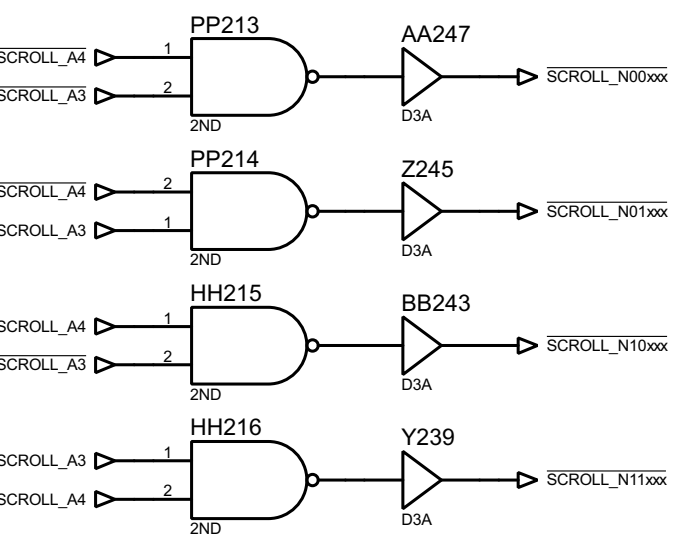
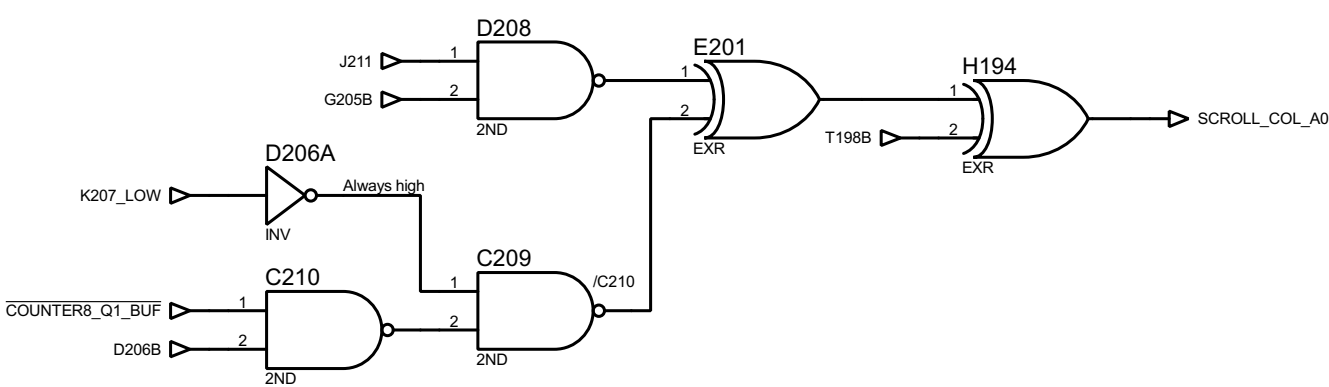
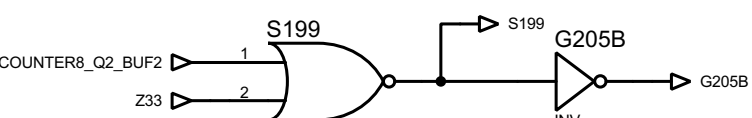
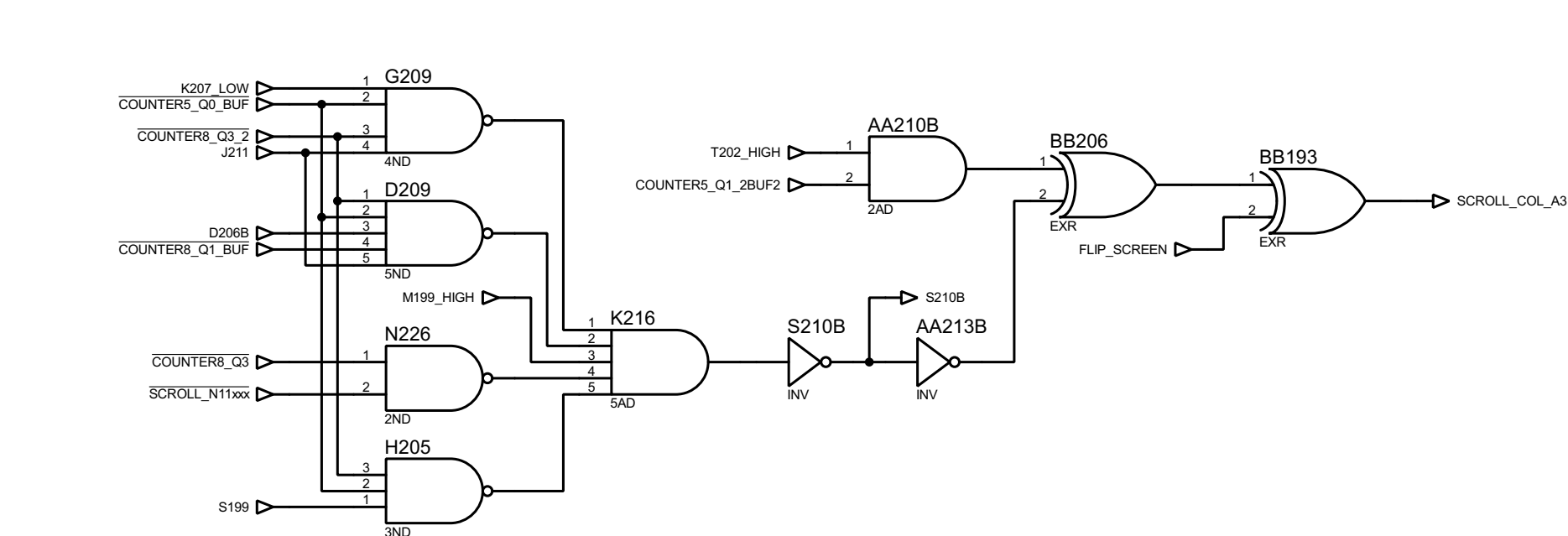


FILE NAME: 007121.pdsprj	DATE: 31/03/2021
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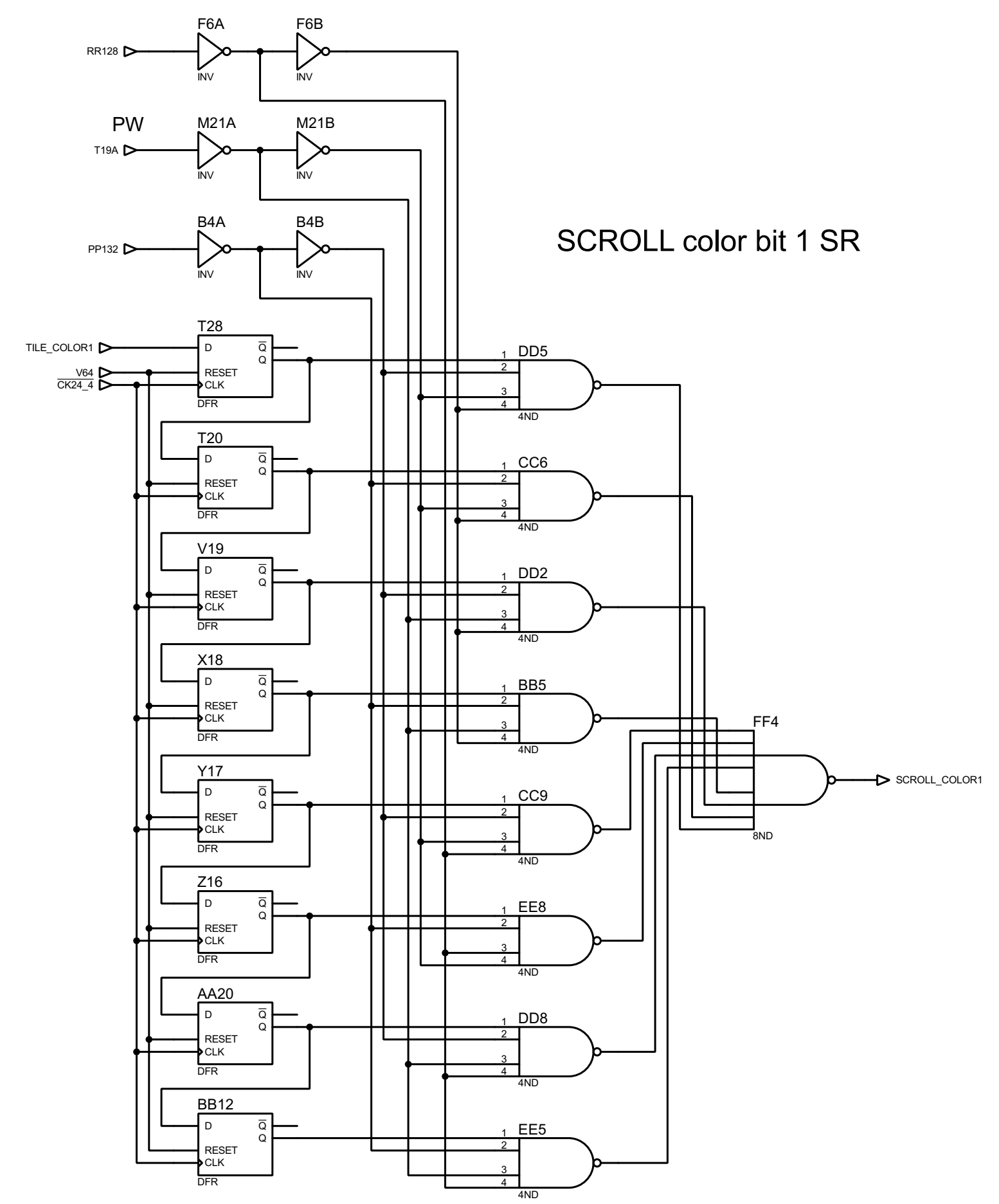
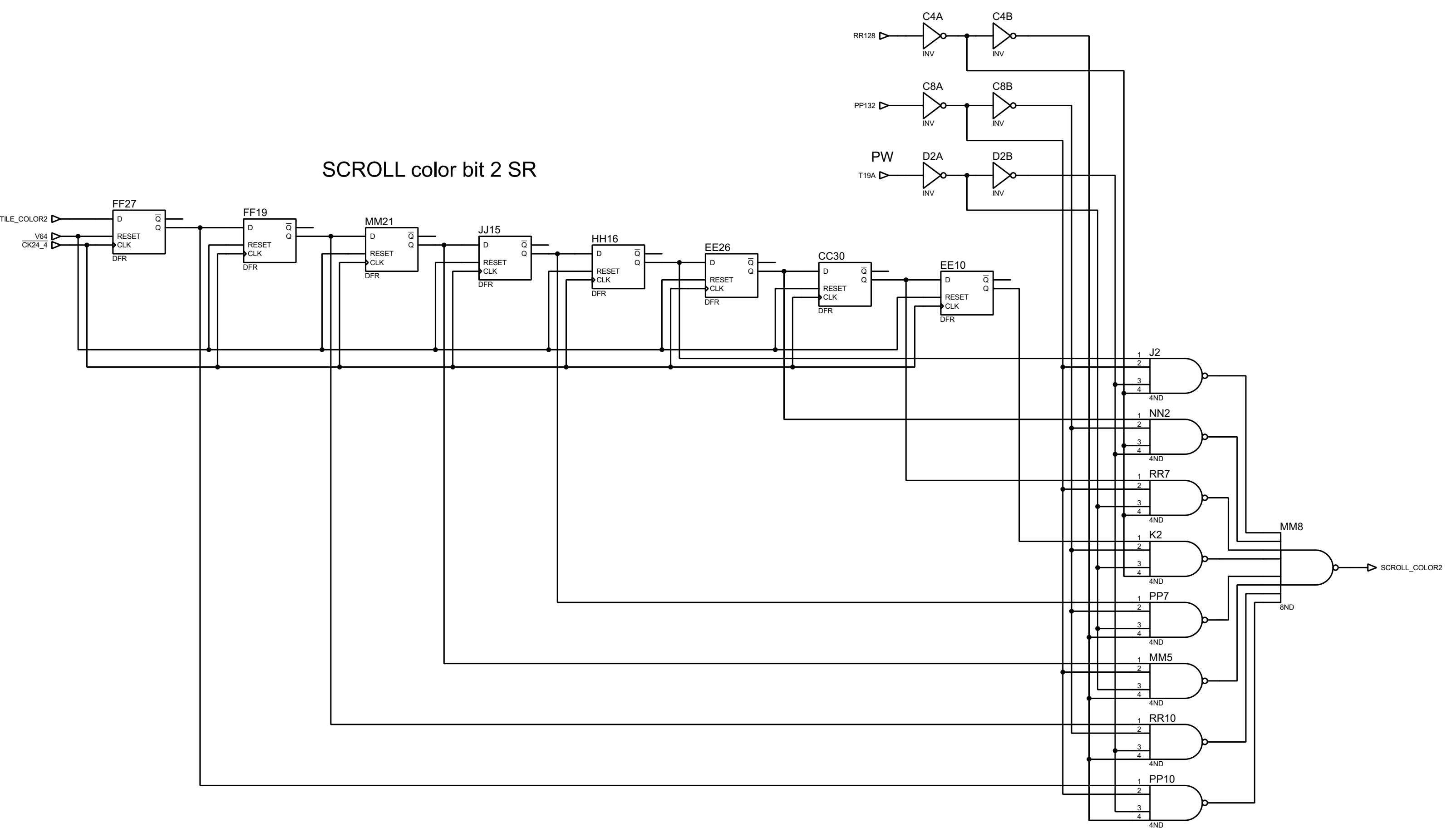
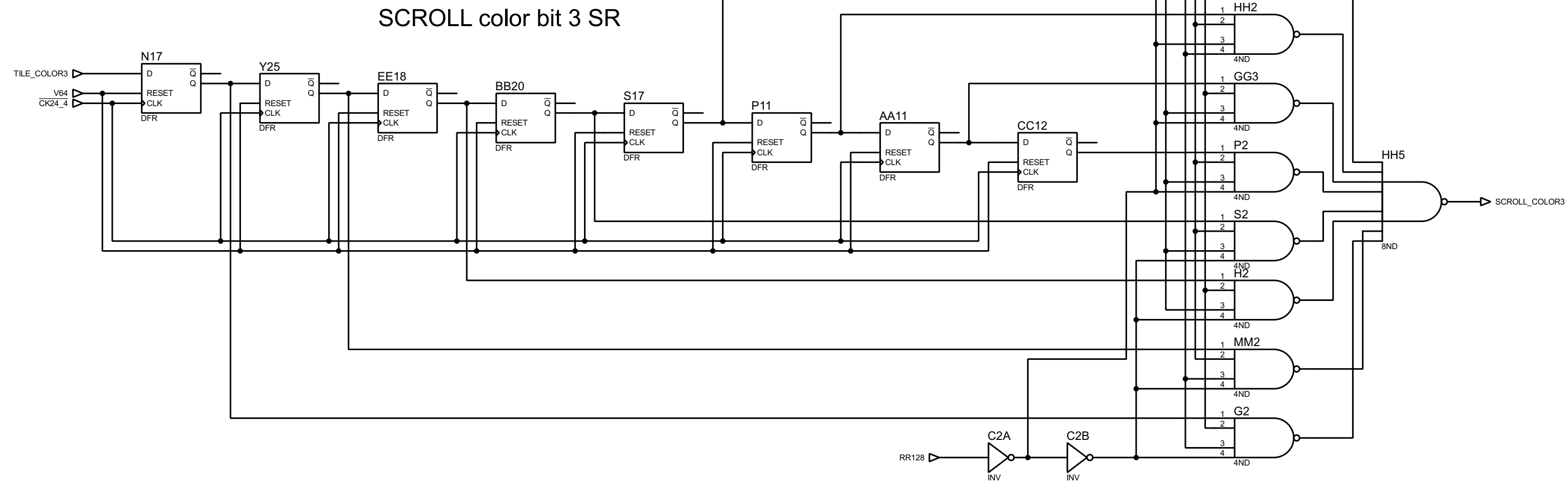
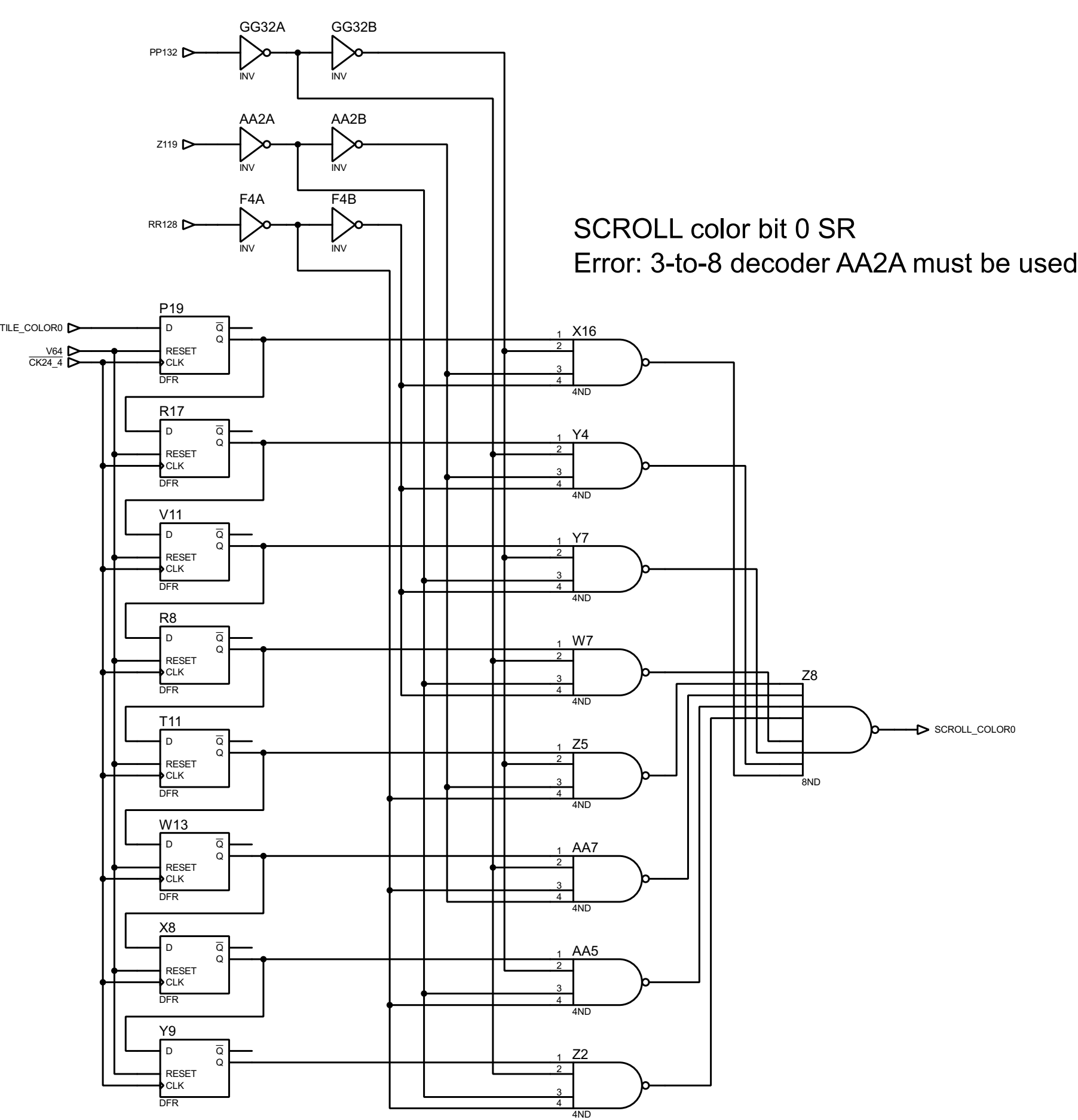
The schematic diagram illustrates the scroll control logic. A central vertical bus connects seven input modules (V187, Y173, W186, GG181, W179, N131, W189) and one output module (EE176). Each module has inputs A0, B0, A1, B1 and outputs OUT, I, and a feedback loop. The OUT signals are inverted by D2A buffers to produce SCROLL_IN0 through SCROLL_IN7. The feedback loop signals are connected to the A6_BUF input of the V187 module.



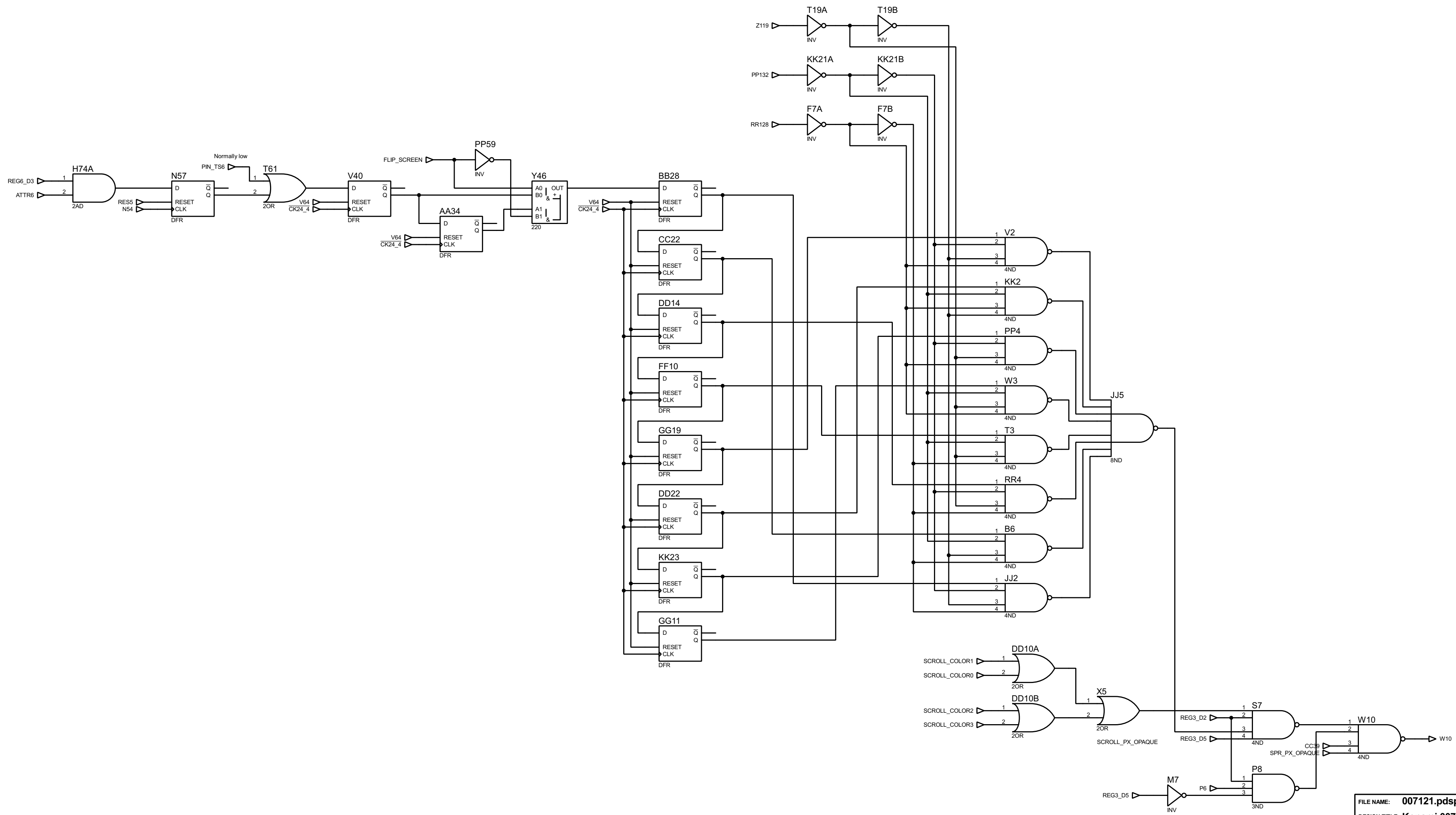


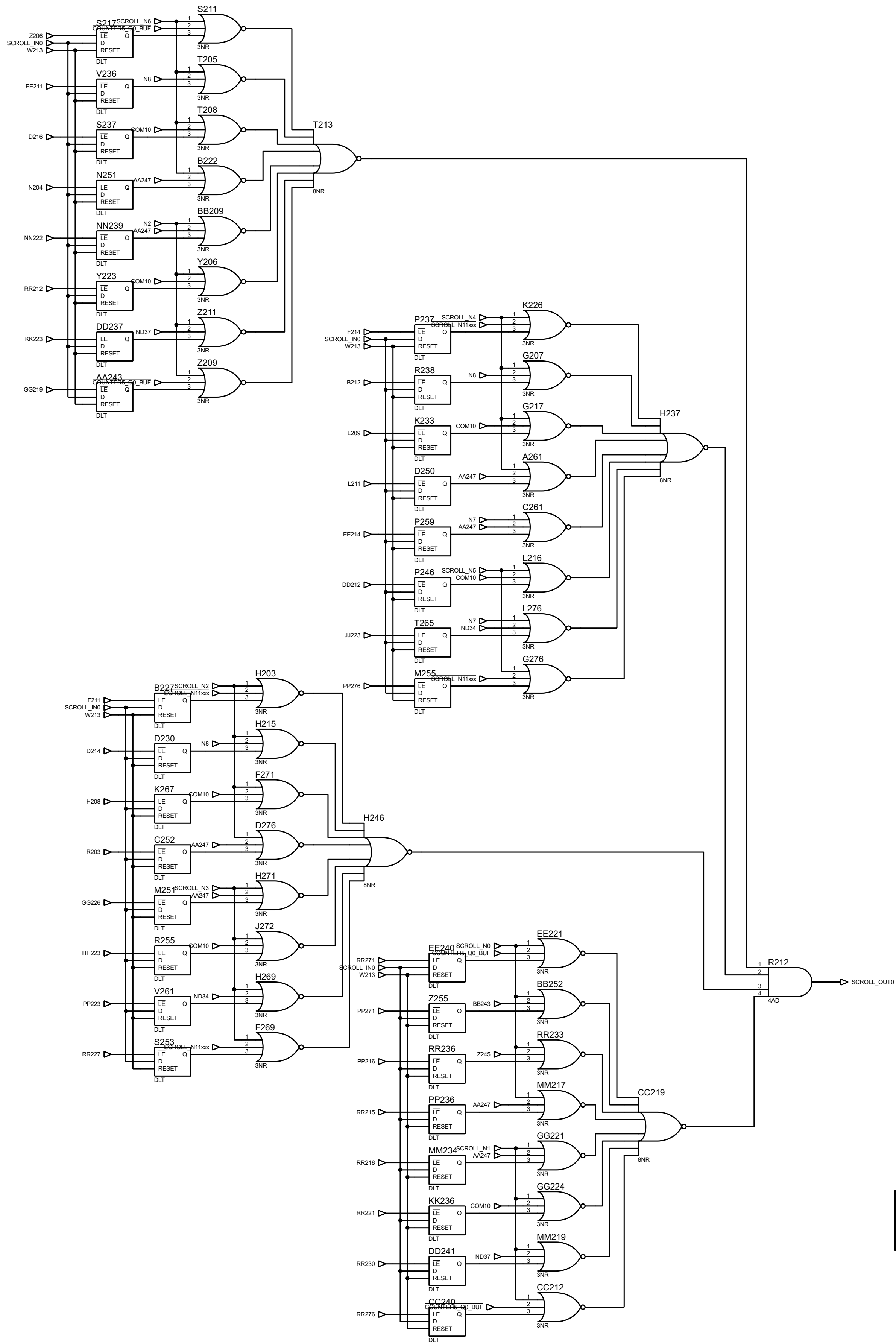
A	NXCS	NWCS	D45	J43B	HH207B
xx00 0xxx x10x xxxx	0	0	1	0	1
xx1x xxxx xxxxx xxxx	0	0	0	1	1
xxxx xxxx xxxxx xxxx	1	0	0	0	1

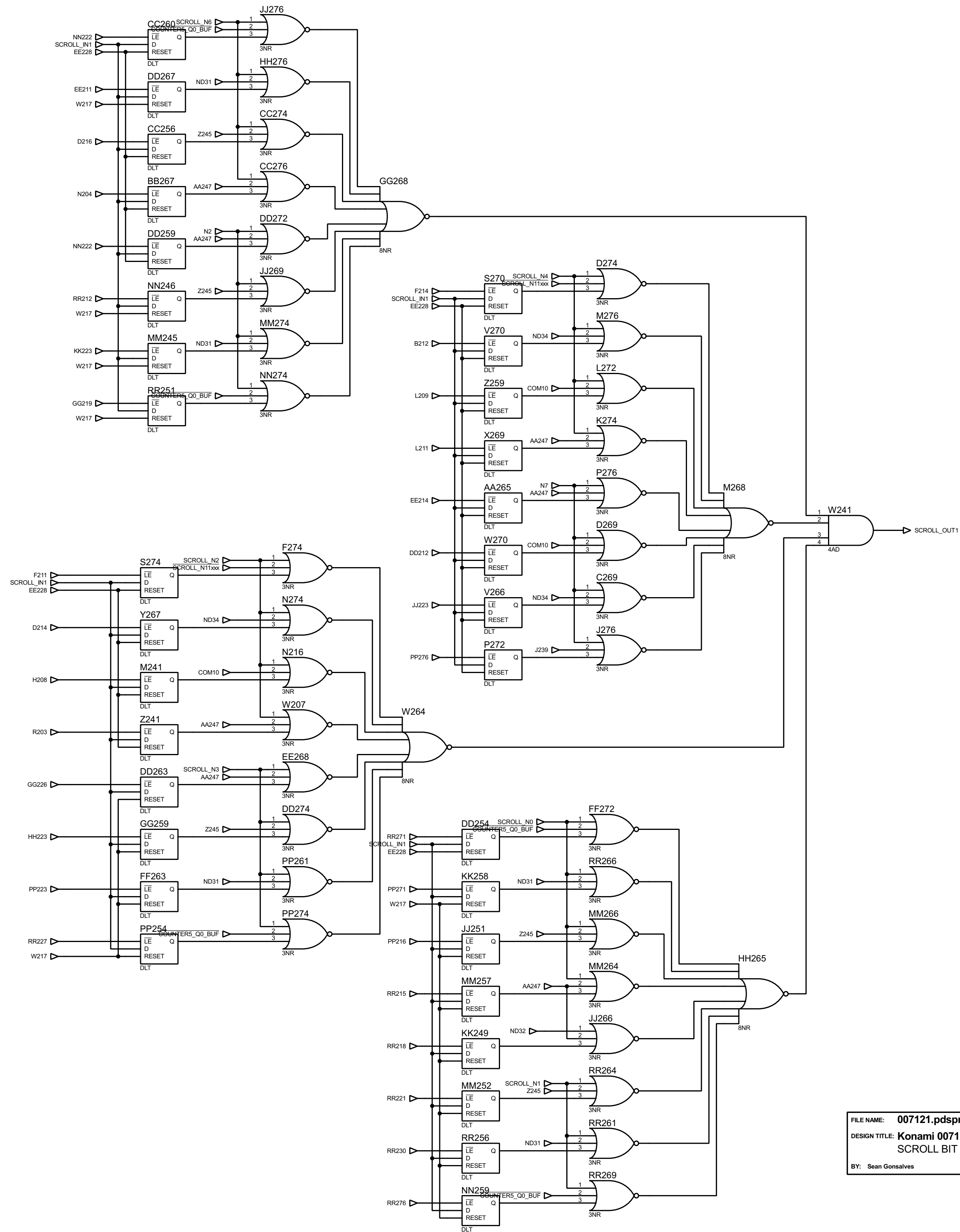
Active when:
-NXCS high and NWCS low
-NXCS low, NWCS low and A13=1
-NXCS low, NWCS low and A=xx00 0xxx x10x xxxx

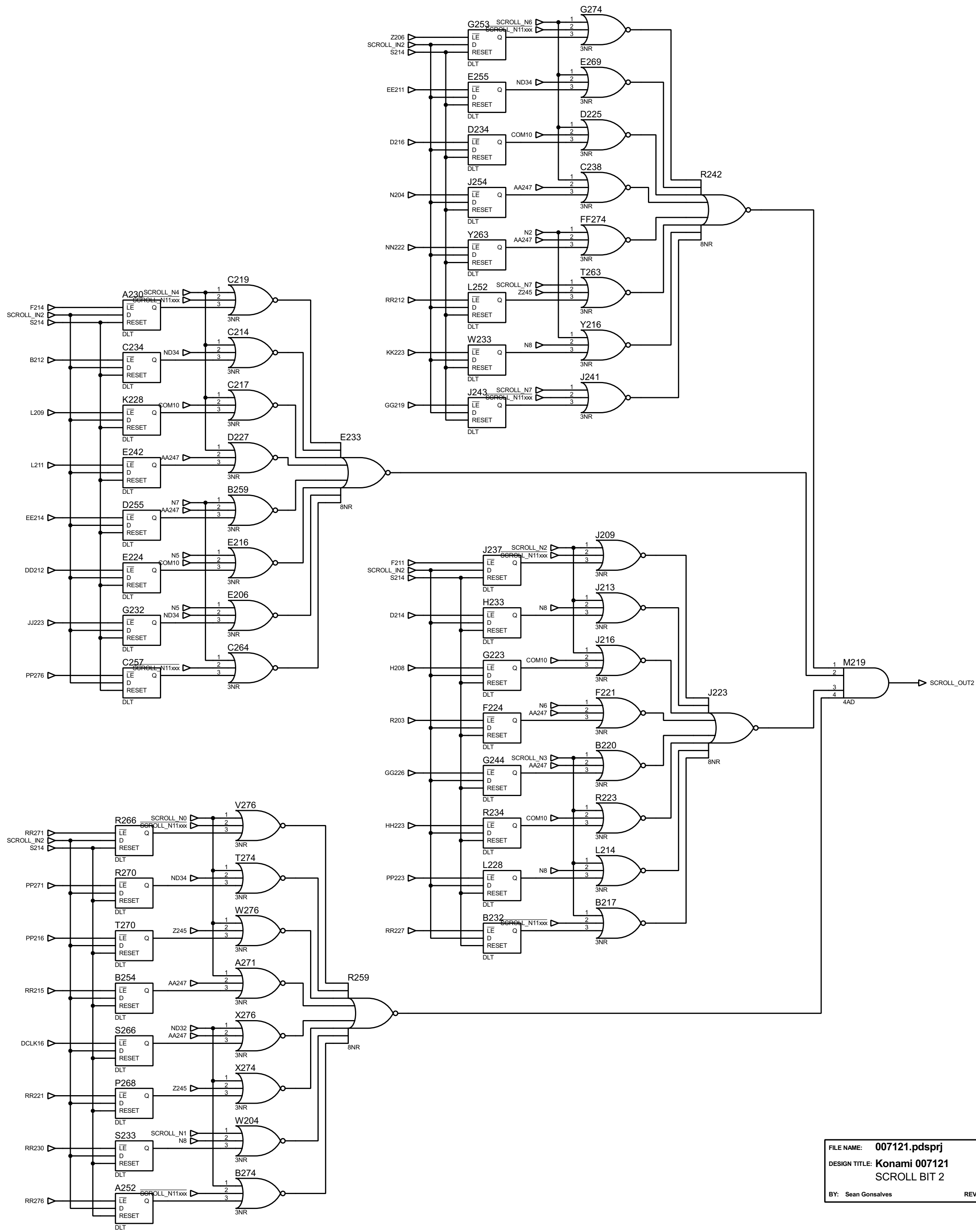


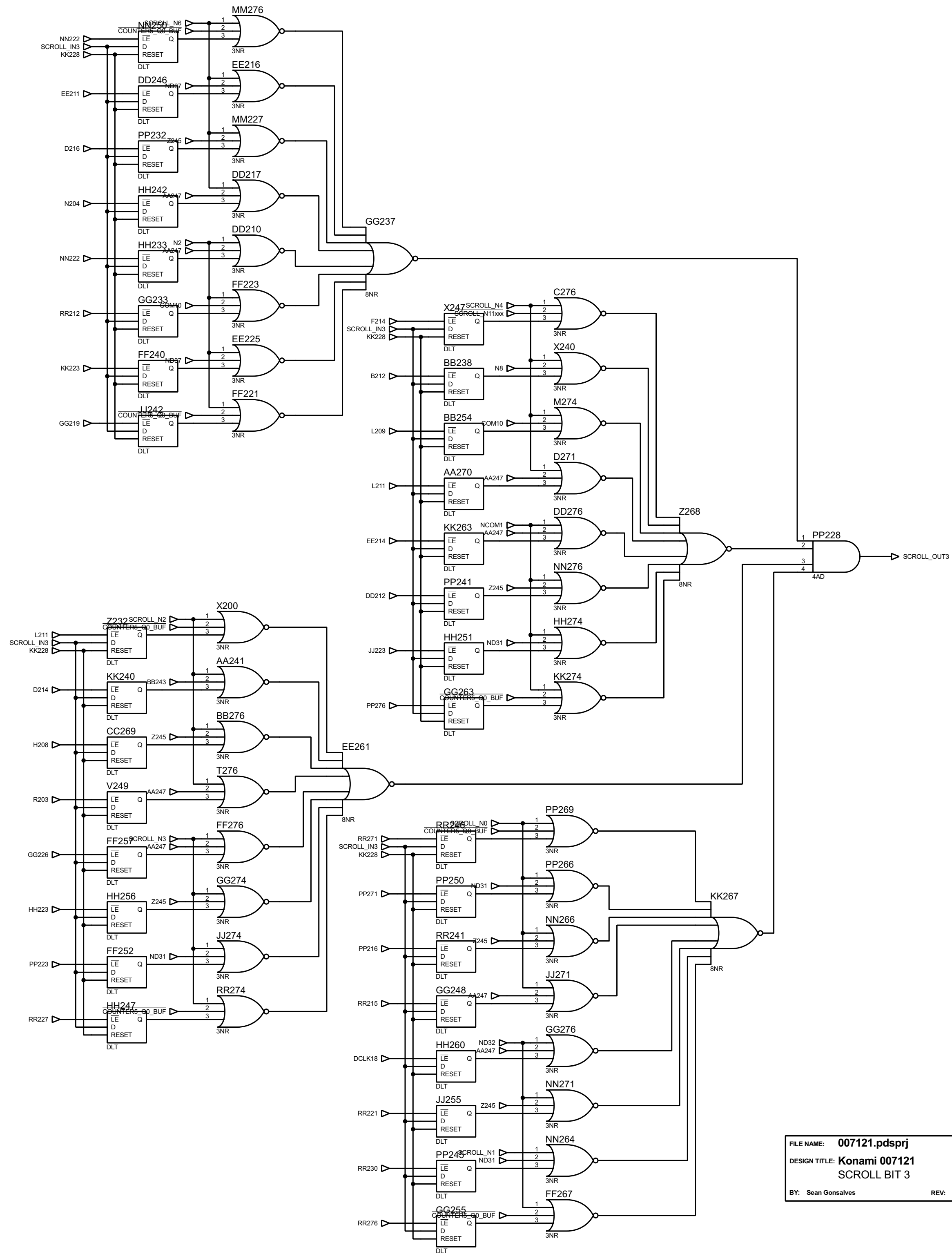
SPR/SCROLL priority bit SR

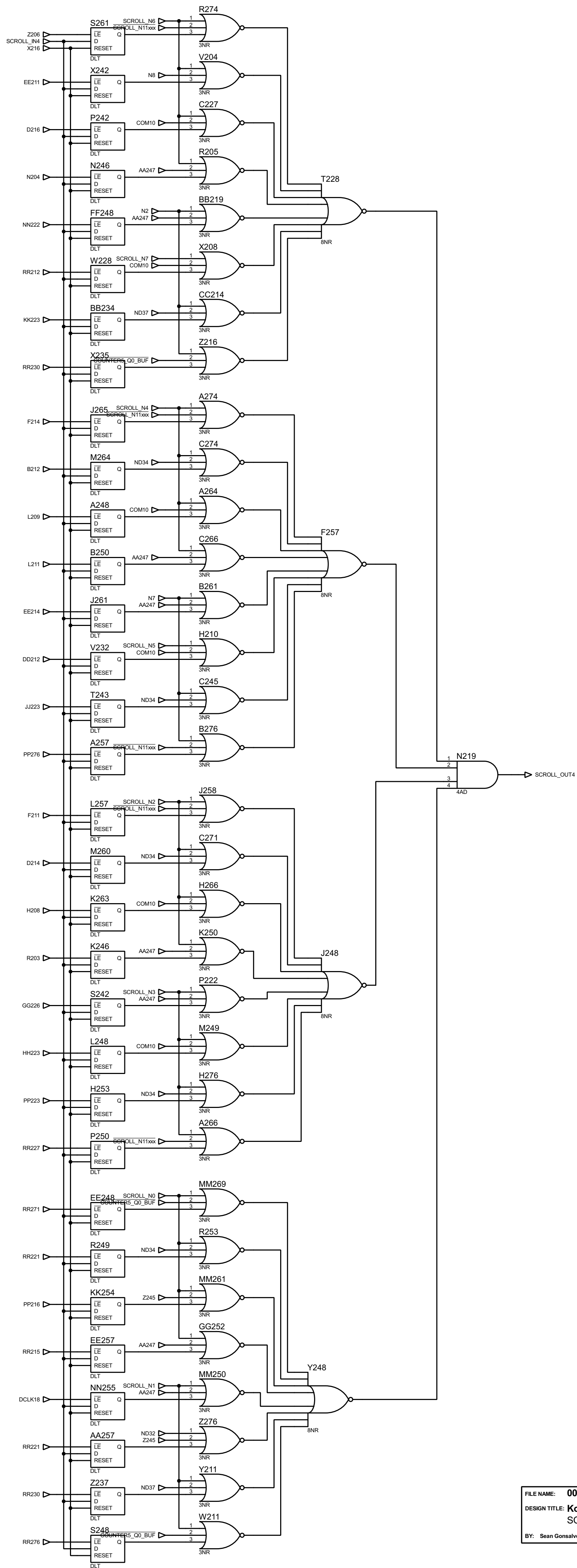


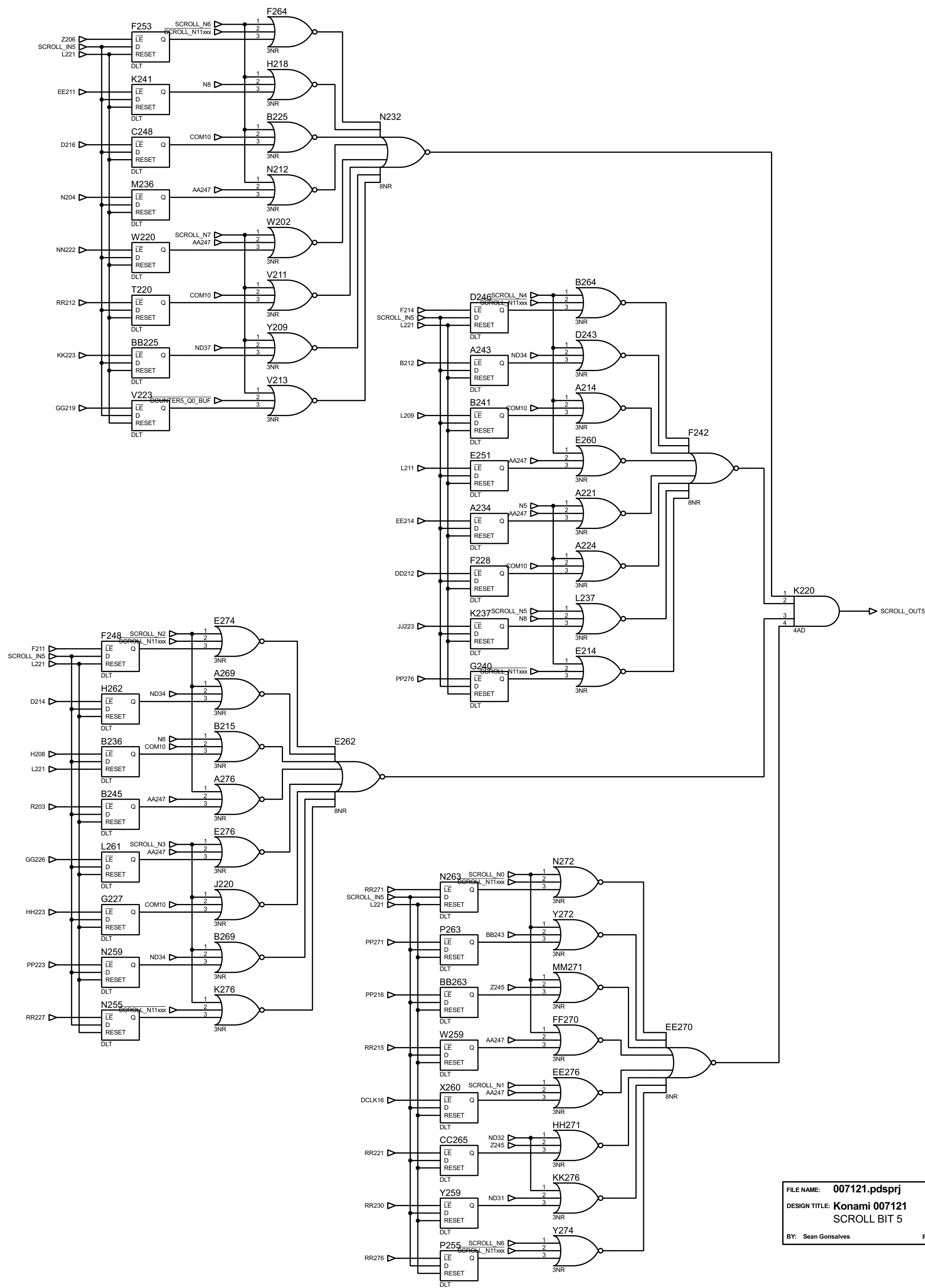


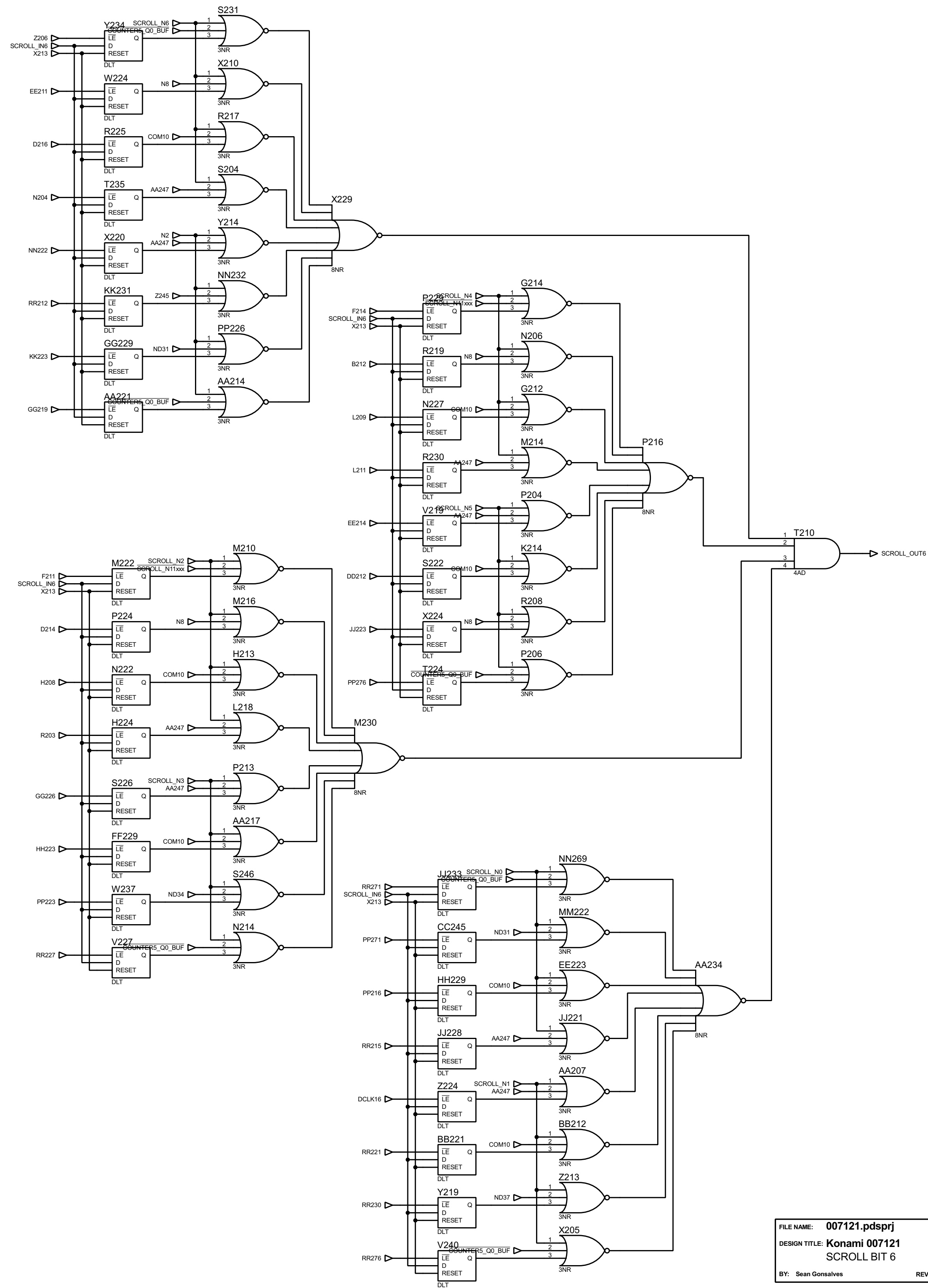


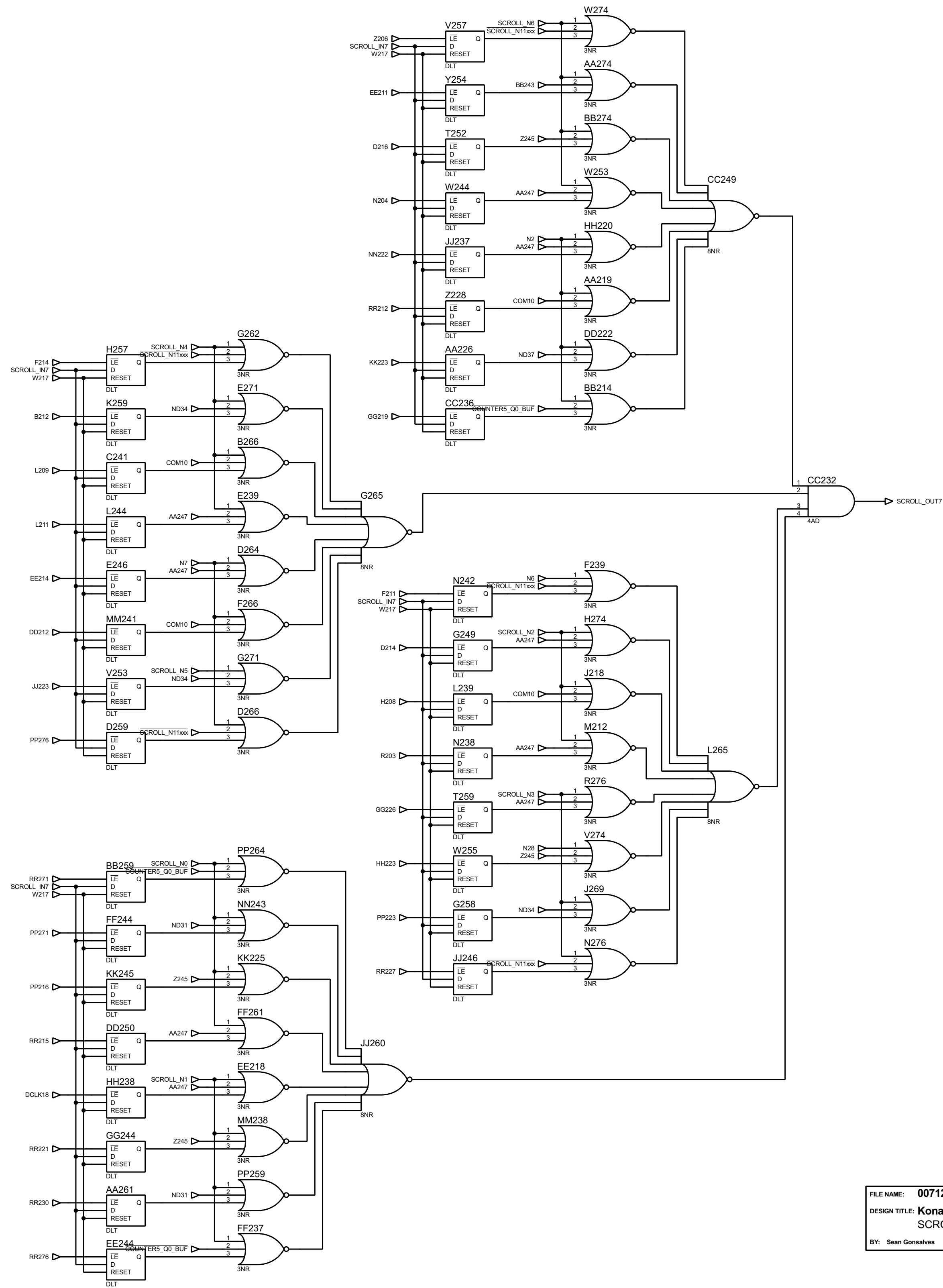


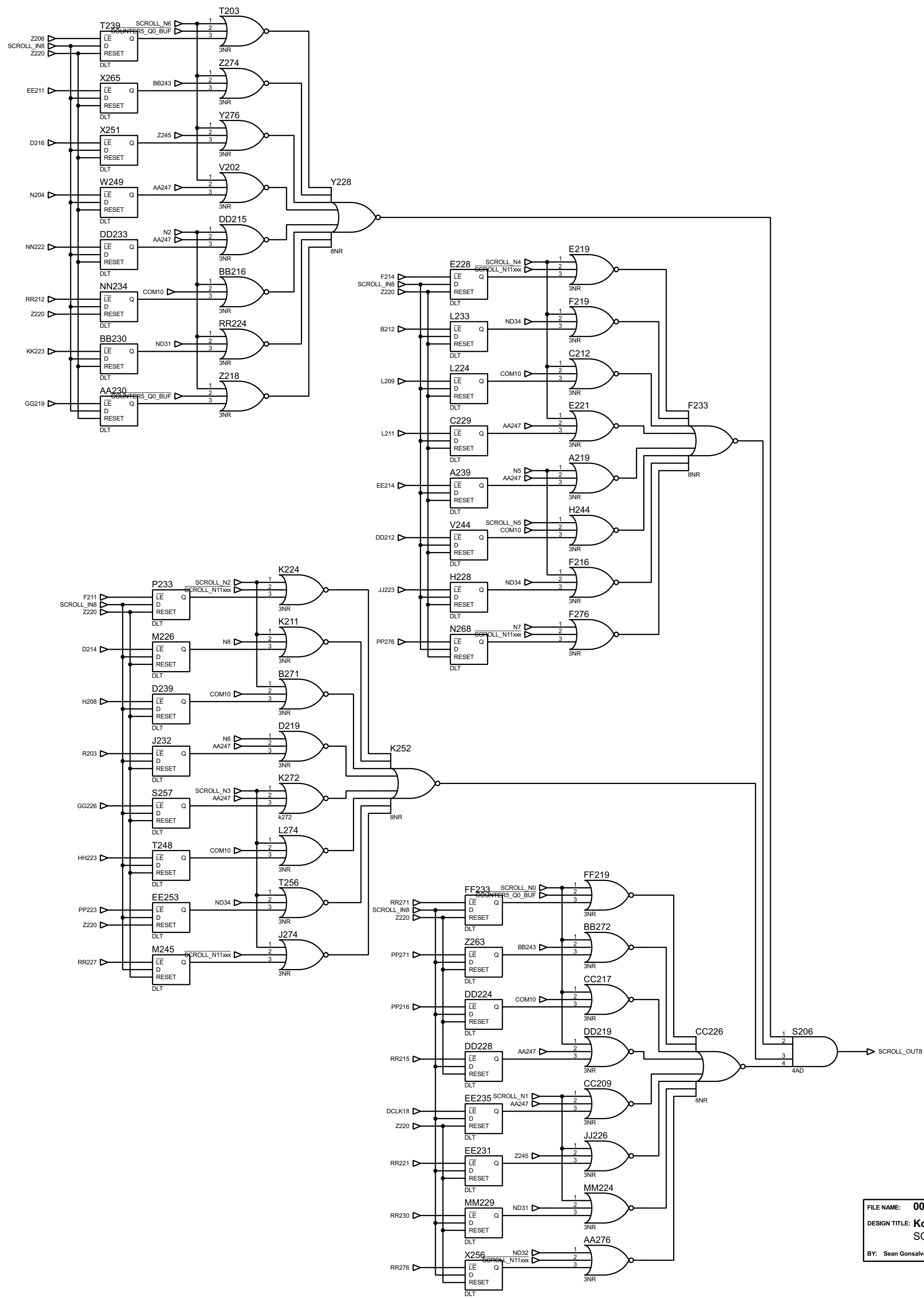


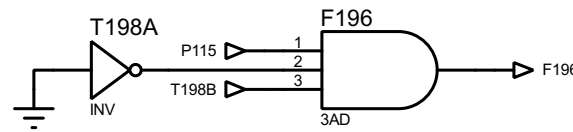
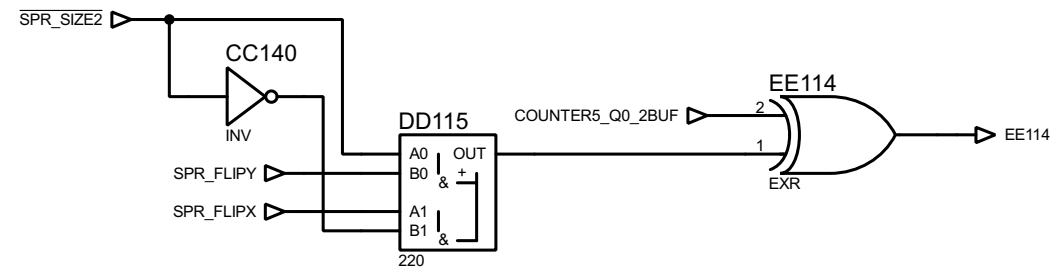
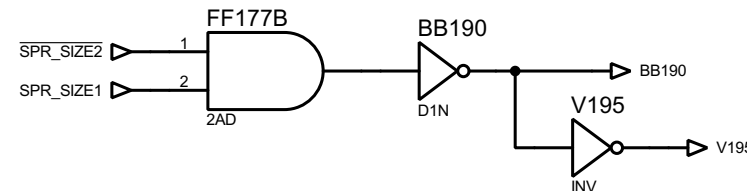
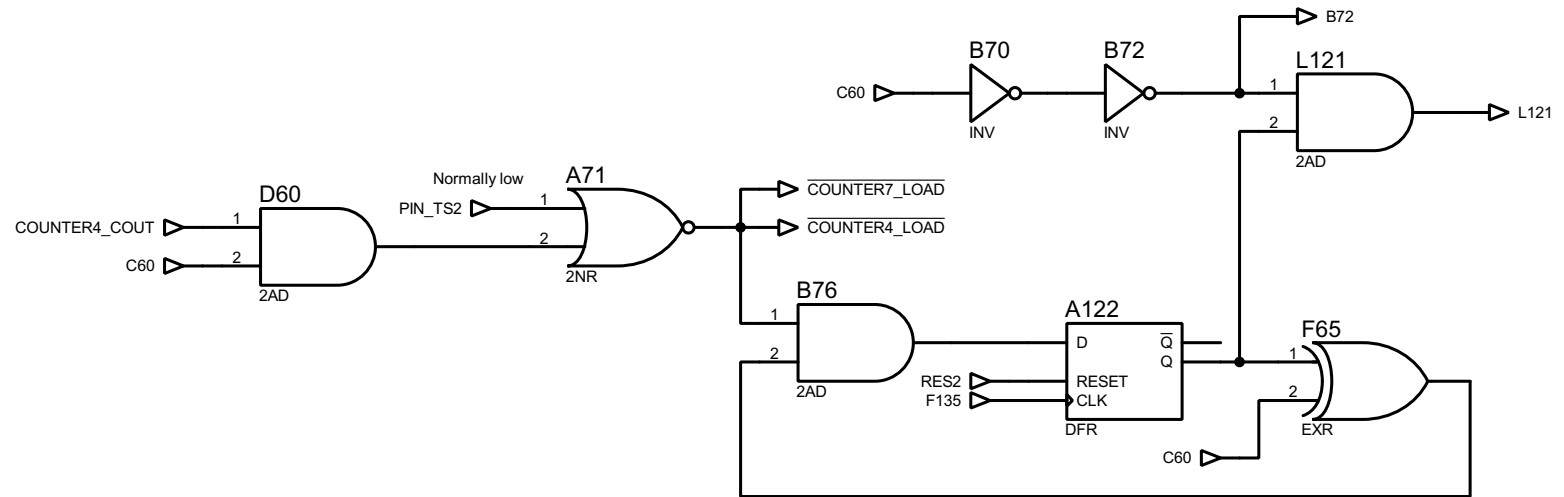
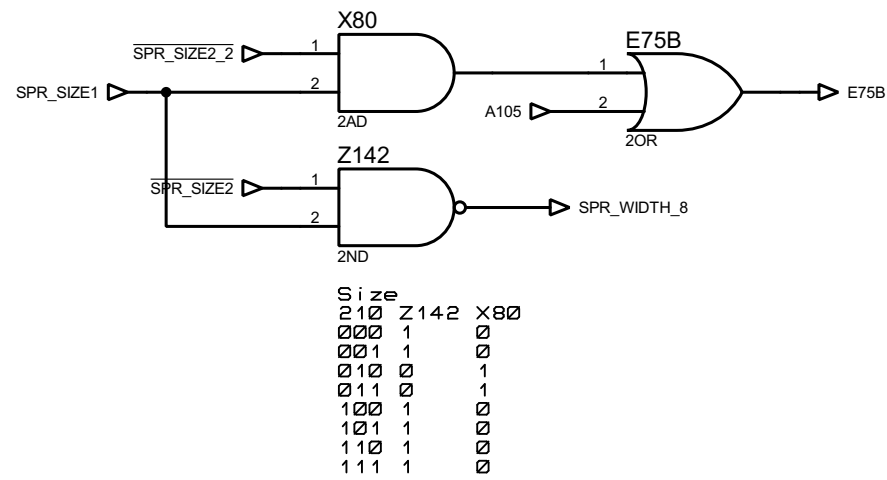
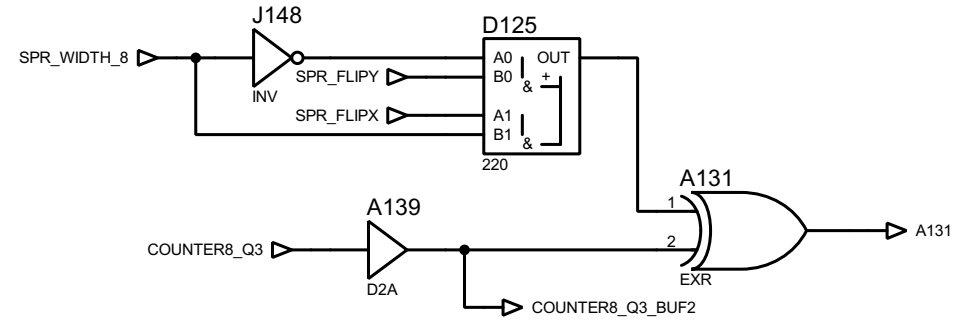
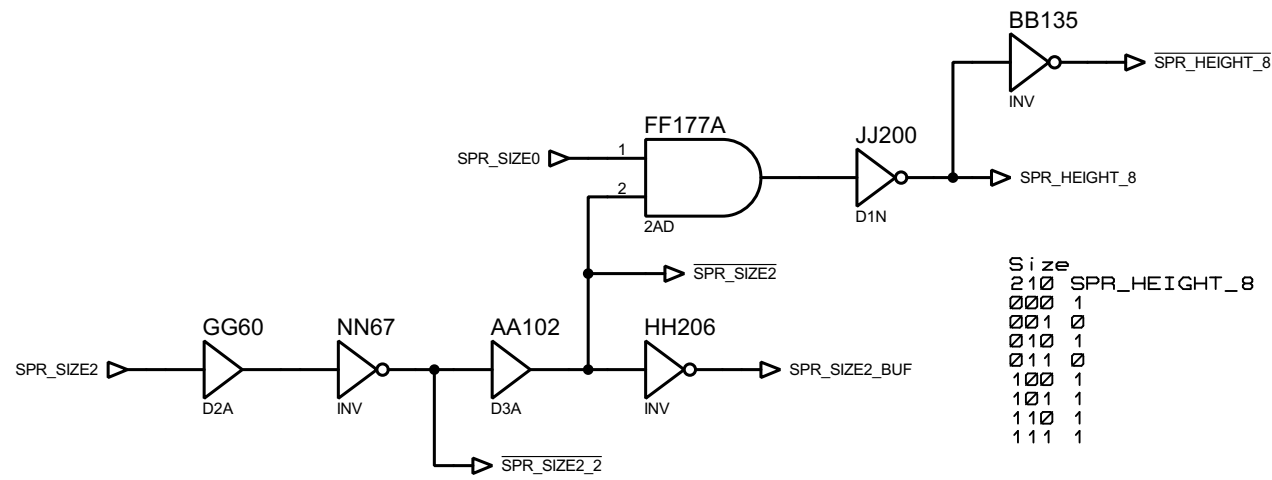






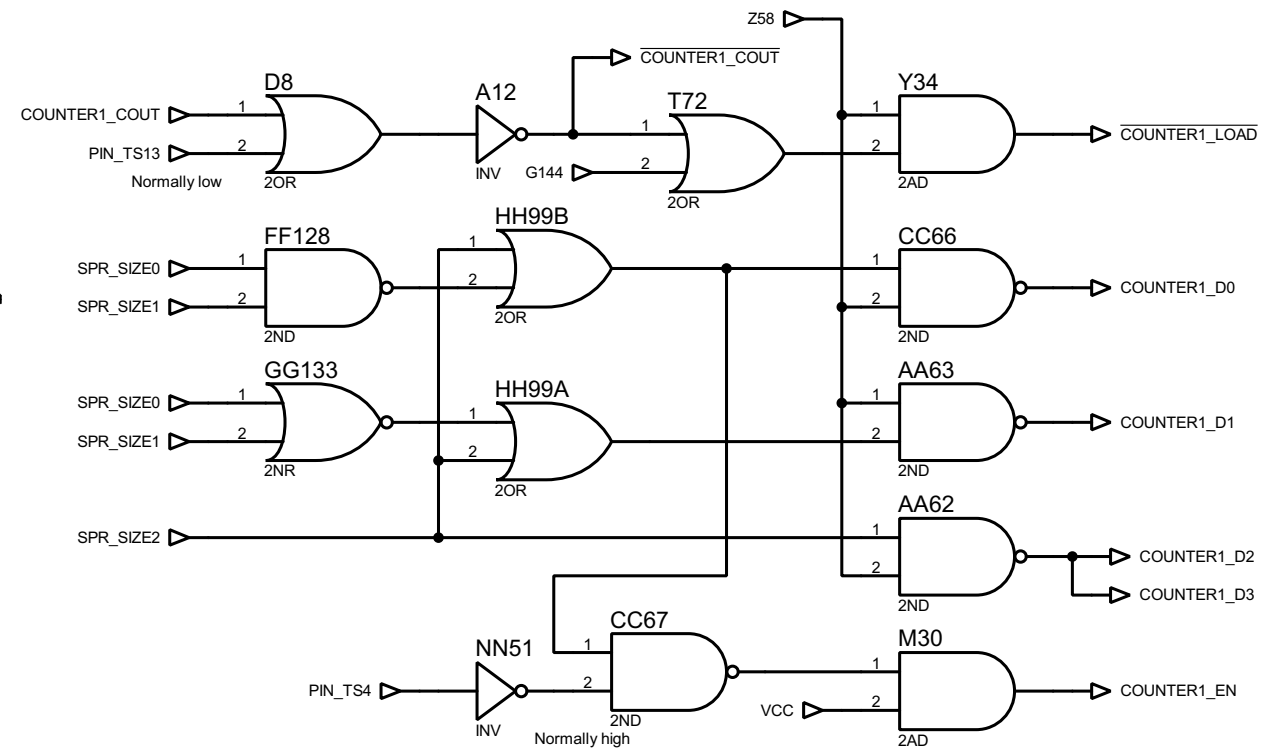




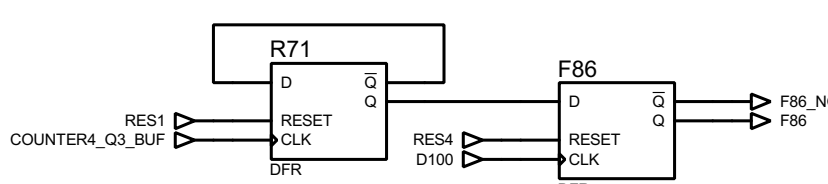
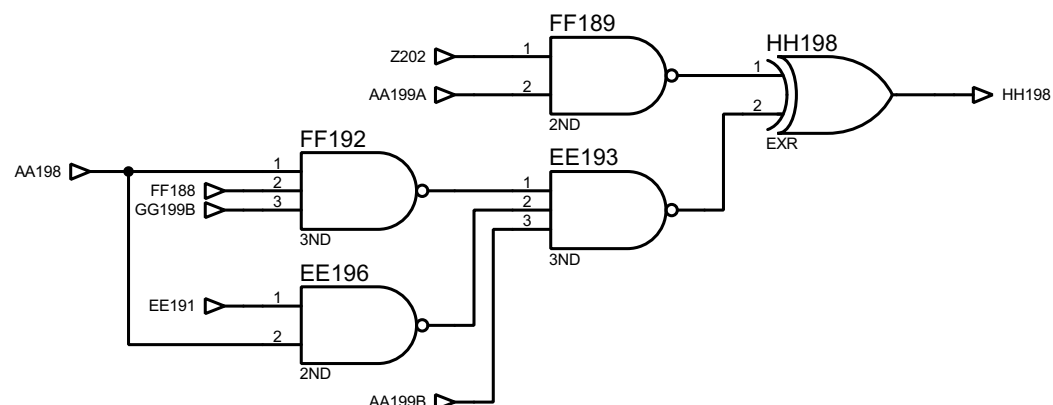
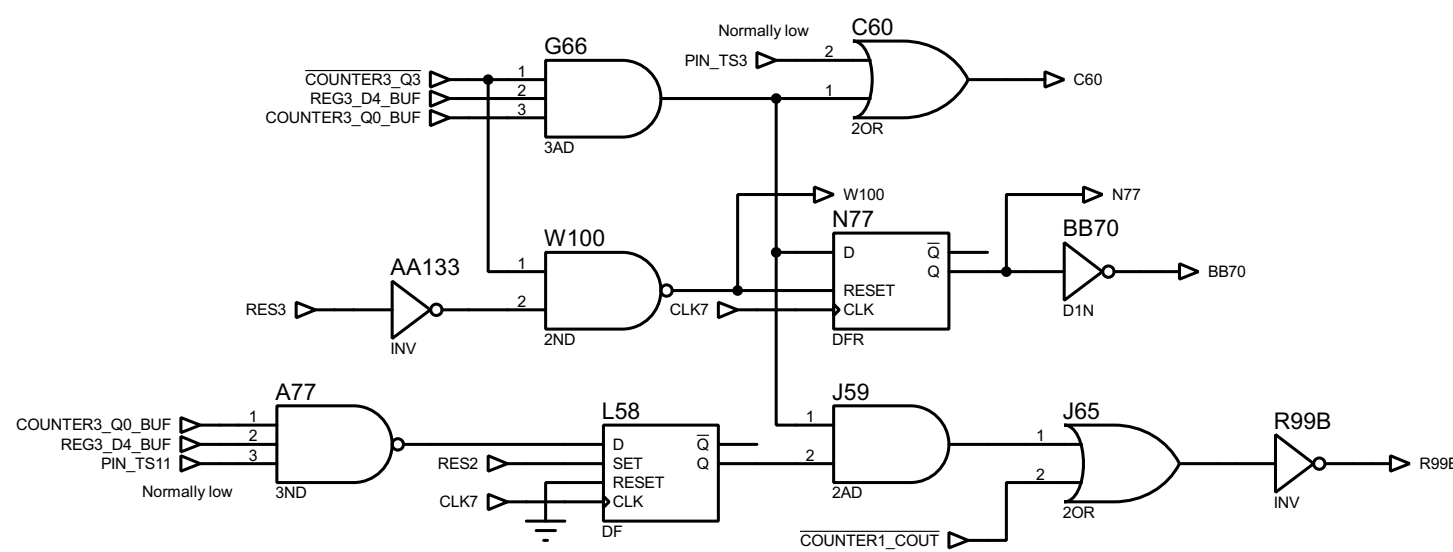
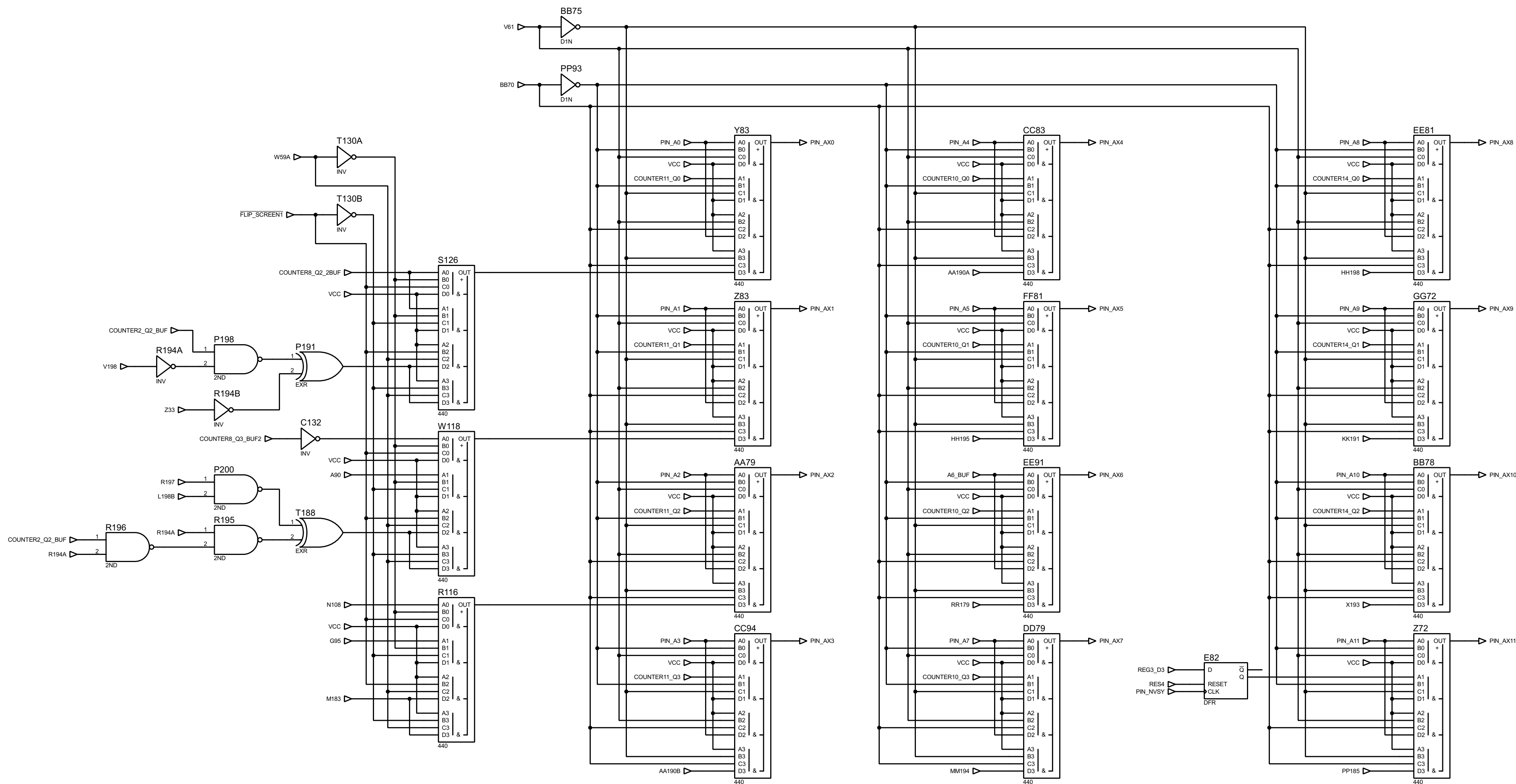


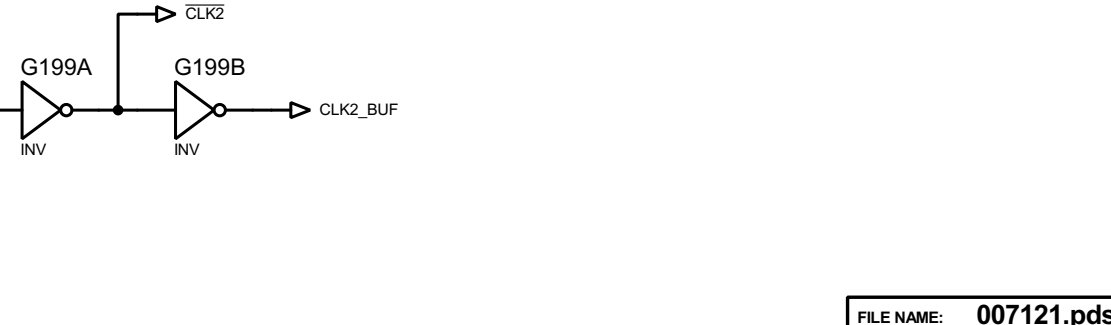
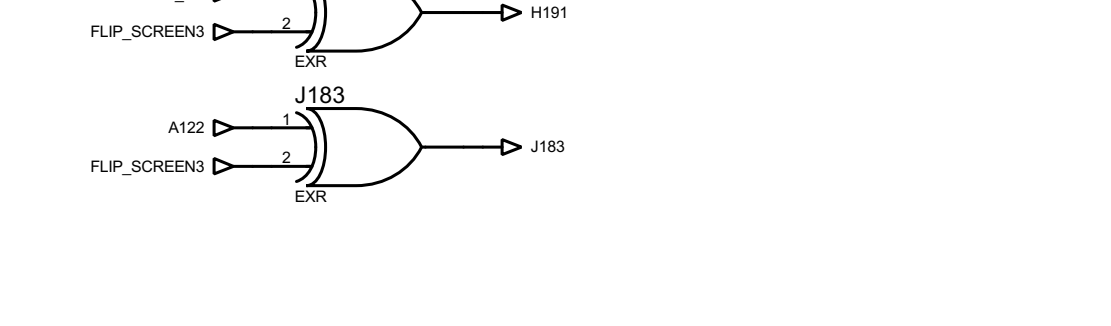
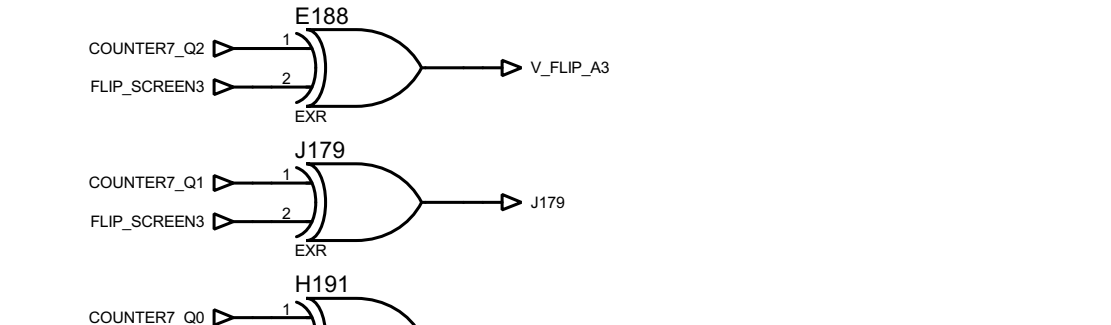
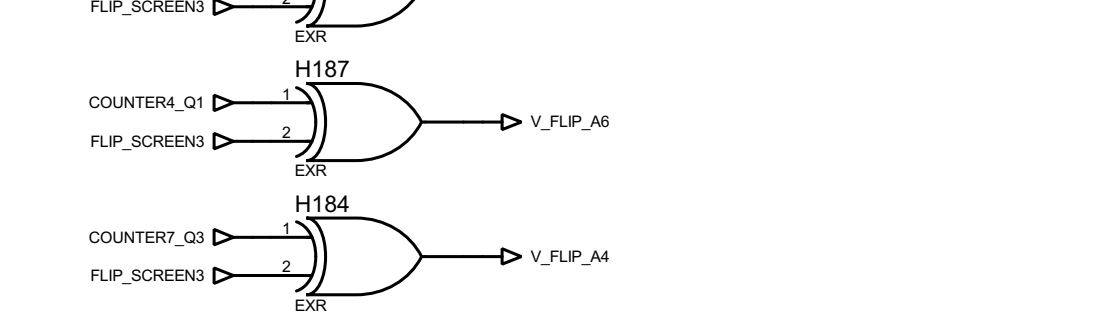
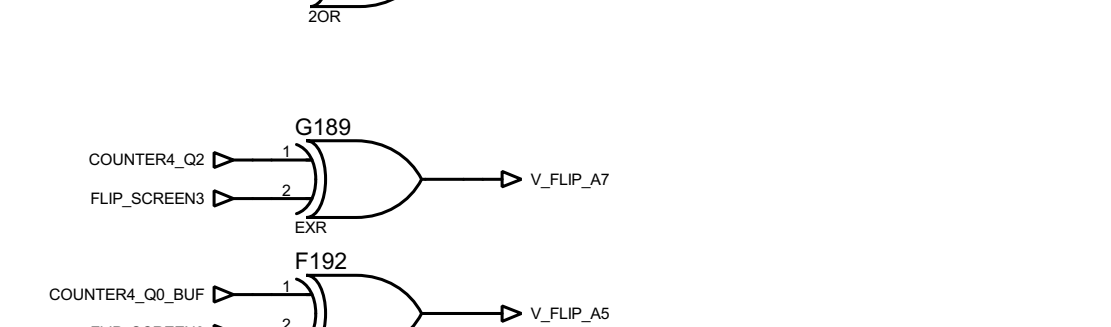
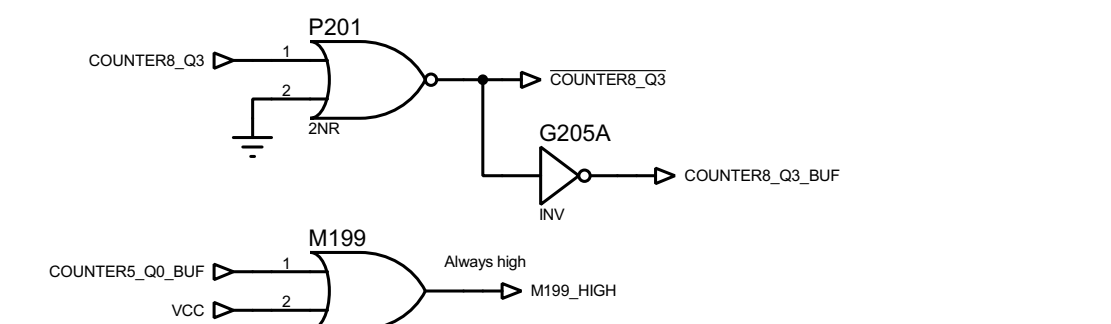
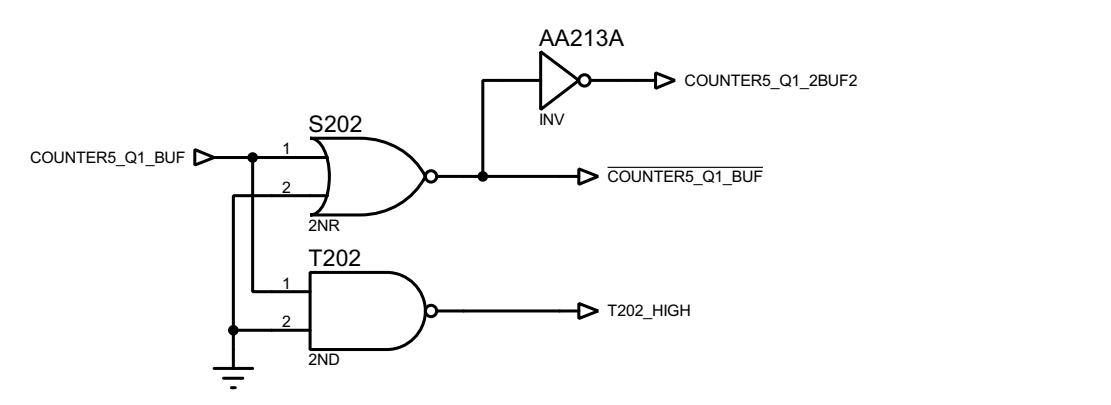
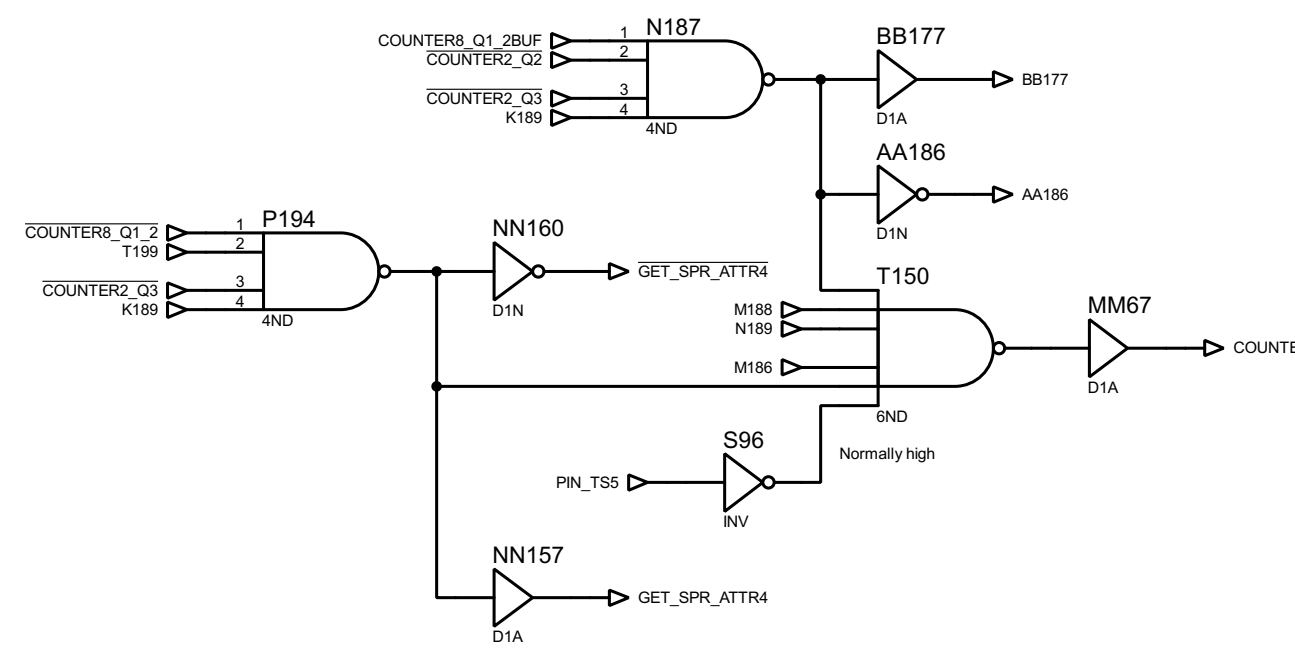
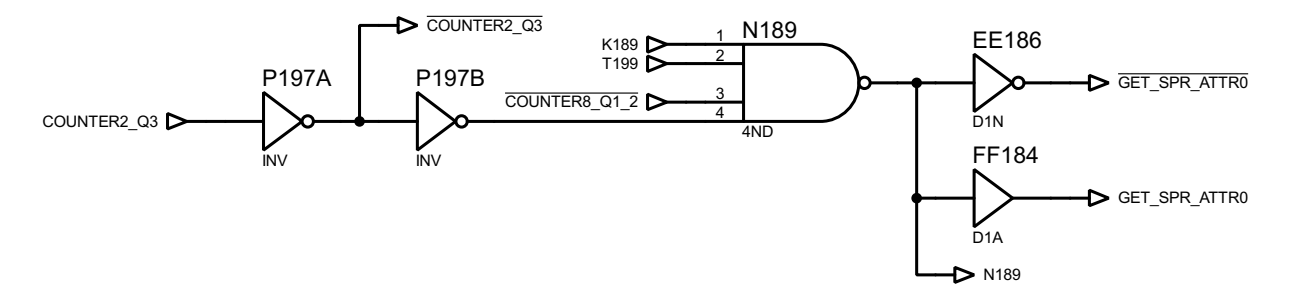
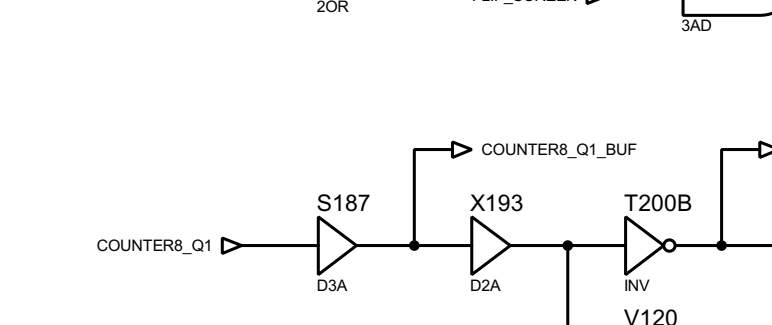
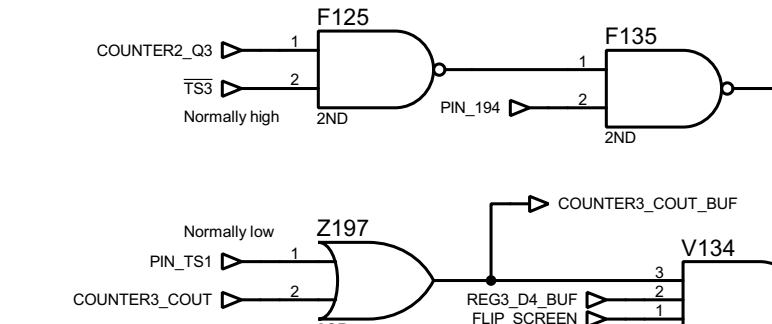
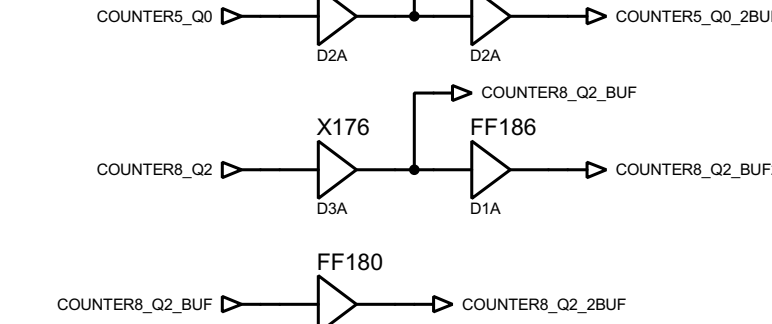
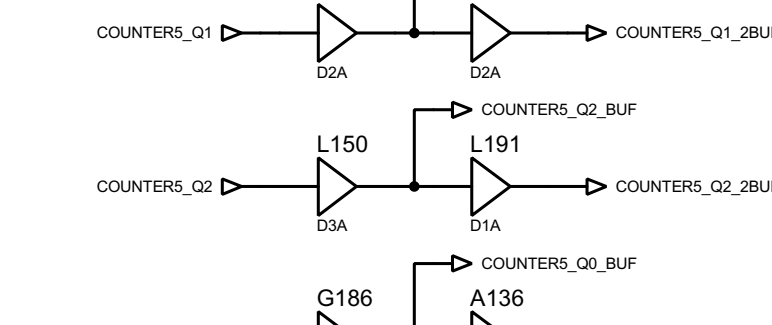
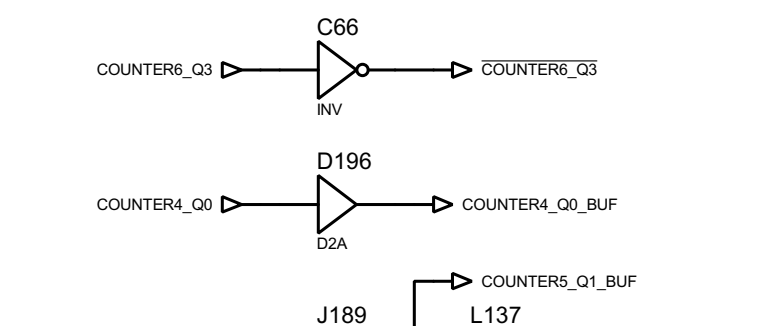
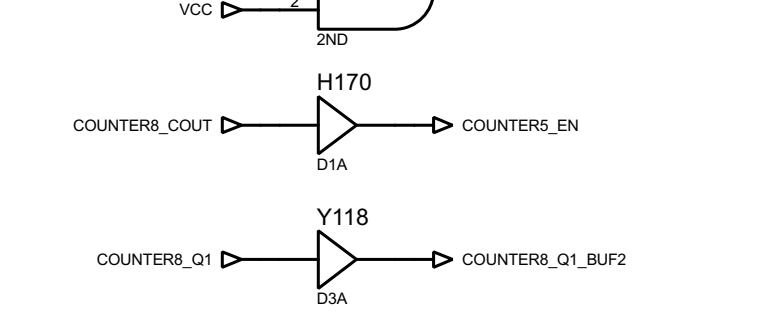
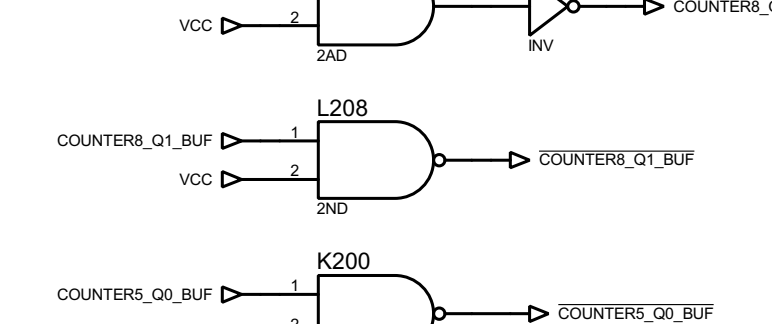
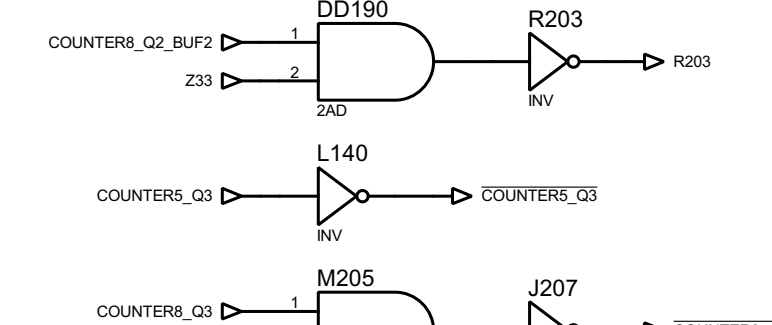
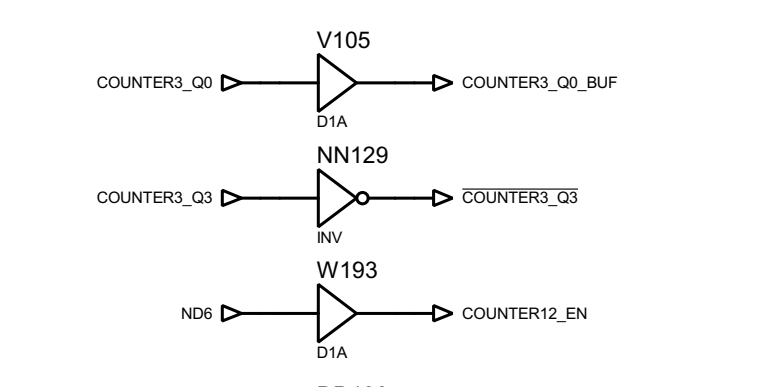
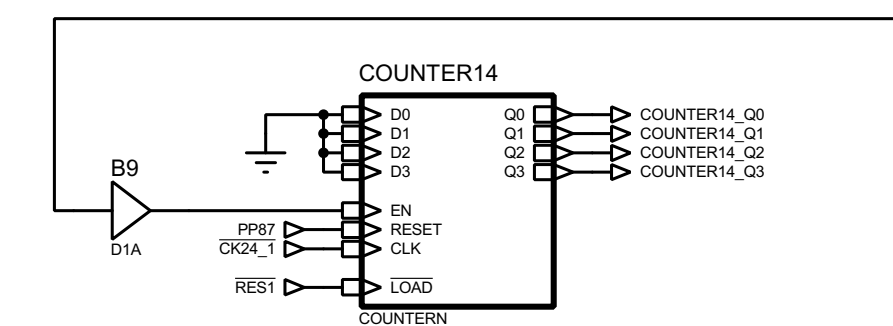
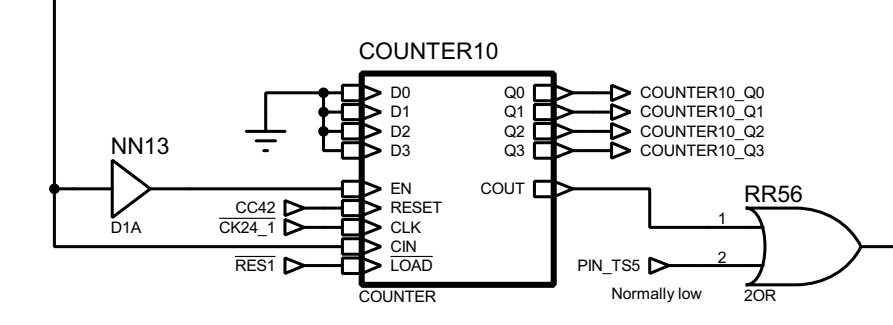
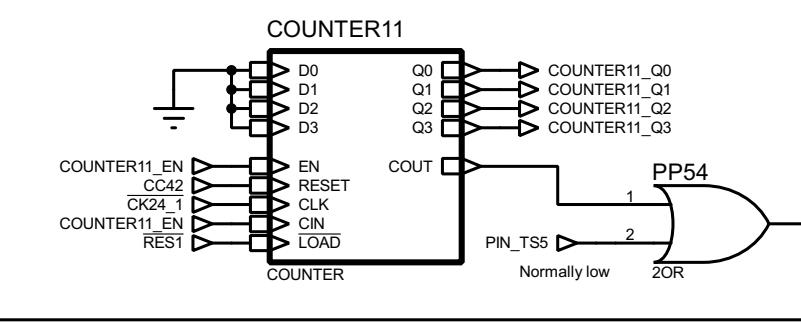
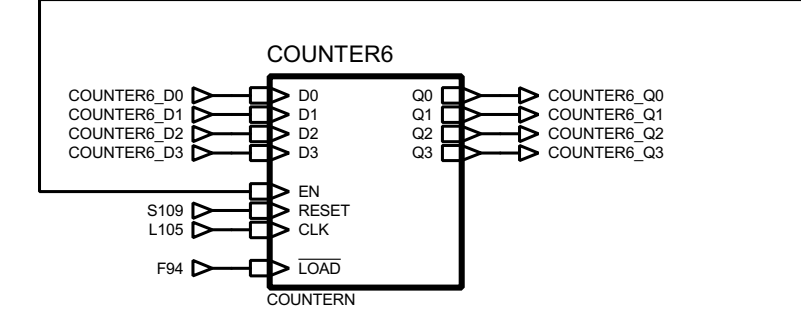
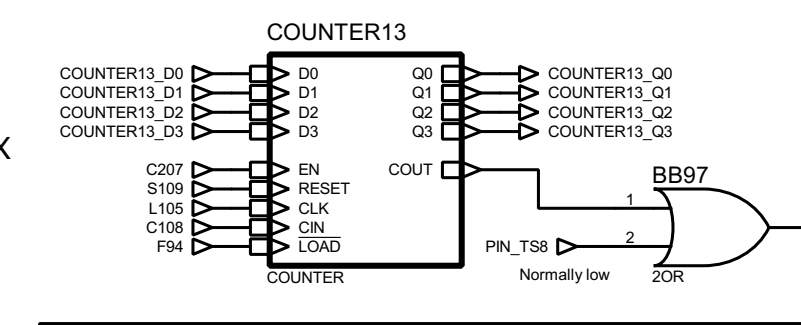
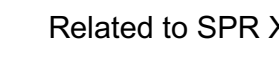
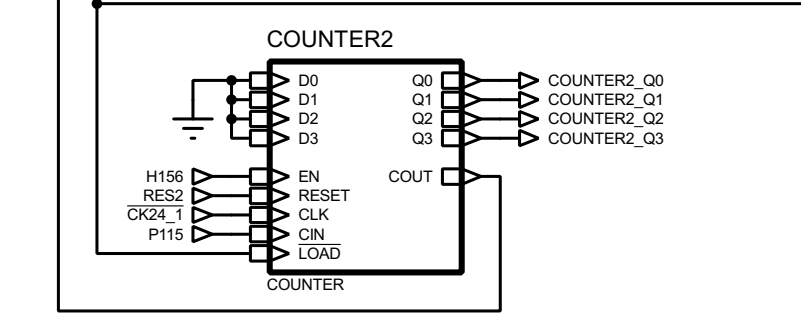
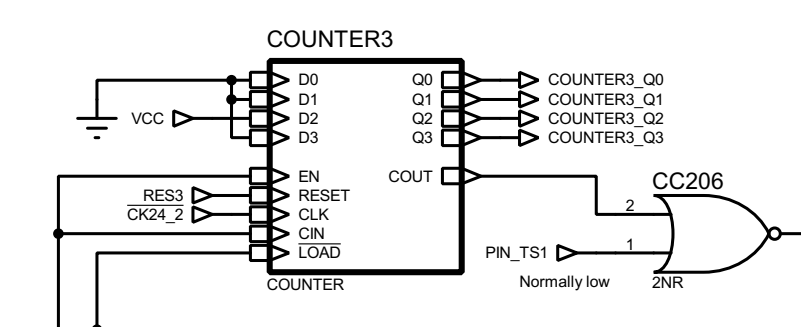
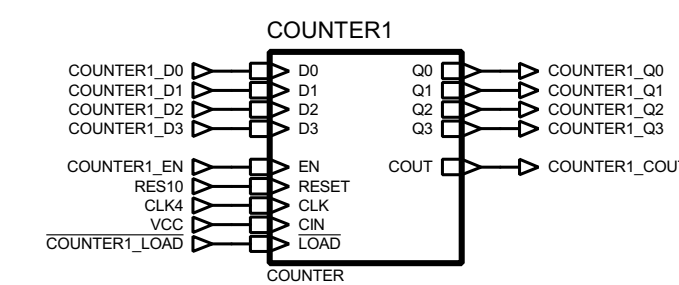
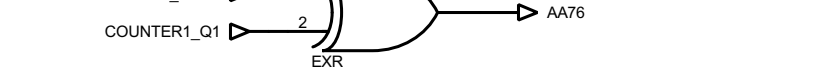
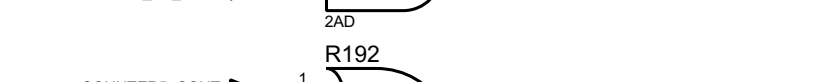
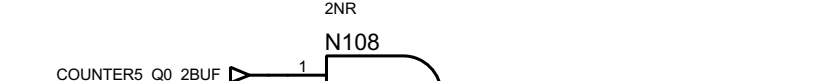
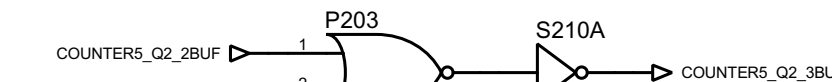
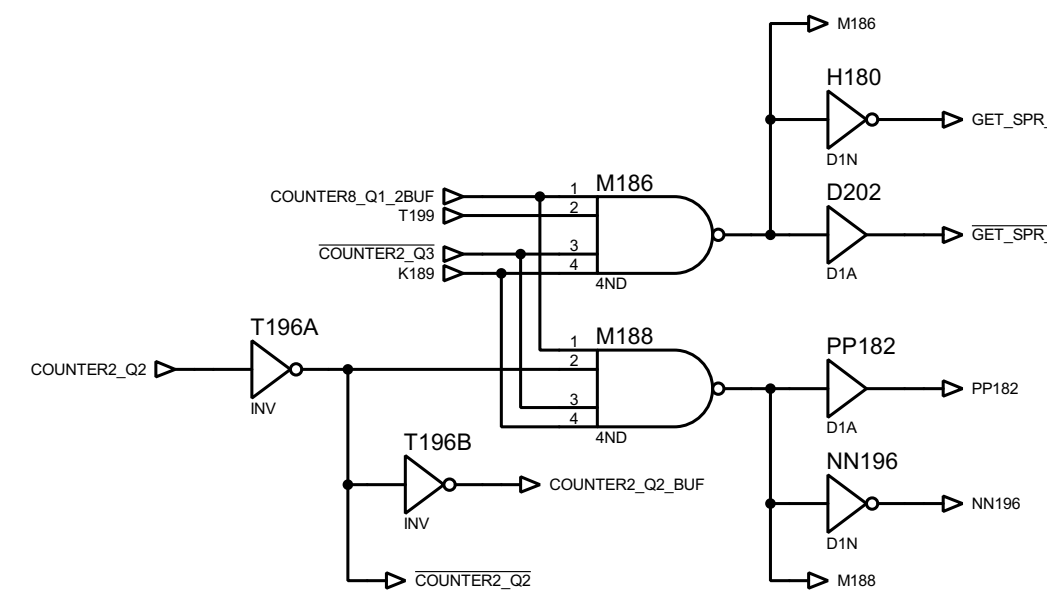
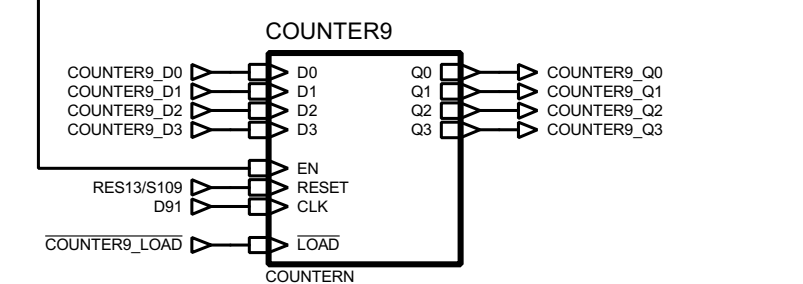
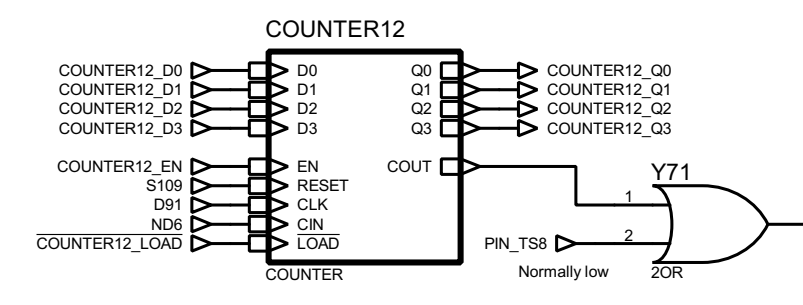
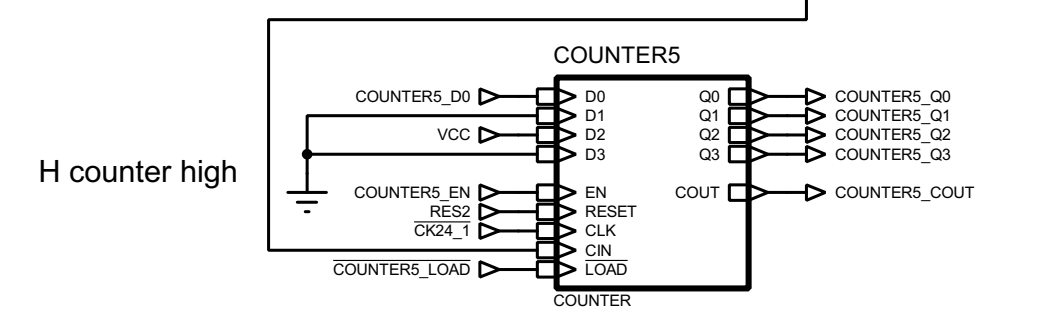
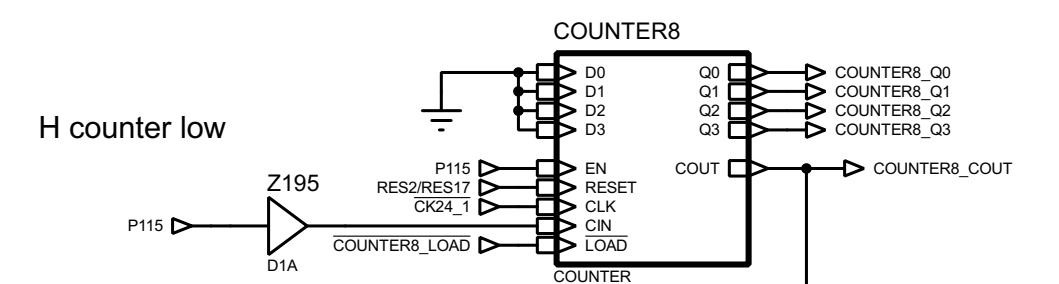
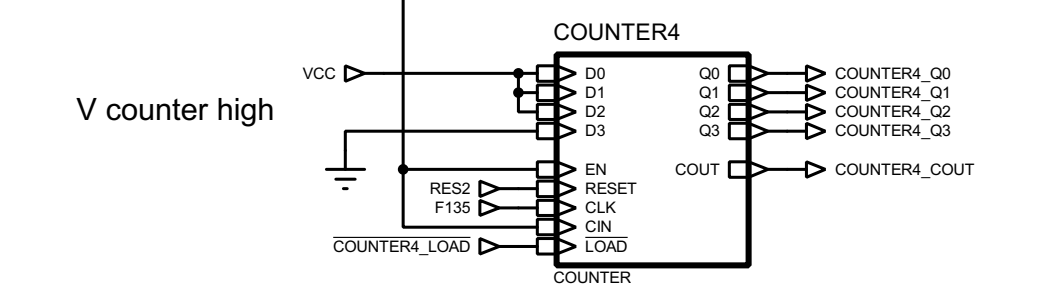
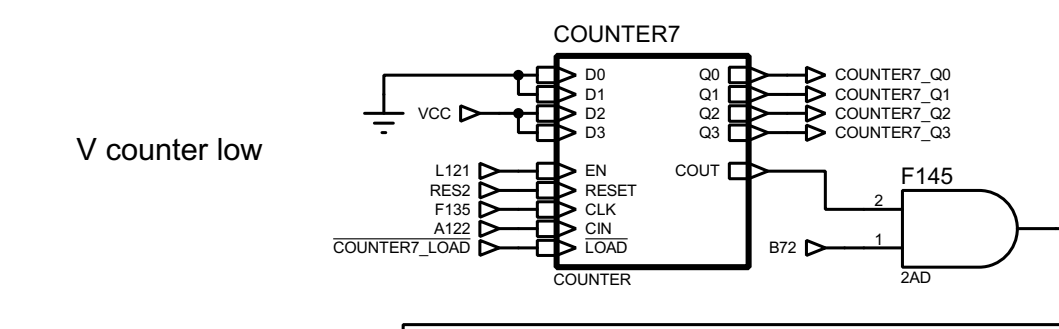
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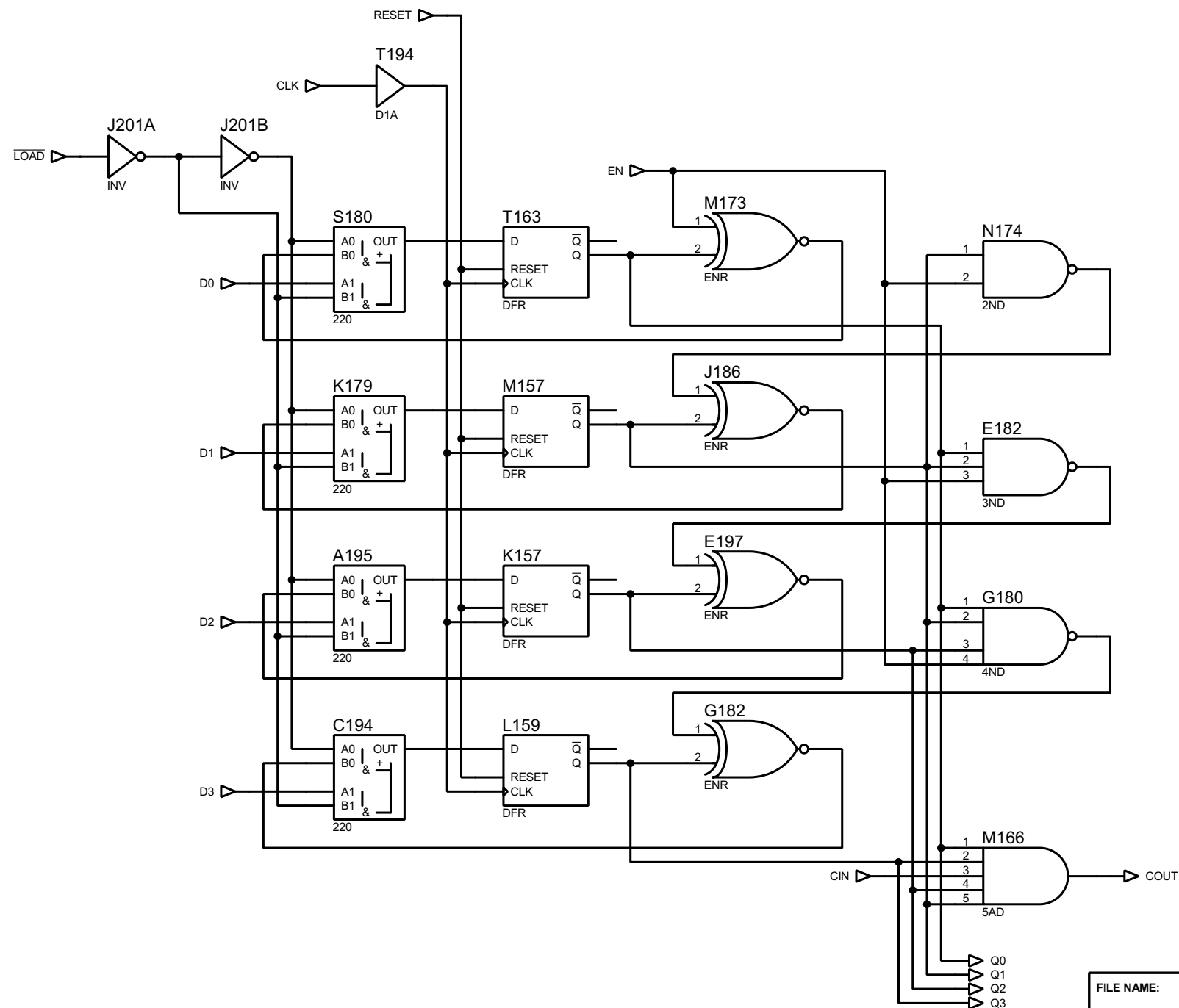
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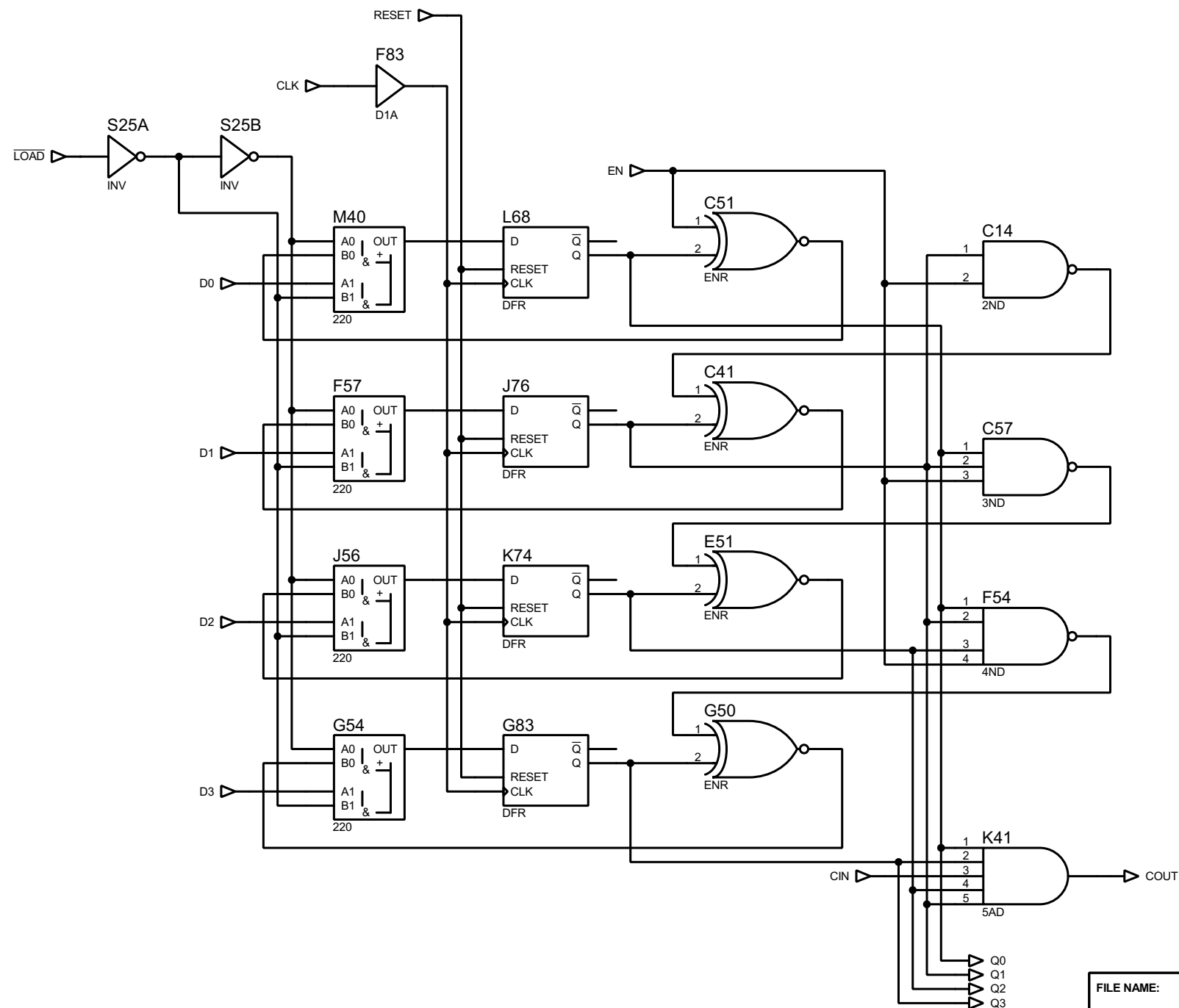
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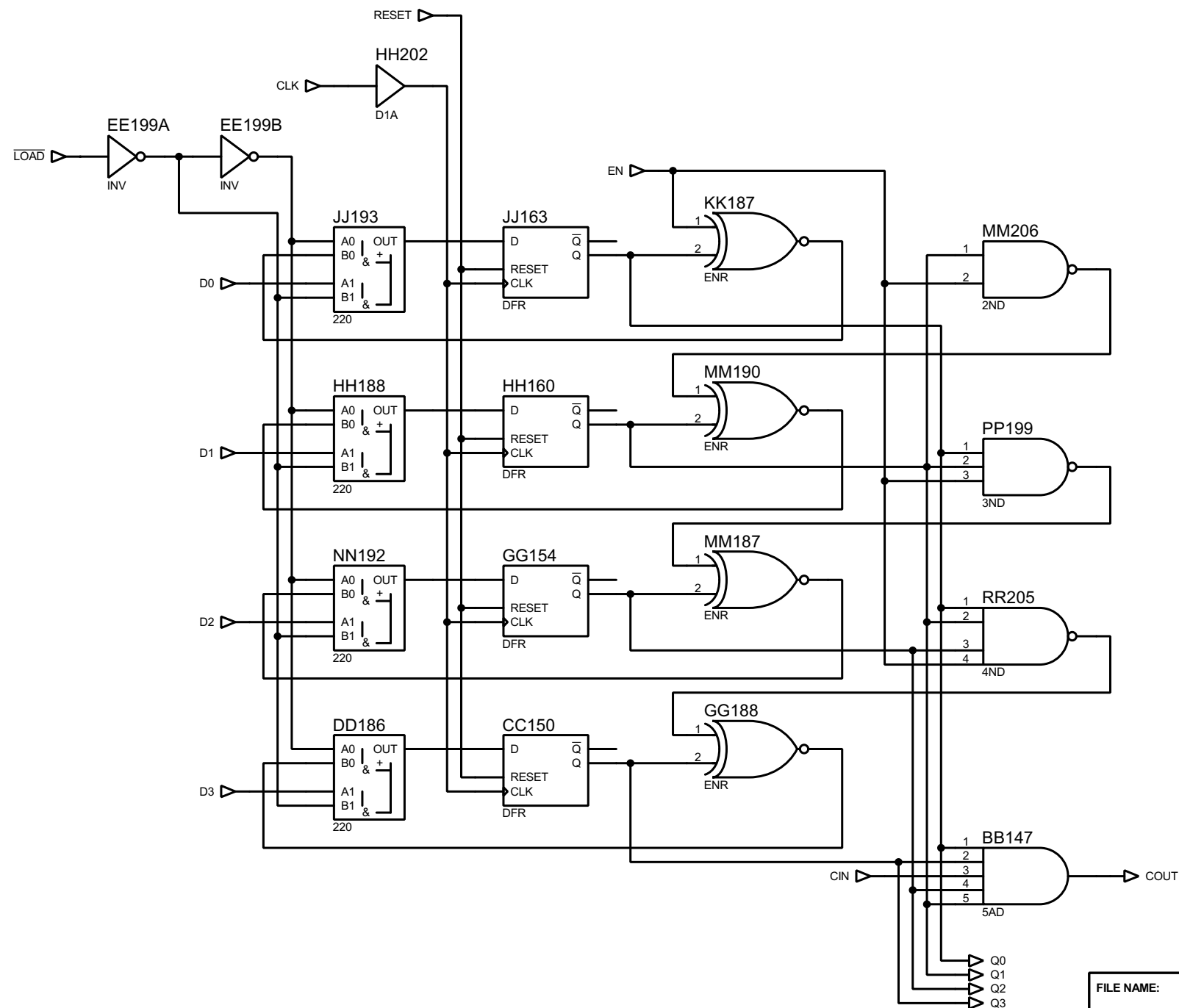


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DESIGN TITLE: Konami 007121	PAGE: 39 of 52
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	TIME: 06:44:38



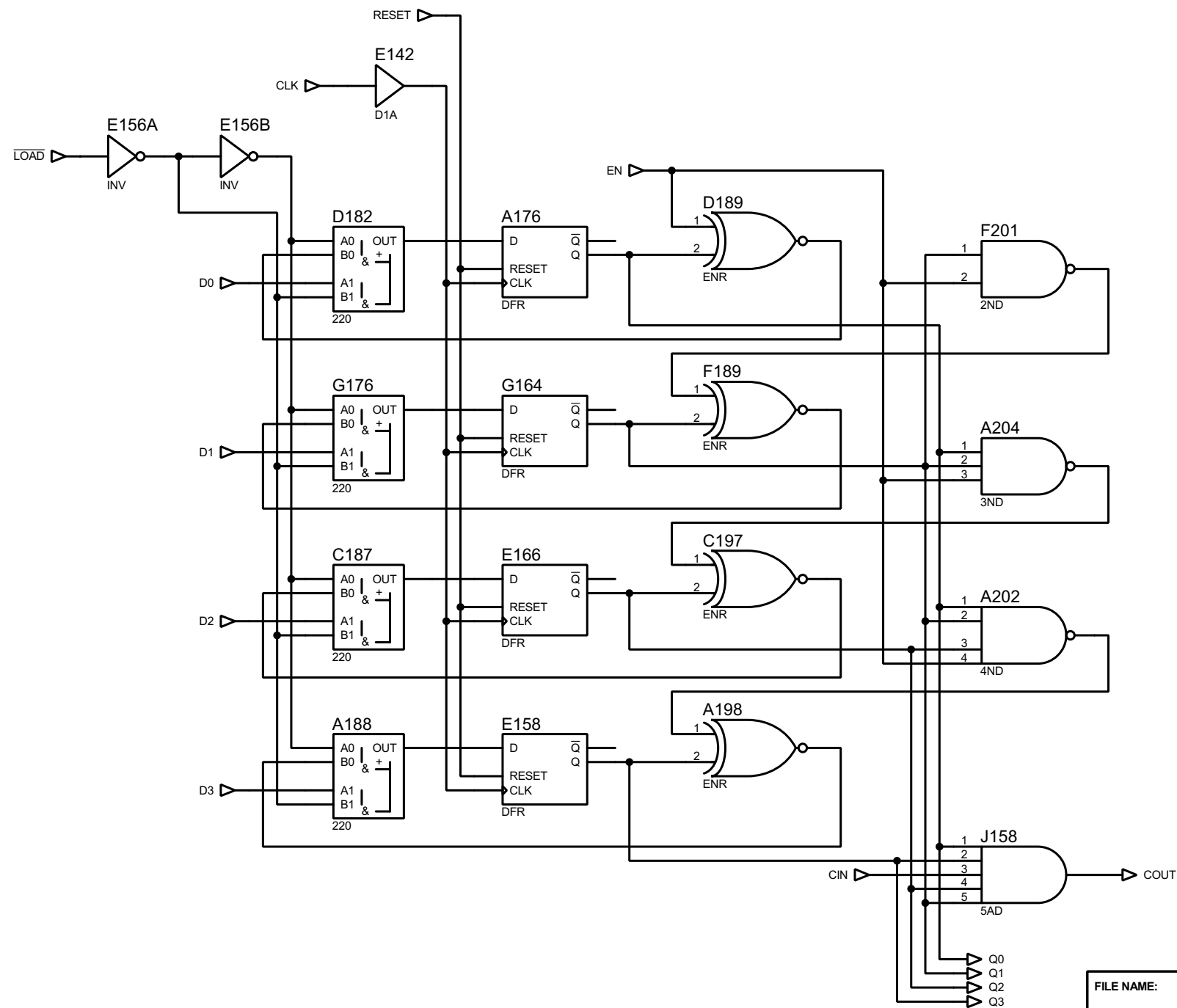
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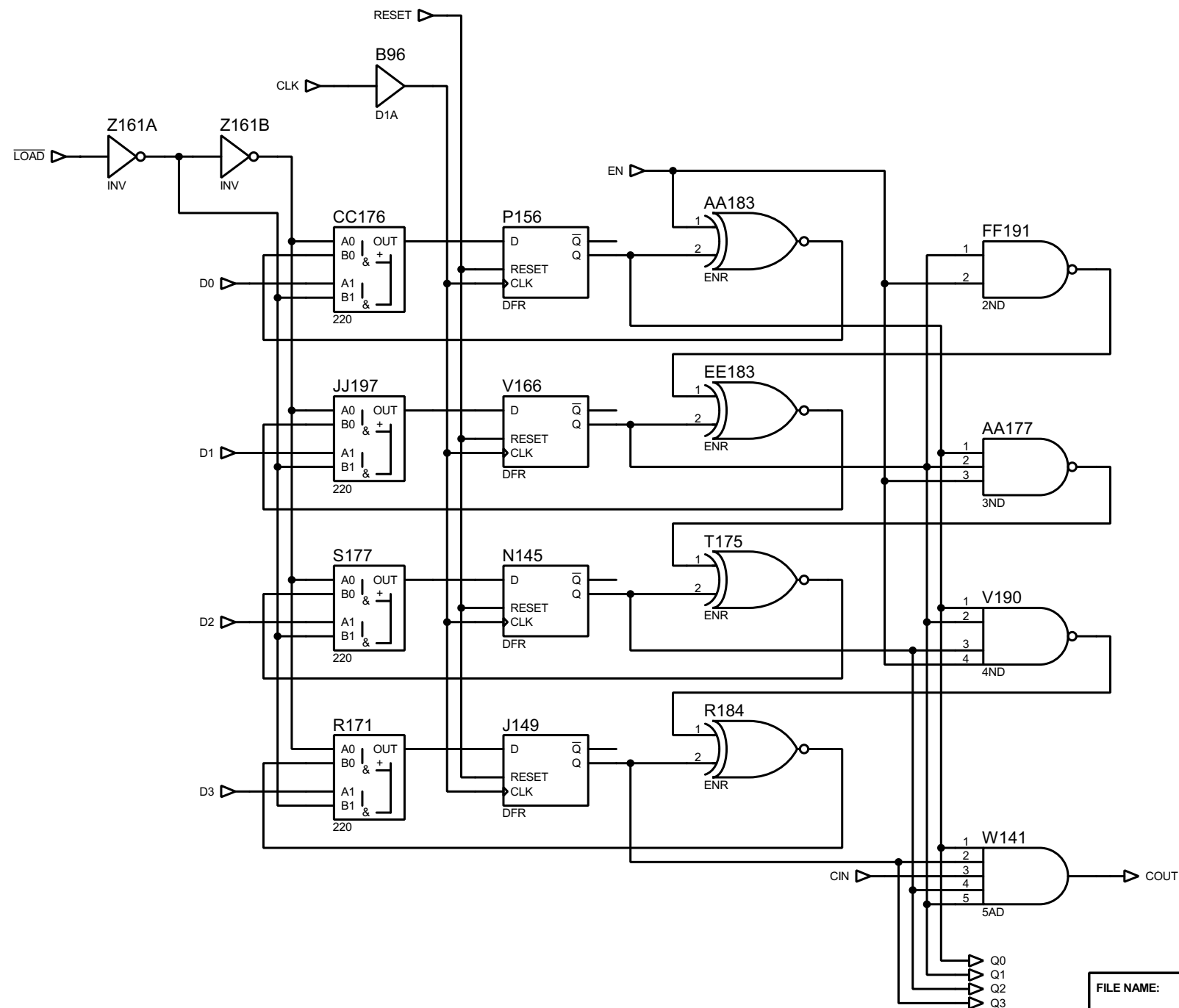


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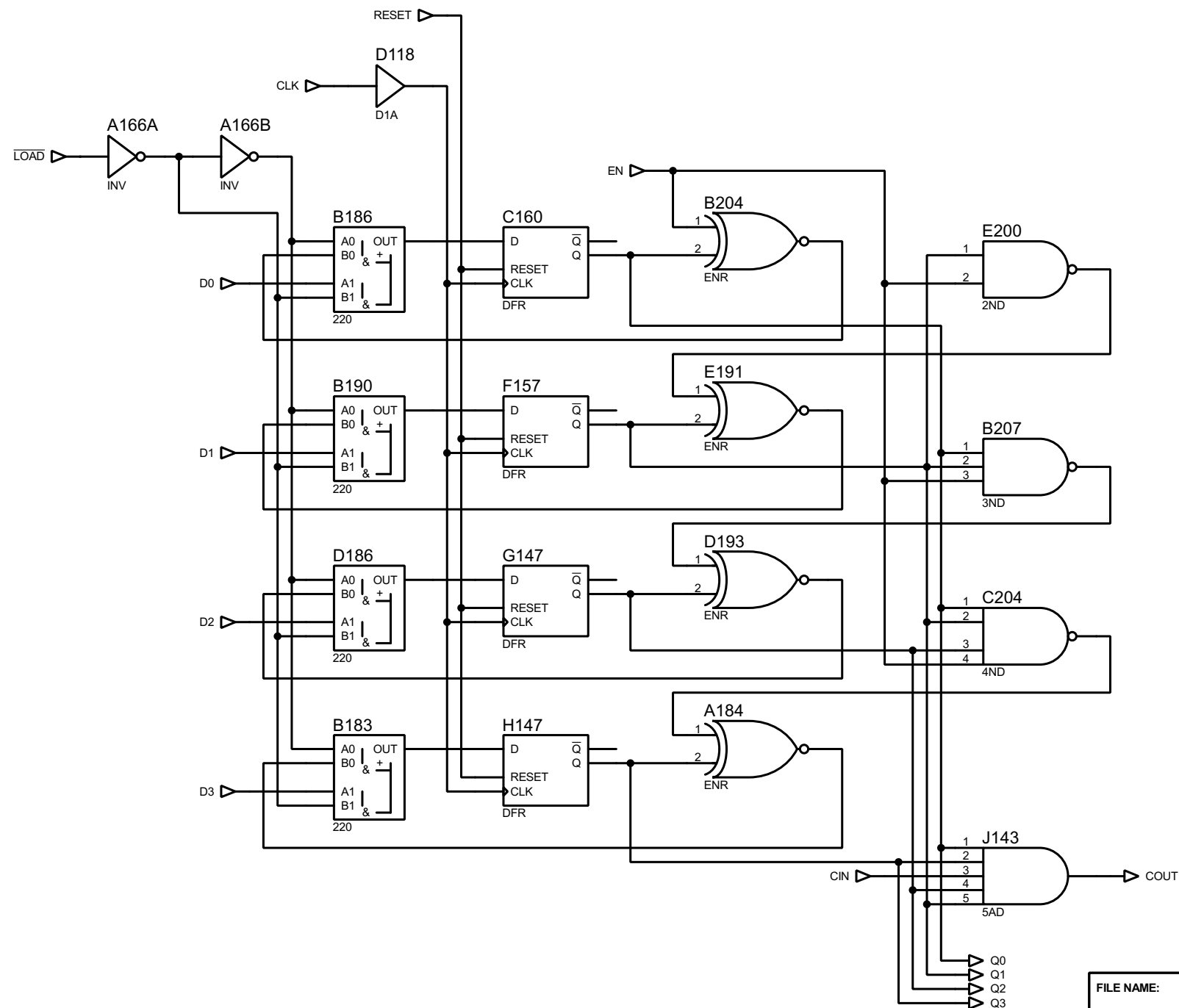




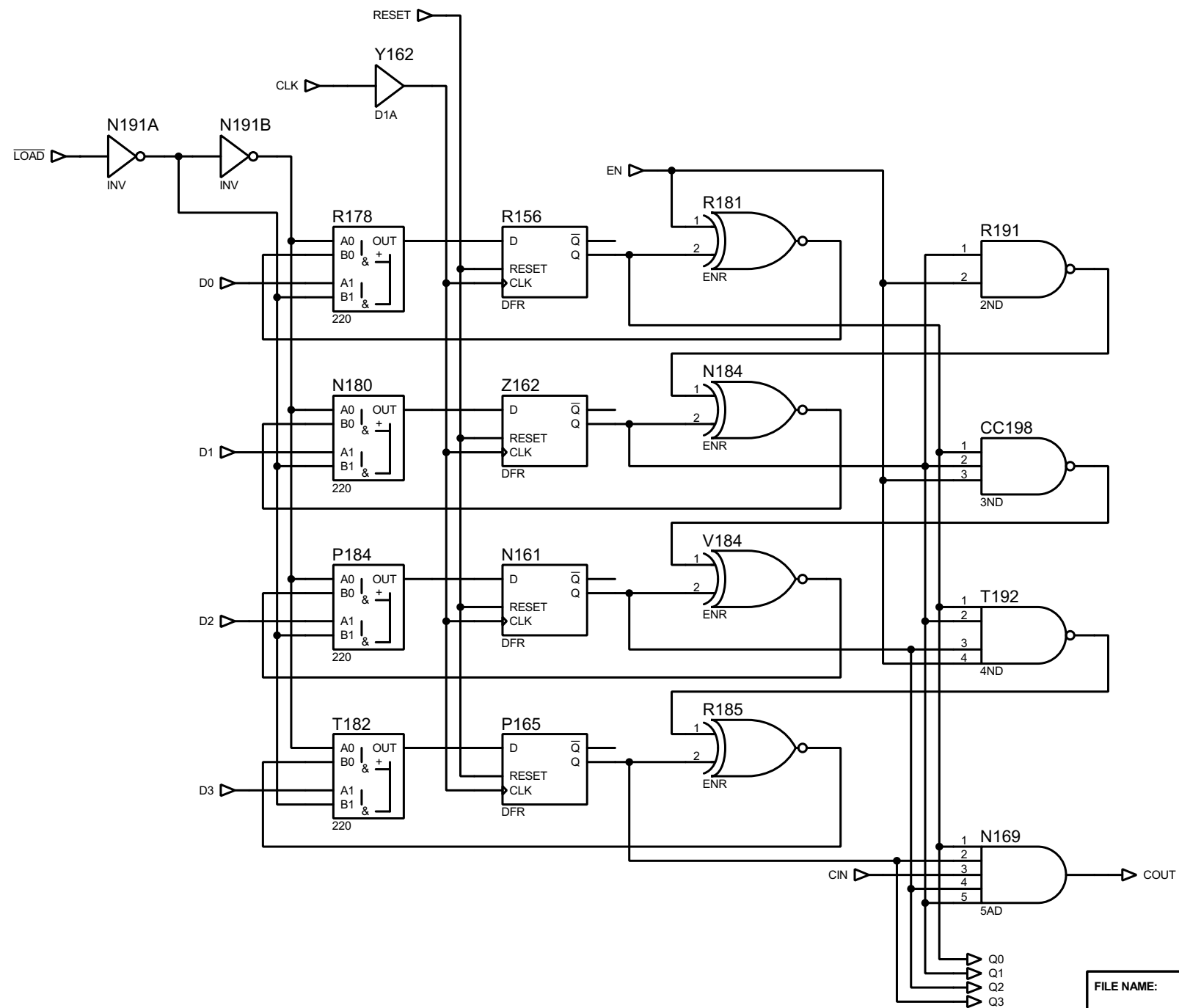
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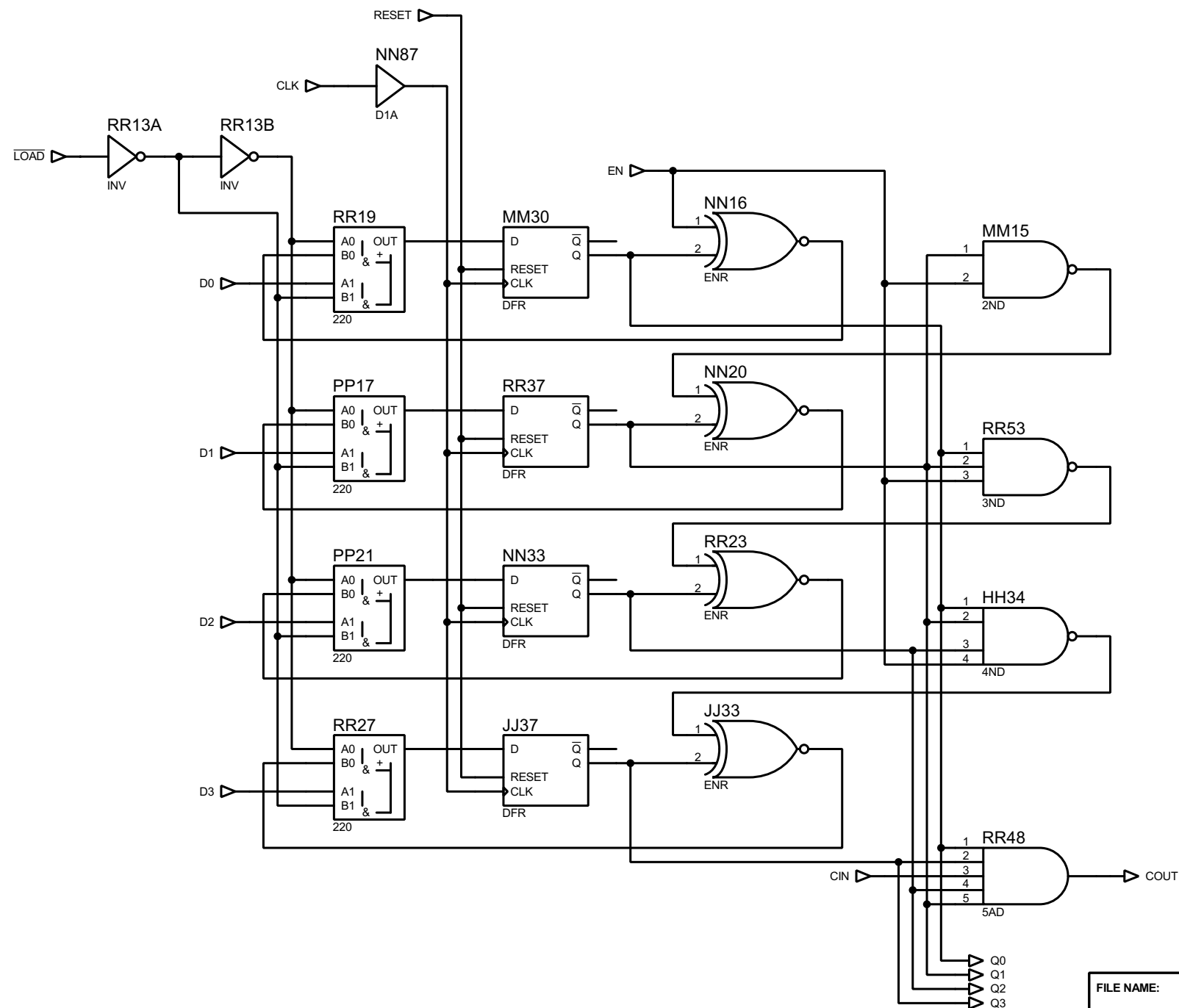
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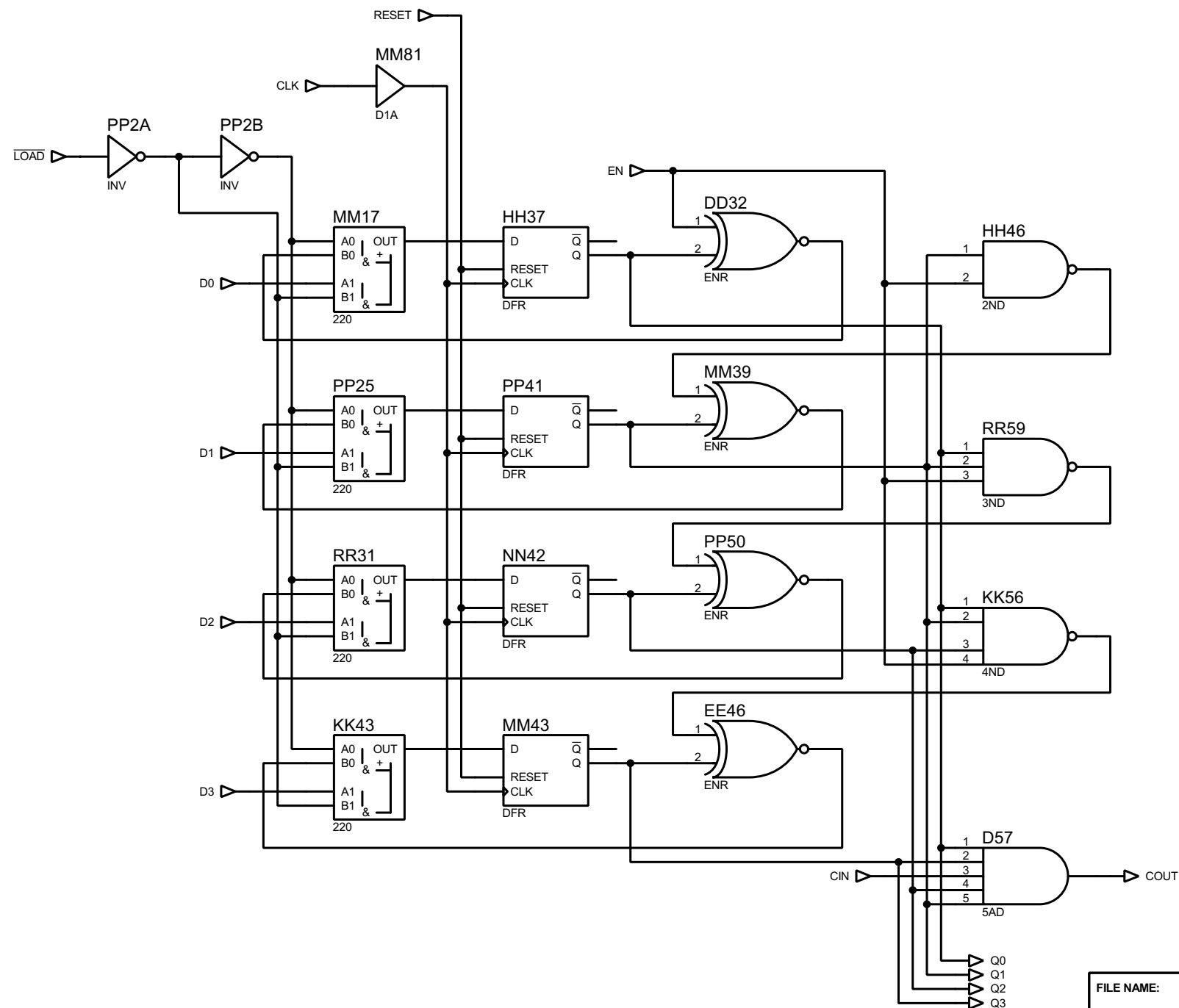
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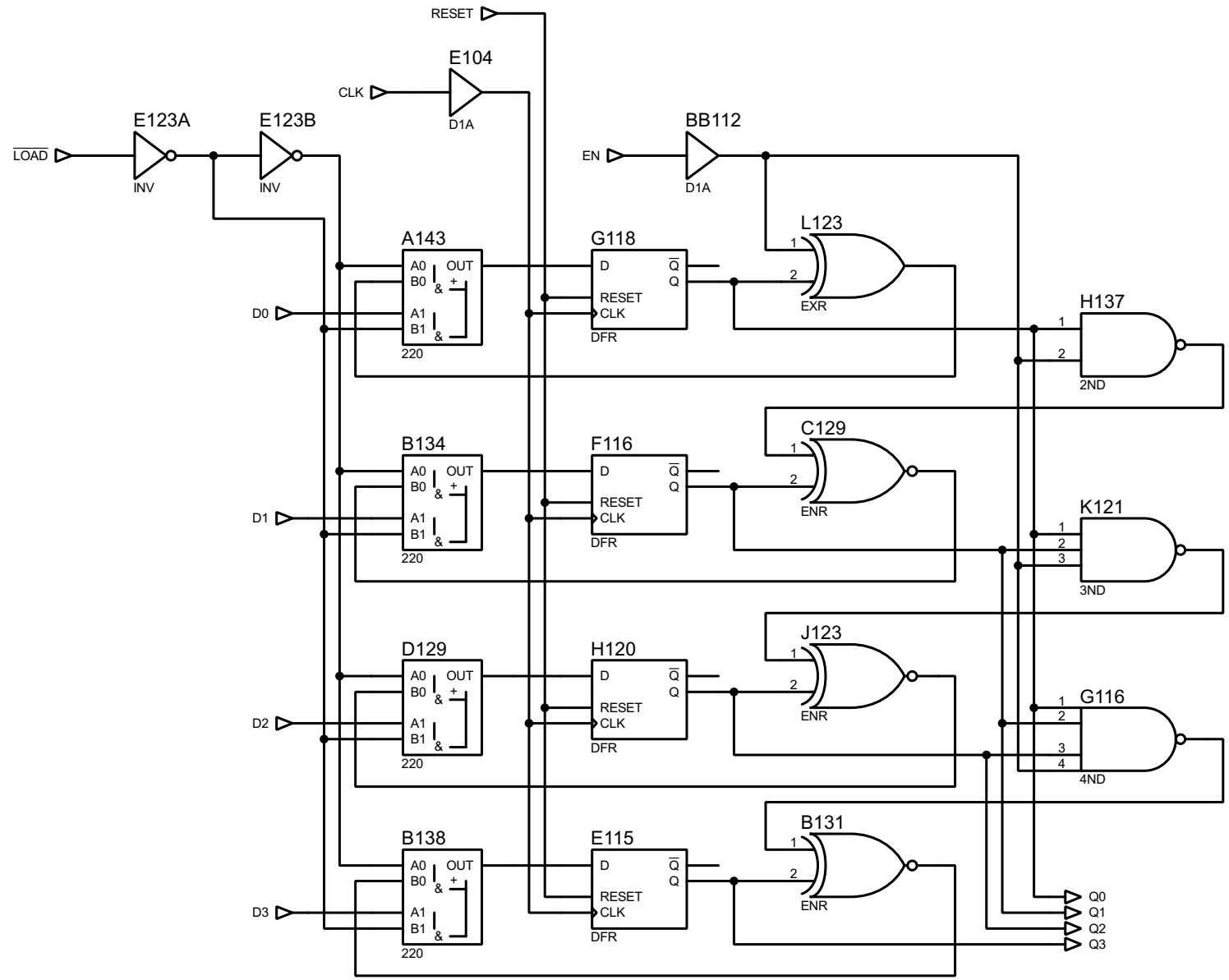
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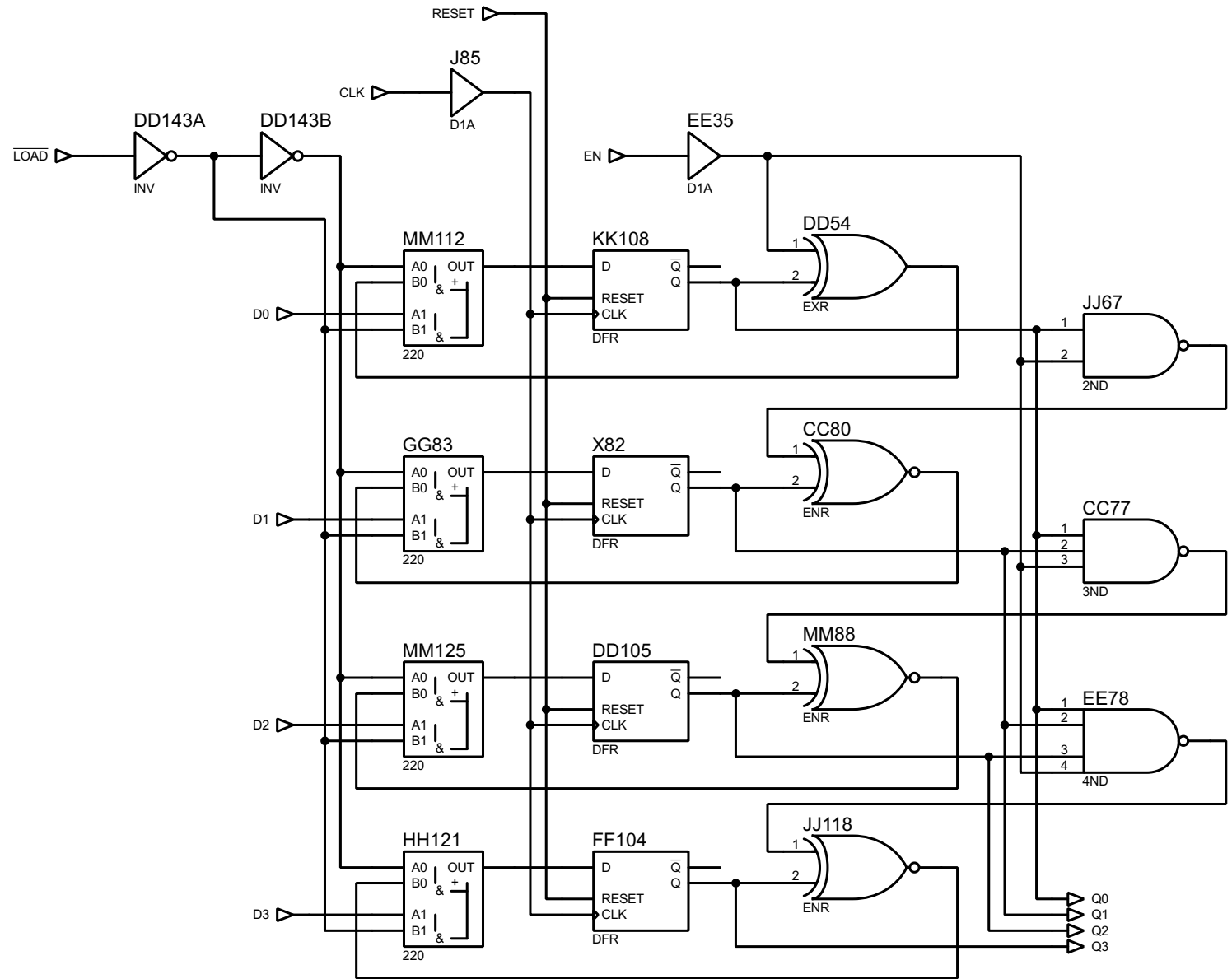
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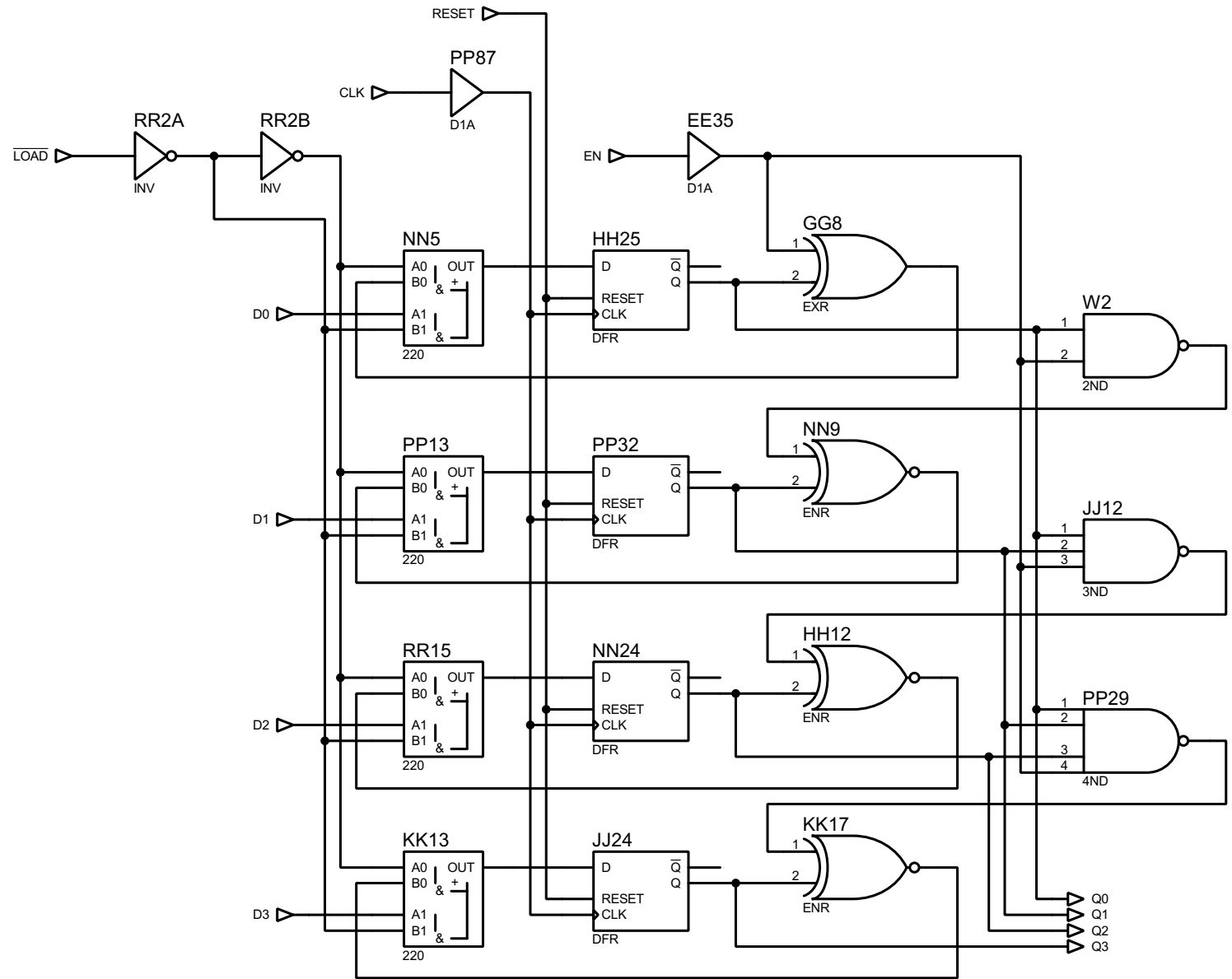
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