

# ASIC QFN Breakout For TinyTapeout 06+

To avoid interfering with signal traces while staying on a 2-layer board, the PDN routes around a good deal. Extra bypass acts as local bulk and compensates for this.

Replace OR with current sense resistors to monitor current on rail

TP1 TP2 TP3 TP4 TP9 GND probe point

U1 ThermalPad

\* I/O pin names: TTE mapping

Openframe\_QFN\_TT06\_REV2

pull-downs used in conjunction with weak PU on demoboard for poor man's ASIC detector circuit.

Other possibility is to use a dedicated I2C eeprom and probe for that.

A\* not used on XX02

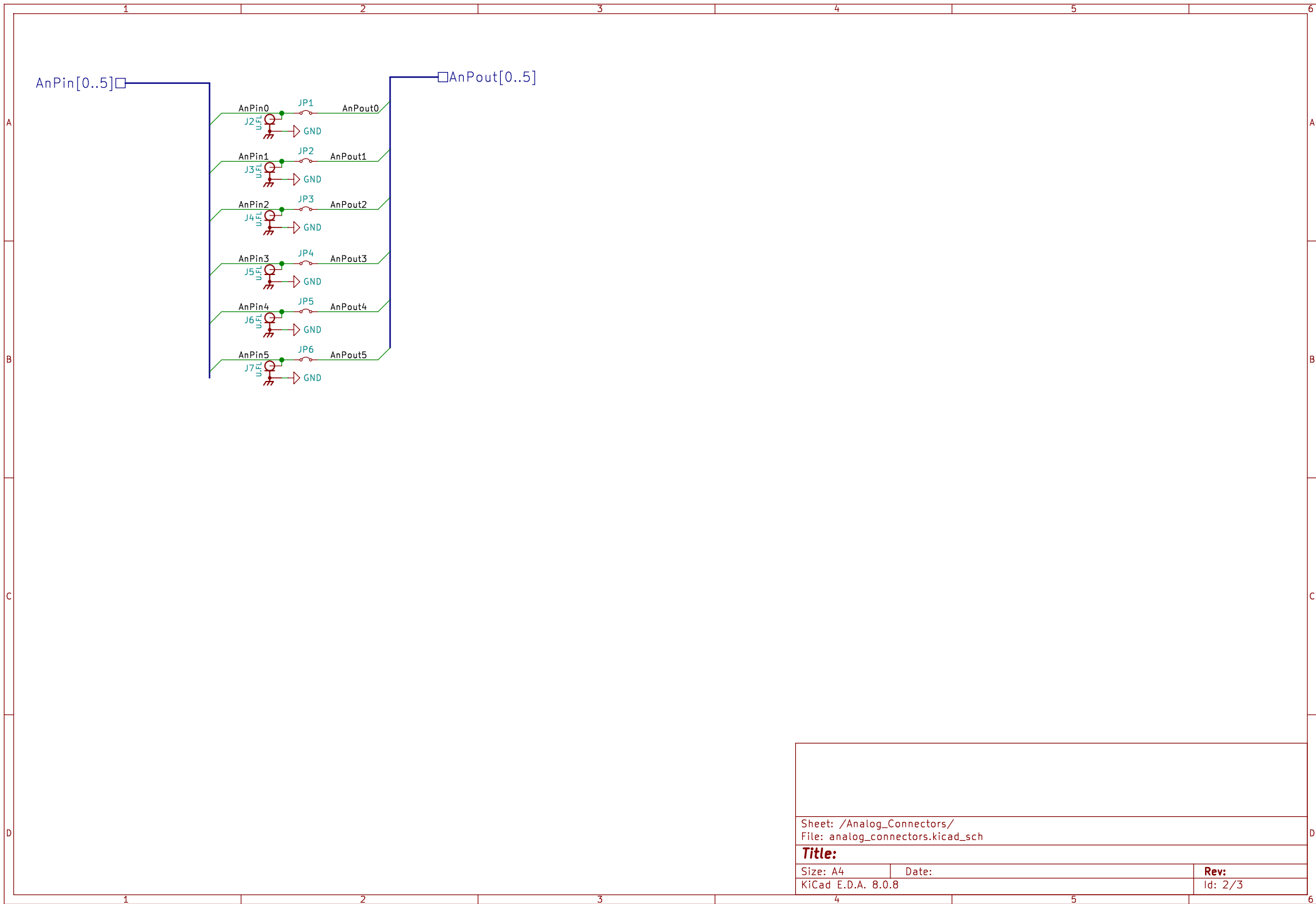
no write protect

Convenience header for ASIC MUX

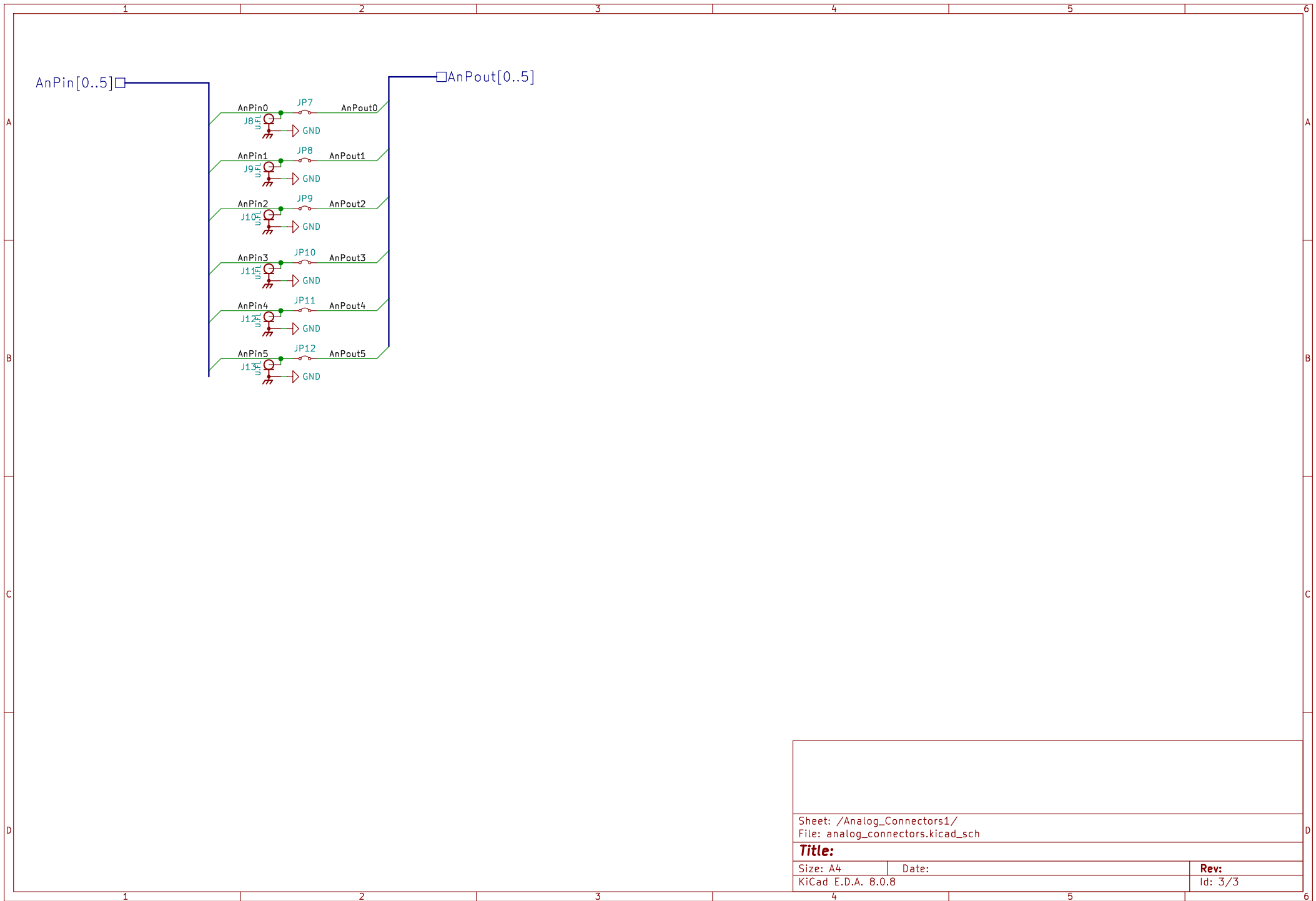
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Psychogenic Technologies  
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**Title: TT06 Openframe breakout**

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