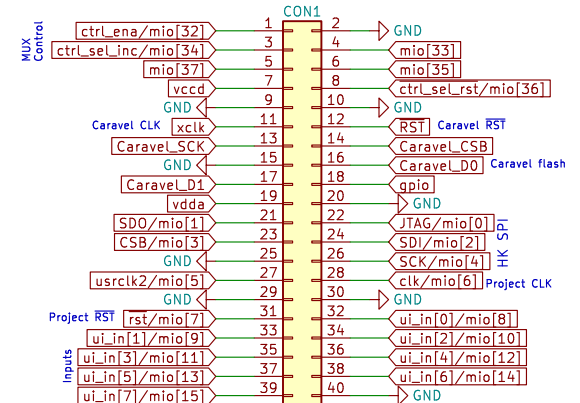


Though the PCB itself has retained the same form-factor, elements included have been re-arranged since prior versions of the breakout board (a breaking change) to allow for objectives above and to provide more room for the eventual FPGA test system.

The pin headers have been placed a sane/easy-to-remember and replicate distance apart on the PCB: 36 millimeters, center-to-center horizontally and at the same height.

As of v3.2, we've moved to standard top-entry 100 mil headers, meaning they must be assembled on the underside of the PCB. Any standard pin socket should do, but guide post holes are to support the inexpensive Liansheng FH-00148



Pin	Function	Direction
1	vdda2	Bidirectional
2	GND	Input
3	vdda2	Bidirectional
4	GND	Input
5	uio[6]/mio[30]	Bidirectional
6	uio[7]/mio[31]	Bidirectional
7	uio[4]/mio[28]	Bidirectional
8	uio[5]/mio[29]	Bidirectional
9	uio[2]/mio[26]	Bidirectional
10	uio[3]/mio[27]	Bidirectional
11	uio[1]/mio[25]	Bidirectional
12	GND	Input
13	GND	Input
14	vcccd2	Input
15	vddio	Input
16	vcccd2	Input
17	vddio	Input
18	GND	Input
19	GND	Input
20	GND	Input
21	uo_out[7]/mio[23]	Output
22	uio[0]/mio[24]	Output
23	uo_out[5]/mio[21]	Output
24	GND	Output
25	GND	Output
26	uo_out[6]/mio[22]	Output
27	uo_out[3]/mio[19]	Output
28	uo_out[4]/mio[20]	Output
29	uo_out[1]/mio[17]	Output
30	uo_out[2]/mio[18]	Output
31	GND	Output
32	uo_out[0]/mio[16]	Output
33	GND	Output
34	GND	Output
35	vccd1	Output
36	GND	Output
37	vccd1	Output
38	GND	Output
39	vdda1	Output
40	GND	Output

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