TinyTapeout ASIC Simulator (for TT3)

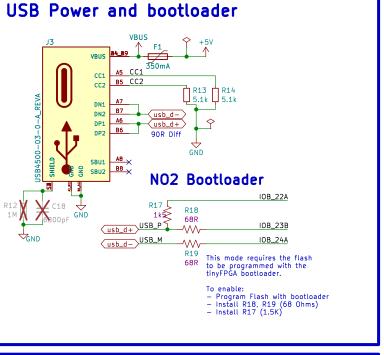
This breakout maps an ICE40UP5K (SG48ITR) FPGA to the TT 1,2 and 3 breakout board.

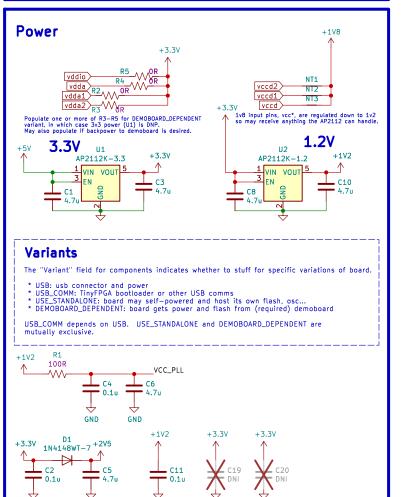
It is designed to allow both stand—alone use and for interaction and support from the TT demo board.

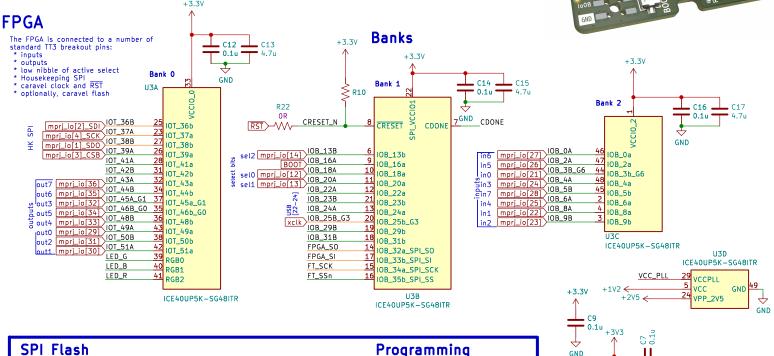
When used with the demo board, few components are required as the hosting PCB can provide power as well as the required serial flash.

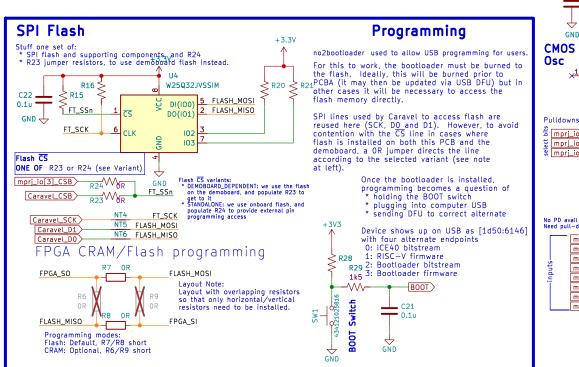
The bootloader may be used along with the USB interface to allow for simple updates via DFU (see Flash and programming sections below).

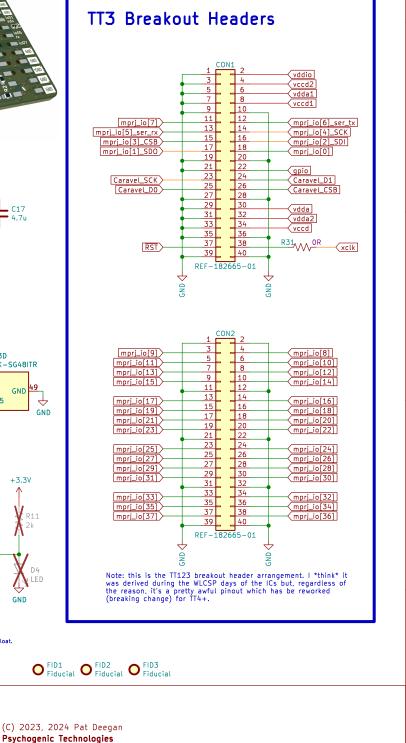
An oscillator, flash and regulation are provided for stand-alone operation (see Variants note).











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File: caravel-breakout-fpga-ICE40UP5K-TT3.kicad_sch

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