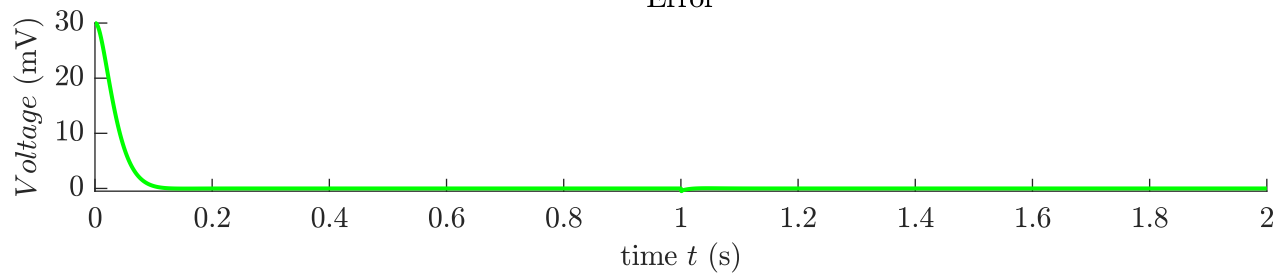
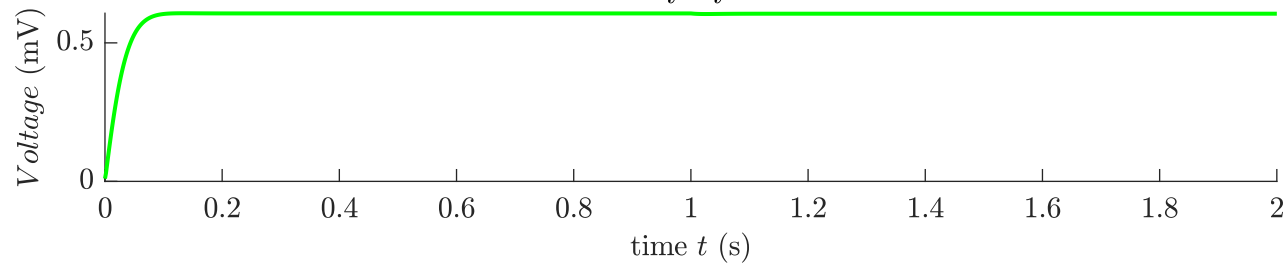


Error



Duty Cycle



Resistance

