American International University- Bangladesh
Department of Electrical and Electronic Engineering
EEE2104: Electronic Devices Laboratory

Title: Study of Diode Rectifiers

Abstract:
A diode rectifies an ac voltage, so that it can be smoothed and converted into a dc voltage. A rectifier, however, can produce a constant or variable DC voltage. A diode rectifier can produce a fixed DC voltage whereas an SCR can produce a variable DC voltage.

Introduction:
The objectives of this lab are to:
1) study Half wave rectifiers,
2) study Full wave rectifiers.

Theory and Methodology:
Diode rectifiers are of the following types:
1. Half-wave rectifier.
2. Full-wave bridge rectifier.
3. Center tapped Full-wave rectifier.

A rectifier, however, cannot produce a smooth DC voltage. So the rectification block that makes the output DC voltage a smooth one follows a filter circuit. In this case, the capacitor acts as a smoothing filter so that the output is nearly a dc voltage. A filtering is not perfect; there will be a remaining voltage fluctuation known as ripple, on the output voltage.

The half-wave voltage signal is normally established by a network with a single diode has an average or equivalent DC voltage level equal to 31.8% of the peak voltage, whereas the full-wave rectified signal has twice the average or DC level of the half-wave signal, or 63.6% of the peak value.

Working Principle of Half-wave rectifier:
In half wave rectifier only half cycle of applied AC voltage is used. Another half cycle of AC voltage (negative cycle) is not used. Only one diode is used which conducts during positive cycle. The circuit diagram of half wave rectifier without capacitor is shown in the following figure.
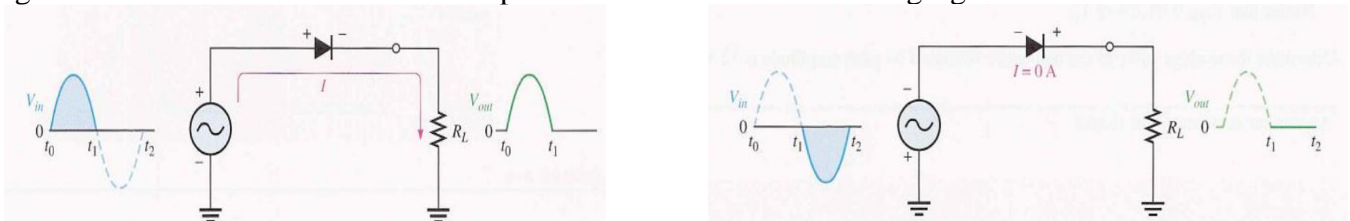

Figure: Half-Wave Rectification

During positive half cycle of the input voltage anode of the diode is positive compared with the cathode. Diode is in forward bias and current passes through the diode and positive cycle develops across the load resistance R_L. During negative half cycle of input voltage, anode is negative with respected to cathode and diode is in reverse bias. No current passes through the diode hence output voltage is zero.

Working Principle of Full-Wave rectifier:
The Bridge rectifier is a circuit, which converts an ac voltage to dc voltage using both half cycles of the input ac voltage. The Bridge rectifier circuit is shown in the following figure.
The circuit has four diodes connected to form a bridge. The ac input voltage is applied to the diagonally opposite ends of the bridge. The load resistance is connected between the other two ends of the bridge. For

the positive half cycle of the input ac voltage, diodes D_1 and D_2 conduct, whereas diodes D_3 and D_4 remain in the OFF state. The conducting diodes will be in series with the load resistance R_L and hence the load current flows through R_L. For the negative half cycle of the input ac voltage, diodes D_3 and D_4 conduct whereas, D_1 and D_2 remain OFF. The conducting diodes D_3 and D_4 will be in series with the load resistance R_L and hence the current flows through R_L in the same direction as in the previous half cycle. Thus a bi-directional wave is converted into a unidirectional wave.
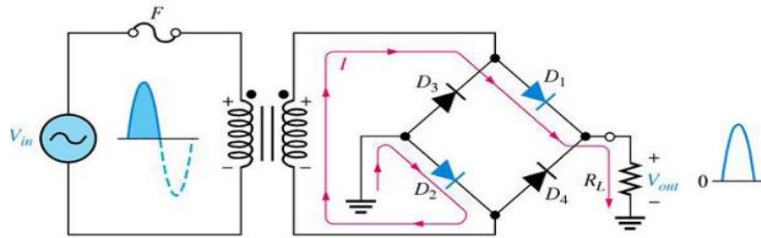

Figure: During positive half-cycle of the input, D_1 and D_2 are forward-biased and conduct current. D_3 and D_4 are reverse-biased
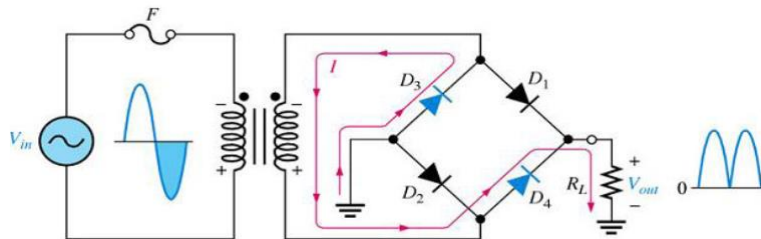

Figure: During negative half-cycle of the input, D_3 and D_4 are forward-biased and conduct current. D_1 and D_2 are reverse-biased

Working Principle of Center Tapped Full-Wave rectifier:
A center tapped rectifier is a type of full wave rectifier that uses two diodes connected to the secondary of a center tapped transformer, as shown in below diagram. The input voltage is coupled through the transformer to the center-tapped secondary. Half of the total secondary voltage appears between the center tap and each end of the secondary winding as shown.
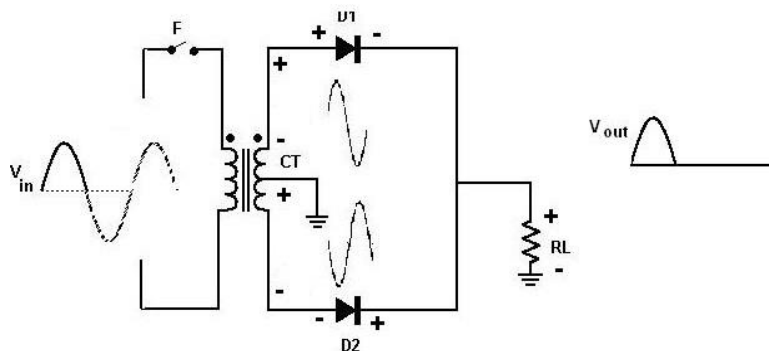

Figure: During positive half-cycle of the input, D_1 is forward-biased and D_2 is reverse-biased.

For a positive half cycle of the input voltage, the polarities of the secondary voltages are shown in figure. This condition forward biases diode D_1 and reverse biases diode D_2. The current path is through D_1 and the load resistor R_L.
For a negative half cycle of the input voltage, the voltage polarities on the secondary are shown. This condition reverse biases D_1 and forward biases D_2. The current path is through D_2 and R_L. Because the output current during both the positive and negative portions of the input cycle are in the same direction through the load the output voltage developed across the load resistor is a full wave rectified dc voltage.
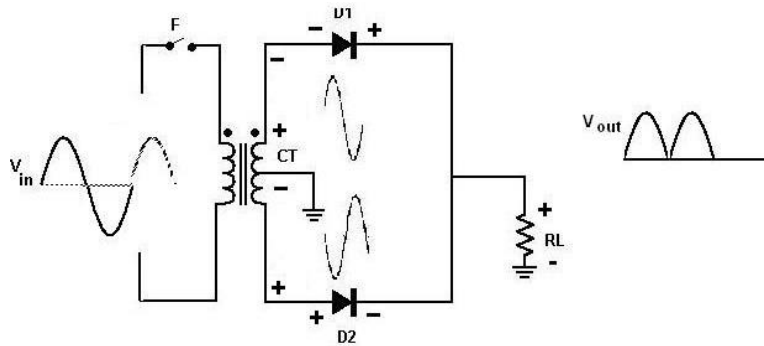
Figure: During negative half-cycle of the input, D_2 is forward-biased and D_1 is reverse-biased.

Apparatus:

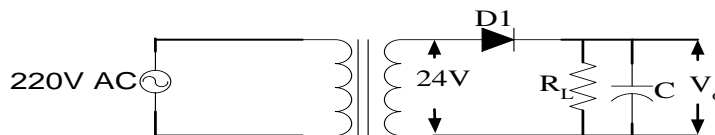| No.	Apparatus	Quantity
1	Diode	4
2	10k Resistance	1
3	Project Board	1
4	Oscilloscope	1
5	Multimeter	1
6	Transformer 220V/12V/9V/6V	1
7	47μF Capacitor	1
8	100μF Capacitor	1
9	Chord	2

Circuit Diagram:

Figure 1: Half wave rectifier.

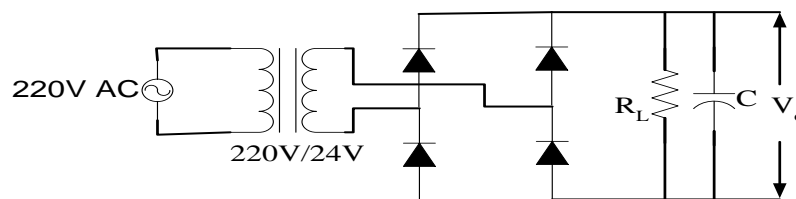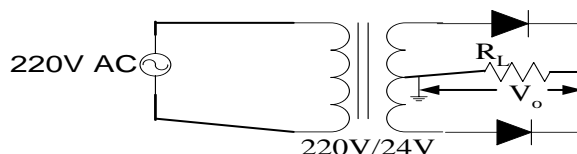Figure 2: Full wave Bridge rectifier.

Figure 3: Full wave Center Tapped rectifier.

Precautions:

1. Never remove or insert a diode into a circuit with voltage applied.
2. When testing a diode, ensure that the test voltage does not exceed the diode's maximum allowable voltage.
3. Ensure a replacement diode into a circuit is in the correct direction.
4. Make sure the correct connection of the transformer

Pre-Lab Homework:
Implement the circuits (Figure 1, Figure 2 and Figure 3) using PSpice. Observe the input output wave shapes and fill up the tables (Table 1, Table 2 and Table 3) using the simulation tool.

Experimental Procedure:
1. Connect the circuit shown in the figure – 1 but without the capacitor.
2. Connect the oscilloscope to observe the wave shapes of the input and output voltages. Measure the peak DC voltage from the oscilloscope.
3. Measure the output voltage by Multimeter and compare it with that obtained from the oscilloscope.
4. Turn off the power supply and connect a 47μF capacitor across the load. Observe the output voltage and measure with the oscilloscope.
5. Measure the output voltage with a Multimeter and compare it with that obtained from the oscilloscope.
6. Turn of the power supply and change the capacitor with the 100μF capacitor.
7. Repeat procedures 4 & 5 to obtain necessary measurements.
8. Repeat procedures 1 through 7 for circuits in figure 2 and 3.

Experimental Data:
Table 1: Data Table for circuit of Figure – 1

	V_o (Oscilloscope)	V_o (Multimeter)
No Capacitance		
47μF		
100 μF		

Table 2: Data Table for circuit of Figure – 2

	V_o (Oscilloscope)	V_o (Multimeter)
No Capacitance		
47μF		
100 μF		

Table 3: Data Table for circuit of Figure – 3

	V_o (Oscilloscope)	V_o (Multimeter)
No Capacitance		
47μF		
100 μF		

Questions for report writing:
1) Draw all the observed wave shapes.
2) What are the effects and significance of using filter capacitance?
3) What is the minimum PIV for the diodes used in circuit 2 & 3?
4) Why circuit 2 is better than the circuit in figure 3?
5) Discuss the experiment as a whole.

Reference(s):
[1]   Adel S. Sedra, Kennth C. Smith, "Microelectronic Circuits", Saunders College Publishing, 3rd ed., ISBN: 0-03-051648-X, 1991.
[2]   David J. Comer, Donald T. Comer, Fundamentals of Electronic Circuit Design, John Wiley & Sons Canada, Ltd.; ISBN: 0471410160, 2002.
[3]   American International University–Bangladesh (AIUB) Electronic Devices Lab Manual.