

American International University- Bangladesh (AIUB) Faculty of Engineering (FE)

Department of Electrical and Electronic Engineering (EEE)

Course Project Report Outline (Electronic Devices Lab)

- 1. Download the attached IEEE template for report writing from the link given in TEAMS.
- 2. Title, Abstract (at least 150 words but not more than 300 words) and Keywords (3-6 keywords separated by a comma). ABSTRACT TISHAT
 [2 marks]
- 3. Introduction **ABIR**

3.1. Background of Study and Motivation	[1 mark]
3.2. Project Objectives	[1 mark]
3.3. A brief Outline of the Report	[1 mark]

4. Literature Review - TISHAT (At least 5 project-related published journal papers within the year 2018 to 2022) [3 marks]

5. Methodology and Modeling

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5.1. IntroductioN-OPI	[1
mark]	
5.2. Working Principle of the Proposed Project OPI	[1 mark]
5.2.1. Process of Work	[1 mark]
5.3. Description of the Components JAHID mark]	[1
5.4. Test/Experimental Setup JAHID mark]	[1

6. Results and Discussions

6.1. Simulation/Numerical Analysis JAHID mark]	[1
6.2. Measured response/Experimental Results shohan mark]	[1
6.3. Comparison between Numerical and Experimental Results shohan mark]	[1
6.4. Cost Analysis RAFI mark]	[1
6.5. Limitations in the Project RAFI mark]	[1

7. Conclusion and Future Endeavors RAFI mark

[1

Appendix (if any, optional)



American International University-Bangladesh (AIUB) Faculty of Engineering (FE) Department of Electrical and Electronic Engineering (EEE)

Course Name:		Course Code:	EEE
Semester:	Spring 2023-2024	Section:	
Faculty Name:	Md. Shaoran Sayem		

Capstone Project Title:	
Project Group #:	

SL#	Student Name	Student ID#
1.		
2.		
3.		
4.		
5.		
6.		

Assessment Materials and Marks Allocation:

CO/ CLO Number	CO/CLO Statement	K	P	A	Assessed Program Outcome Indicator	BNQF	Teaching- Learning Strategy	Assessment Strategy
2	Select engineering tools (e.g., MULTISIM) to simulate different electronic circuit problems considering the limitations.	K6	P1 P4 P5		P.e.1.C6	FS.6		Capstone Project Report
3	Communicate effectively by giving and responding to clear instructions to produce an effective presentations for complex engineering solutions			A1 A3 A5	P.j.1. A2	SS.1		Capstone Project Presentation

Assessment Rubrics:

COs	Excellent to Proficient [5-4]	Good [3]	Acceptable [2]	Unacceptable [1]	No Response [0]	Secured Marks
CO3 P.d.1.P.	The outcome of the project demonstrates a course project using semiconductor devices, passive components, etc. that can solve a complex engineering problem in the electrical and electronic engineering discipline through appropriate research.	somewhat demonstrates a course project using semiconductor devices,	engineering problem properly in the electrical	does not demonstrate a course project using semiconductor devices,	No Response at all/copied from others/ identical submissions with gross	

Comments	Total Marks	
Comments	(5)	