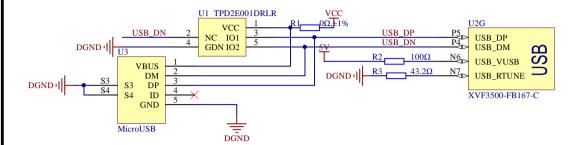
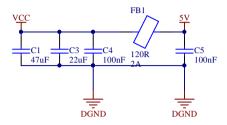
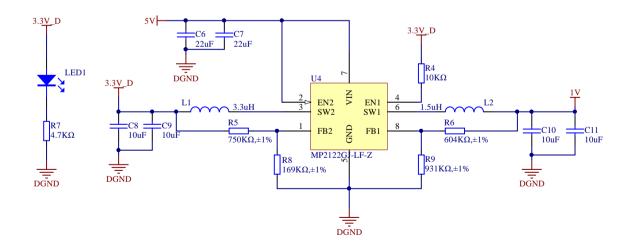
1.XVF3500 USB & POWER

(1) 5V和USB接口





(2) 3.3V和1V电源



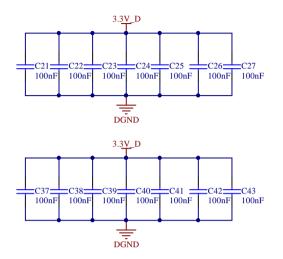
图纸名称

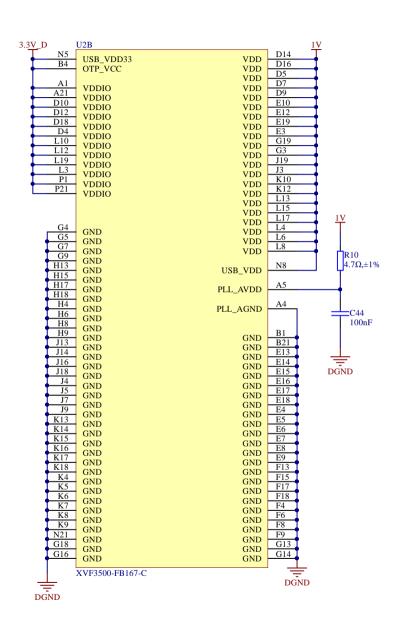
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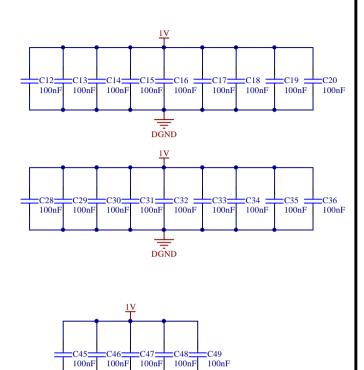
图纸描述	XVF3500 I	USB & POWER
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2.XVF3500 POWERR

(1) XVF3500 POWER







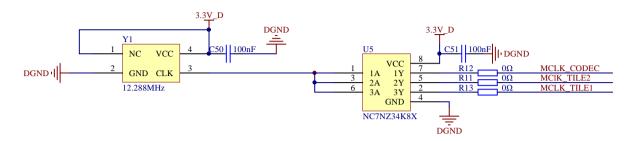
— □ DGND

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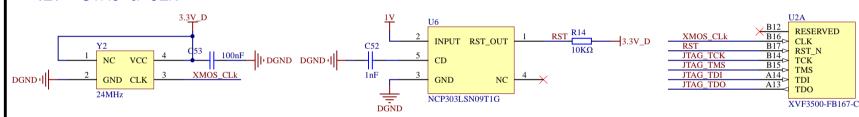
图纸描述	XVF3500 I	POWER
ם דוישי	DATEDATE	וי די די

3.XVF3500 CONFIG

(1) 主时钟产生



(2) JTAG & CLK



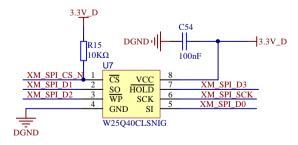
	P2	
JTAG_TDI	1 2	XL_UP1
JTAG_TMS	2 4	XL_UP0
JTAG_TCK	5 6	XL_DN0
JTAG_TDO	7 8	XL_DN1
RST	9 10	I DGND
	9 10] II-DGND
	Header 5X	7.2

图纸版本 V1.00

PXVF3500-KIT

第3张/共5张

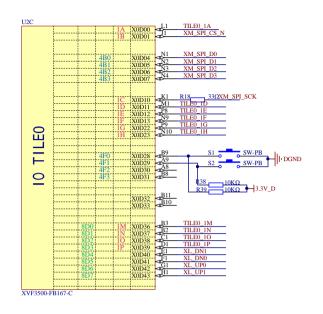
(3) Flash电路



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OGO.bmp	

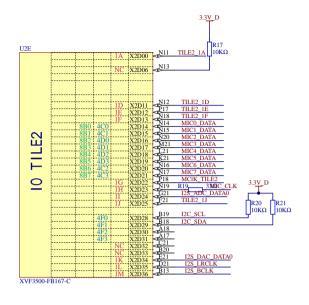
4.XVF3500 10

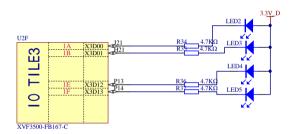
(1) XVF3500 I0



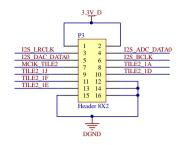
MCLK_TILE1

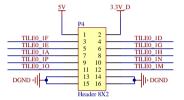
X1D26 287





(2) 扩展接口





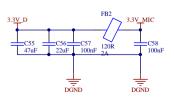
(3) MIC接口

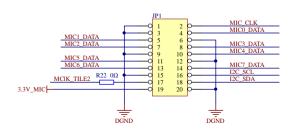
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XVF3500-FB167-C





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OGO.bmp	

图纸描述 XVF3500 10分布

设计人员 PAWPAW 设计日期 2018-03-31

图纸名称 XVF3500 10

产品型号 PXVF3500-KIT

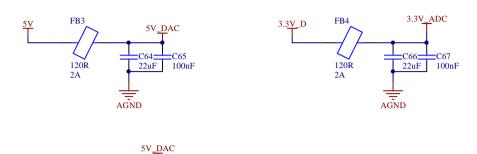
图纸版本

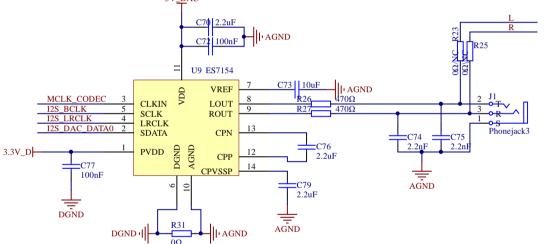
V1.00

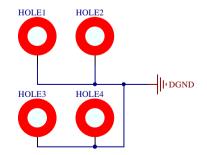
第 4 张 / 共 5 张 A3

5.DAC&ADC

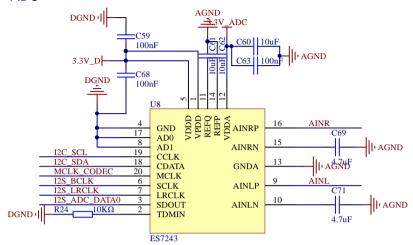
DAC (1)



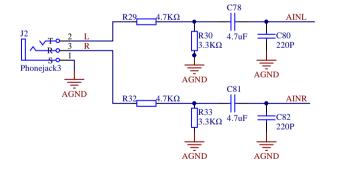


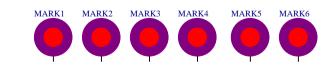


(2) ADC









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图纸描述 DAC&ADC实现 设计人员

PAWPAW

设计日期 2016-03-31 图纸名称

DAC&ADC

产品型号

PXVF3500-KIT

图纸版本 V1.00

第5张/共5张