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STM32系列

产品选型手册

32位微控制器(MCU)
32位微处理器(MPU)

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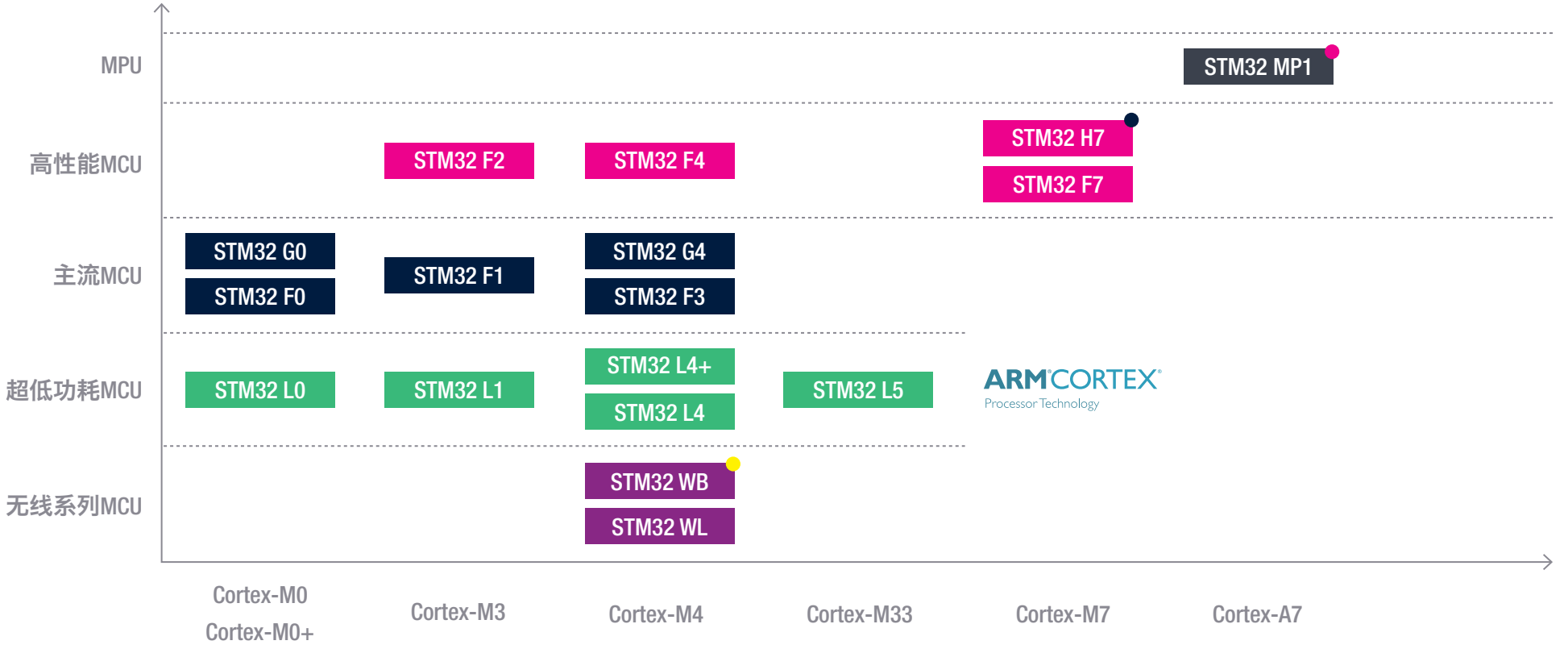
基于Arm Cortex内核的32位MCU和MPU



● Cortex-M0+无线协处理器　● 集成双Arm Cortex-A7和Cortex-M4内核　● 含Arm Cortex-M7和Cortex-M4双核产品

STM32 G0系列 – Arm® Cortex®-M0+全新入门级MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32G0x0超值型 - 64 MHz																																																					
STM32G030J6M6	64	Cortex-M0+	32	8	0	SO-8	6	2-3.6	5	0	1	0	0	1	8	0	0	0	0	0	0	0	2	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G030F6P6	64	Cortex-M0+	32	8	0	TSSOP 20	18	2-3.6	5	0	1	0	0	1	13	0	0	0	0	0	0	0	2	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G030K6T6	64	Cortex-M0+	32	8	0	LQFP32	30	2-3.6	5	0	1	0	0	1	16	0	0	0	0	0	0	0	2	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G030K8T6	64	Cortex-M0+	64	8	0	LQFP32	30	2-3.6	5	0	1	0	0	1	16	0	0	0	0	0	0	0	2	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G030C6T6	64	Cortex-M0+	32	8	0	LQFP48	44	2-3.6	5	0	1	0	0	1	19	0	0	0	0	0	0	0	2	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G030C8T6	64	Cortex-M0+	64	8	0	LQFP48	44	2-3.6	5	0	1	0	0	1	19	0	0	0	0	0	0	0	2	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G070KBT6	64	Cortex-M0+	128	36	0	LQFP32	29	2-3.6	8	0	1	0	0	1	13	0	0	0	0	0	0	0	2	0	[1]	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G070CBT6	64	Cortex-M0+	128	36	0	LQFP48	43	2-3.6	8	0	1	0	0	1	17	0	0	0	0	0	0	0	2	0	[1]	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G070RBT6	64	Cortex-M0+	128	36	0	LQFP64	59	2-3.6	8	0	1	0	0	1	19	0	0	0	0	0	0	0	2	0	[1]	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G0x1入门型 - 64 MHz																																																					
STM32G031J6M6	64	Cortex-M0+	32	8	0	SO-8	6	1.7-3.6	5	1	1	2	0	1	8	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G031F6P6	64	Cortex-M0+	32	8	0	TSSOP 20	18	1.7-3.6	5	1	1	2	0	1	17	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G031F8P6	64	Cortex-M0+	64	8	0	TSSOP 20	18	1.7-3.6	5	1	1	2	0	1	17	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G031G6U6	64	Cortex-M0+	32	8	0	UFQFPN28	26	1.7-3.6	5	1	1	2	0	1	17	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G031G8U6	64	Cortex-M0+	64	8	0	UFQFPN28	26	1.7-3.6	5	1	1	2	0	1	17	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G031K6T6	64	Cortex-M0+	32	8	0	LQFP32	30	1.7-3.6	5	1	1	2	0	1	18	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G031K8T6	64	Cortex-M0+	64	8	0	LQFP32	30	1.7-3.6	5	1	1	2	0	1	18	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G031K6U6	64	Cortex-M0+	32	8	0	UFQFPN32	30	1.7-3.6	5	1	1	2	0	1	18	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 G0系列 – Arm® Cortex®-M0+全新入门级MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32G031K8U6	64	Cortex-M0+	64	8	0	UFQFPN32	30	1.7-3.6	5	1	1	2	0	1	18	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G031C6T6	64	Cortex-M0+	32	8	0	LQFP48	44	1.7-3.6	5	1	1	2	0	1	19	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G031C8T6	64	Cortex-M0+	64	8	0	LQFP48	44	1.7-3.6	5	1	1	2	0	1	19	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G031C6U6	64	Cortex-M0+	32	8	0	UFQFPN48	44	1.7-3.6	5	1	1	2	0	1	19	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G031C8U6	64	Cortex-M0+	64	8	0	UFQFPN48	44	1.7-3.6	5	1	1	2	0	1	19	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G041C8T6	64	Cortex-M0+	64	8	0	LQFP48	44	1.7-3.6	5	1	1	2	0	1	19	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32G041C8U6	64	Cortex-M0+	64	8	0	UFQFPN48	44	1.7-3.6	5	1	1	2	0	1	19	0	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32G071EBY6	64	Cortex-M0+	128	36	0	WLCSP25	23	1.7-3.6	8	1	1	2	0	1	12	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G071G8U6	64	Cortex-M0+	64	36	0	UFQFPN28	26	1.7-3.6	8	1	1	2	0	1	12	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G071GBU6	64	Cortex-M0+	128	36	0	UFQFPN28	26	1.7-3.6	8	1	1	2	0	1	12	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G071K8T6	64	Cortex-M0+	64	36	0	LQFP32	30	1.7-3.6	8	1	1	2	0	1	13	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G071K8U6	64	Cortex-M0+	64	36	0	UFQFPN32	30	1.7-3.6	8	1	1	2	0	1	13	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G071KBT6	64	Cortex-M0+	128	36	0	LQFP32	30	1.7-3.6	8	1	1	2	0	1	13	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G071KBU6	64	Cortex-M0+	128	36	0	UFQFPN32	30	1.7-3.6	8	1	1	2	0	1	13	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G071C8T6	64	Cortex-M0+	64	36	0	LQFP48	44	1.7-3.6	8	1	1	2	0	1	17	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G071C8U6	64	Cortex-M0+	64	36	0	UFQFPN48	44	1.7-3.6	8	1	1	2	0	1	17	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G071CBT6	64	Cortex-M0+	128	36	0	LQFP48	44	1.7-3.6	8	1	1	2	0	1	17	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G071CBU6	64	Cortex-M0+	128	36	0	UFQFPN48	44	1.7-3.6	8	1	1	2	0	1	17	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 G0系列 – Arm® Cortex®-M0+全新入门级MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32G071R8T6	64	Cortex-M0+	64	36	0	LQFP64	60	1.7-3.6	8	1	1	2	0	1	19	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G071RBT6	64	Cortex-M0+	128	36	0	LQFP64	60	1.7-3.6	8	1	1	2	0	1	19	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G081EBY6	64	Cortex-M0+	128	36	0	WLCSP25	23	1.7-3.6	8	1	1	2	0	1	12	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32G081GBU6	64	Cortex-M0+	128	36	0	UFQFPN28	26	1.7-3.6	8	1	1	2	0	1	12	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32G081KBT6	64	Cortex-M0+	128	36	0	LQFP32	30	1.7-3.6	8	1	1	2	0	1	13	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32G081KBU6	64	Cortex-M0+	128	36	0	UFQFPN32	30	1.7-3.6	8	1	1	2	0	1	13	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32G081CBT6	64	Cortex-M0+	128	36	0	LQFP48	44	1.7-3.6	8	1	1	2	0	1	16	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32G081CBU6	64	Cortex-M0+	128	36	0	UFQFPN48	44	1.7-3.6	8	1	1	2	0	1	16	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32G081RBT6	64	Cortex-M0+	128	36	0	LQFP64	60	1.7-3.6	8	1	1	2	0	1	18	0	0	2	2	0	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0

STM32 G4系列 – Arm® Cortex®-M4模数混合型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32G4x1基本型 - 170 MHz																																																						
STM32G431K6T6	170 MHz	Cortex-M4	32	32	0	LQFP32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431K8T6	170 MHz	Cortex-M4	64	32	0	LQFP32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431KBT6	170 MHz	Cortex-M4	128	32	0	LQFP32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431K6U6	170 MHz	Cortex-M4	32	32	0	UFQFPN32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431K8U6	170 MHz	Cortex-M4	64	32	0	UFQFPN32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431KBU6	170 MHz	Cortex-M4	128	32	0	UFQFPN32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431C6T6	170 MHz	Cortex-M4	32	32	0	LQFP48	38	1.71-3.6	9	1	2	1	0	2	17	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431C8T6	170 MHz	Cortex-M4	64	32	0	LQFP48	38	1.71-3.6	9	1	2	1	0	2	17	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431CBT6	170 MHz	Cortex-M4	128	32	0	LQFP48	38	1.71-3.6	9	1	2	1	0	2	17	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431C6U6	170 MHz	Cortex-M4	32	32	0	UFQFPN48	42	1.71-3.6	9	1	2	1	0	2	18	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431C8U6	170 MHz	Cortex-M4	64	32	0	UFQFPN48	42	1.71-3.6	9	1	2	1	0	2	18	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431CBU6	170 MHz	Cortex-M4	128	32	0	UFQFPN48	42	1.71-3.6	9	1	2	1	0	2	18	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431C6Y6	170 MHz	Cortex-M4	32	32	0	WLCSP49	41	1.71-3.6	9	1	2	1	0	2	18	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431C8Y6	170 MHz	Cortex-M4	64	32	0	WLCSP49	41	1.71-3.6	9	1	2	1	0	2	18	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431CBY6	170 MHz	Cortex-M4	128	32	0	WLCSP49	41	1.71-3.6	9	1	2	1	0	2	18	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431R6T6	170 MHz	Cortex-M4	32	32	0	LQFP64	52	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G431R8T6	170 MHz	Cortex-M4	64	32	0	LQFP64	52	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0

STM32 G4系列 – Arm® Cortex®-M4模数混合型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32G431RBT6	170 MHz	Cortex-M4	128	32	0	LQFP64	52	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G431R6I6	170 MHz	Cortex-M4	32	32	0	UFBGA64	52	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G431R8I6	170 MHz	Cortex-M4	64	32	0	UFBGA64	52	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G431RBI6	170 MHz	Cortex-M4	128	32	0	UFBGA64	52	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G431V6T6	170 MHz	Cortex-M4	32	32	0	LQFP100	86	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G431V8T6	170 MHz	Cortex-M4	64	32	0	LQFP100	86	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G431VBT6	170 MHz	Cortex-M4	128	32	0	LQFP100	86	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G441KBT6	170 MHz	Cortex-M4	128	32	0	LQFP32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
STM32G441KBU6	170 MHz	Cortex-M4	128	32	0	UFQFPN32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
STM32G441CBT6	170 MHz	Cortex-M4	128	32	0	LQFP48	38	1.71-3.6	9	1	2	1	0	2	17	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
STM32G441CBU6	170 MHz	Cortex-M4	128	32	0	UFQFPN48	38	1.71-3.6	9	1	2	1	0	2	18	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
STM32G441RBT6	170 MHz	Cortex-M4	128	32	0	LQFP64	52	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
STM32G441RBH6	170 MHz	Cortex-M4	128	32	0	TFBGA64	52	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
STM32G441VBT6	170 MHz	Cortex-M4	128	32	0	LQFP100	86	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
STM32G4x3高性能型 - 170 MHz																																																						
STM32G473CBT6	170 MHz	Cortex-M4	128	120	0	LQFP48	38	1.71-3.6	10	2	3	1	0	5	20	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G473CCT6	170 MHz	Cortex-M4	256	120	0	LQFP48	38	1.71-3.6	10	2	3	1	0	5	20	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G473CET6	170 MHz	Cortex-M4	512	120	0	LQFP48	38	1.71-3.6	10	2	3	1	0	5	20	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0

STM32 G4系列 – Arm® Cortex®-M4模数混合型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32G473CBU6	170 MHz	Cortex-M4	128	120	0	UFQFPN48	42	1.71-3.6	10	2	3	1	0	5	21	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473CCU6	170 MHz	Cortex-M4	256	120	0	UFQFPN48	42	1.71-3.6	10	2	3	1	0	5	21	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473CEU6	170 MHz	Cortex-M4	512	120	0	UFQFPN48	42	1.71-3.6	10	2	3	1	0	5	21	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473RBT6	170 MHz	Cortex-M4	128	120	0	LQFP64	52	1.71-3.6	10	2	3	1	0	5	26	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473RCT6	170 MHz	Cortex-M4	256	120	0	LQFP64	52	1.71-3.6	10	2	3	1	0	5	26	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473RET6	170 MHz	Cortex-M4	512	120	0	LQFP64	52	1.71-3.6	10	2	3	1	0	5	26	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473VBT6	170 MHz	Cortex-M4	128	120	0	LQFP100	86	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473VCT6	170 MHz	Cortex-M4	256	120	0	LQFP100	86	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473VET6	170 MHz	Cortex-M4	512	120	0	LQFP100	86	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473VBH6	170 MHz	Cortex-M4	128	120	0	TFBGA100	86	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473VCH6	170 MHz	Cortex-M4	256	120	0	TFBGA100	86	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473VEH6	170 MHz	Cortex-M4	512	120	0	TFBGA100	86	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473QBT6	170 MHz	Cortex-M4	128	120	0	LQFP128	107	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473QCT6	170 MHz	Cortex-M4	256	120	0	LQFP128	107	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G473QET6	170 MHz	Cortex-M4	512	120	0	LQFP128	107	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G483CET6	170 MHz	Cortex-M4	512	120	0	LQFP48	38	1.71-3.6	10	2	3	1	0	5	20	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	1	0
STM32G483CEU6	170 MHz	Cortex-M4	512	120	0	UFQFPN48	42	1.71-3.6	10	2	3	1	0	5	21	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	1	0
STM32G483RET6	170 MHz	Cortex-M4	512	120	0	LQFP64	52	1.71-3.6	10	2	3	1	0	5	26	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	1	0

STM32 G4系列 – Arm® Cortex®-M4模数混合型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32G483VET6	170 MHz	Cortex-M4	512	120	0	LQFP100	86	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	1	0
STM32G483VEH6	170 MHz	Cortex-M4	512	120	0	TFBGA100	86	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	1	0
STM32G483QET6	170 MHz	Cortex-M4	512	120	0	LQFP128	107	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	1	0
STM32G4x4高分辨率定时器系列 - 170 MHz																																																						
STM32G474CET6	170 MHz	Cortex-M4	512	128	0	LQFP48	38	1.71-3.6	10	2	3	1	1	5	20	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474CEU6	170 MHz	Cortex-M4	512	128	0	UFQFPN48	42	1.71-3.6	10	2	3	1	1	5	21	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474CCT6	170 MHz	Cortex-M4	256	128	0	LQFP48	38	1.71-3.6	10	2	3	1	1	5	20	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474CCU6	170 MHz	Cortex-M4	256	128	0	UFQFPN48	42	1.71-3.6	10	2	3	1	1	5	21	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474CBT6	170 MHz	Cortex-M4	128	128	0	LQFP48	38	1.71-3.6	10	2	3	1	1	5	20	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474CBU6	170 MHz	Cortex-M4	128	128	0	UFQFPN48	42	1.71-3.6	10	2	3	1	1	5	21	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474RET6	170 MHz	Cortex-M4	512	128	0	LQFP64	52	1.71-3.6	10	2	3	1	1	5	26	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474RCT6	170 MHz	Cortex-M4	256	128	0	LQFP64	52	1.71-3.6	10	2	3	1	1	5	26	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474RBT6	170 MHz	Cortex-M4	128	128	0	LQFP64	52	1.71-3.6	10	2	3	1	1	5	26	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474VET6	170 MHz	Cortex-M4	512	128	0	LQFP100	86	1.71-3.6	10	2	3	1	1	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474VEH6	170 MHz	Cortex -M4	512	128	0	TFBGA100	86	1.71-3.6	10	2	3	1	1	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474VCT6	170 MHz	Cortex-M4	256	128	0	LQFP100	86	1.71-3.6	10	2	3	1	1	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474VCH6	170 MHz	Cortex-M4	256	128	0	TFBGA100	86	1.71-3.6	10	2	3	1	1	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0
STM32G474VBT6	170 MHz	Cortex-M4	128	128	0	LQFP100	86	1.71-3.6	10	2	3	1	1	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0	0

STM32 G4系列 – Arm® Cortex®-M4模数混合型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32G474VBH6	170 MHz	Cortex-M4	128	128	0	TFBGA100	86	1.71-3.6	10	2	3	1	1	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G474QET6	170 MHz	Cortex-M4	512	128	0	LQFP128	107	1.71-3.6	10	2	3	1	1	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G474QCT6	170 MHz	Cortex-M4	256	128	0	LQFP128	107	1.71-3.6	10	2	3	1	1	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G474QBT6	170 MHz	Cortex-M4	126	128	0	LQFP128	107	1.71-3.6	10	2	3	1	1	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0
STM32G484CET6	170 MHz	Cortex-M4	512	128	0	LQFP48	38	1.71-3.6	10	2	3	1	1	5	20	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
STM32G484CEU6	170 MHz	Cortex-M4	512	128	0	UFQFPN48	42	1.71-3.6	10	2	3	1	1	5	21	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
STM32G484RET6	170 MHz	Cortex-M4	512	128	0	LQFP64	52	1.71-3.6	10	2	3	1	1	5	26	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
STM32G484VET6	170 MHz	Cortex-M4	512	128	0	LQFP100	86	1.71-3.6	10	2	3	1	1	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
STM32G484VEH6	170 MHz	Cortex-M4	512	128	0	TFBGA100	86	1.71-3.6	10	2	3	1	1	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0
STM32G484QET6	170 MHz	Cortex-M4	512	128	0	LQFP128	107	1.71-3.6	10	2	3	1	1	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	1	0

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F0x0超值型 - 48 MHz																																																					
STM32F030F4P6	48	Cortex-M0	16	4	0	TSSOP20	15	2.4-3.6	5	0	1	0	0	1	11	0	0	0	0	0	0	0	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F030K6T6	48	Cortex-M0	32	4	0	LQFP32	26	2.4-3.6	5	0	1	0	0	1	12	0	0	0	0	0	0	0	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F030C6T6	48	Cortex-M0	32	4	0	LQFP48	39	2.4-3.6	5	0	1	0	0	1	12	0	0	0	0	0	0	0	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F030C8T6	48	Cortex-M0	64	8	0	LQFP48	39	2.4-3.6	7	0	1	0	0	1	12	0	0	0	0	0	0	0	2	0	0	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F030CCT6	48	Cortex-M0	256	32	0	LQFP48	37	2.4-3.6	8	0	1	0	0	1	12	0	0	0	0	0	0	0	2	0	0	2	6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F030R8T6	48	Cortex-M0	64	8	0	LQFP64	55	2.4-3.6	7	0	1	0	0	1	18	0	0	0	0	0	0	0	2	0	0	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F030RCT6	48	Cortex-M0	256	32	0	LQFP64	51	2.4-3.6	8	0	1	0	0	1	18	0	0	0	0	0	0	0	2	0	0	2	6	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F070F6P6	48	Cortex-M0	32	6	0	TSSOP20	15	2.4-3.6	5	0	1	0	0	1	9	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F070C6T6	48	Cortex-M0	32	6	0	LQFP48	37	2.4-3.6	5	0	1	0	0	1	10	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F070CBT6	48	Cortex-M0	128	16	0	LQFP48	37	2.4-3.6	8	0	1	0	0	1	10	0	0	0	0	0	0	0	2	0	0	2	4	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F070RBT6	48	Cortex-M0	128	16	0	LQFP64	51	2.4-3.6	8	0	1	0	0	1	16	0	0	0	0	0	0	0	2	0	0	2	4	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F0x1入门型 - 48 MHz																																																					
STM32F031F4P6	48	Cortex-M0	16	4	0	TSSOP20	15	2-3.6	5	1	1	0	0	1	9	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F031G4U6	48	Cortex-M0	16	4	0	UFQFPN28	23	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F031K4U6	48	Cortex-M0	16	4	0	UFQFPN32	27	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F031K4T6	48	Cortex-M0	16	4	0	LQFP32	25	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F031C4T6	48	Cortex-M0	16	4	0	LQFP48	39	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F031F6P6	48	Cortex-M0	32	4	0	TSSOP20	15	2-3.6	5	1	1	0	0	1	9	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F031E6Y6	48	Cortex-M0	32	4	0	WLCSP25	20	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F031G6U6	48	Cortex-M0	32	4	0	UFQFPN28	23	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F031K6T6	48	Cortex-M0	32	4	0	LQFP32	25	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F031K6U6	48	Cortex-M0	32	4	0	UFQFPN32	27	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F031C6T6	48	Cortex-M0	32	4	0	LQFP48	39	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051K4T6	48	Cortex-M0	16	8	0	LQFP32	25	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051K4U6	48	Cortex-M0	16	8	0	UFQFPN32	27	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051K6T6	48	Cortex-M0	32	8	0	LQFP32	25	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	1	0	[1]	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051K6U6	48	Cortex-M0	32	8	0	UFQFPN32	27	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	1	0	[1]	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051K8T6	48	Cortex-M0	64	8	0	LQFP32	25	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	1	0	[1]	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051K8U6	48	Cortex-M0	64	8	0	UFQFPN32	27	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	1	0	[1]	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051T8Y6	48	Cortex-M0	64	8	0	WLCSP36	29	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	1	0	[1]	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051C4T6	48	Cortex-M0	16	8	0	LQFP48	39	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051C4U6	48	Cortex-M0	16	8	0	UFQFPN48	39	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051C6T6	48	Cortex-M0	32	8	0	LQFP48	39	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	1	0	[1]	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051C6U6	48	Cortex-M0	32	8	0	UFQFPN48	39	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	1	0	[1]	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051C8T6	48	Cortex-M0	64	8	0	LQFP48	39	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	2	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051C8U6	48	Cortex-M0	64	8	0	UFQFPN48	39	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	0	2	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F051R4T6	48	Cortex-M0	16	8	0	LQFP64	55	2-3.6	7	1	1	0	0	1	16	0	0	1	2	0	0	0	2	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051R6T6	48	Cortex-M0	32	8	0	LQFP64	55	2-3.6	7	1	1	0	0	1	16	0	0	1	2	0	0	0	2	0	[1]	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051R8T6	48	Cortex-M0	64	8	0	LQFP64	55	2-3.6	7	1	1	0	0	1	16	0	0	1	2	0	0	0	2	0	[1]	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051R4H6	48	Cortex-M0	16	8	0	UFBGA64	55	2-3.6	7	1	1	0	0	1	16	0	0	1	2	0	0	0	2	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051R6H6	48	Cortex-M0	32	8	0	UFBGA64	55	2-3.6	7	1	1	0	0	1	16	0	0	1	2	0	0	0	2	0	[1]	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051R8H6	48	Cortex-M0	64	8	0	UFBGA64	55	2-3.6	7	1	1	0	0	1	16	0	0	1	2	0	0	0	2	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F071CBY6	48	Cortex-M0	128	16	0	WLCSP49	37	2-3.6	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F071CBT6	48	Cortex-M0	128	16	0	LQFP48	37	2-3.6	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F071CBU6	48	Cortex-M0	128	16	0	UFQFPN48	37	2-3.6	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F071RBT6	48	Cortex-M0	128	16	0	LQFP64	51	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F071V8H6	48	Cortex-M0	64	16	0	UFBGA100	87	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F071V8T6	48	Cortex-M0	64	16	0	LQFP100	87	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F071VBH6	48	Cortex-M0	128	16	0	UFBGA100	87	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F071VBT6	48	Cortex-M0	128	16	0	LQFP100	87	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091CBT6	48	Cortex-M0	128	32	0	LQFP48	38	2-3.6	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	6	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091CBU6	48	Cortex-M0	128	32	0	UFQFPN48	38	2-3.6	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	6	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091CCT6	48	Cortex-M0	256	32	0	LQFP48	38	2-3.6	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	6	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091CCU6	48	Cortex-M0	256	32	0	UFQFPN48	38	2-3.6	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	6	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F091RBT6	48	Cortex-M0	128	32	0	LQFP64	52	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091RBH6	48	Cortex-M0	128	32	0	UFBGA64	52	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091RBY6	48	Cortex-M0	128	32	0	WLCSP64	52	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091RCT6	48	Cortex-M0	256	32	0	LQFP64	52	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091RCH6	48	Cortex-M0	256	32	0	UFBGA64	52	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091RCY6	48	Cortex-M0	256	32	0	WLCSP64	52	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091VBT6	48	Cortex-M0	128	32	0	LQFP100	88	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091VBH6	48	Cortex-M0	128	32	0	UFBGA100	88	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091VCT6	48	Cortex-M0	256	32	0	LQFP100	88	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F091VCH6	48	Cortex-M0	256	32	0	UFBGA100	88	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F0x2 USB型 - 48 MHz																																																					
STM32F042F4P6	48	Cortex-M0	16	6	0	TSSOP20	16	2-3.6	5	1	1	0	0	1	9	0	0	0	0	0	0	0	1	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F042F6P6	48	Cortex-M0	32	6	0	TSSOP20	16	2-3.6	5	1	1	0	0	1	9	0	0	0	0	0	0	0	1	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F042G4U6	48	Cortex-M0	16	6	0	UFQFPN28	24	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F042G6U6	48	Cortex-M0	32	6	0	UFQFPN28	24	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F042K4T6	48	Cortex-M0	16	6	0	LQFP32	26	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F042K4U6	48	Cortex-M0	16	6	0	UFQFPN32	28	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F042K6T6	48	Cortex-M0	32	6	0	LQFP32	26	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F042K6U6	48	Cortex-M0	32	6	0	UFQFPN32	28	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F042T4Y6	48	Cortex-M0	16	6	0	WLCSP36	30	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F042T6Y6	48	Cortex-M0	32	6	0	WLCSP36	30	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F042C4T6	48	Cortex-M0	16	6	0	LQFP48	38	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	2	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F042C4U6	48	Cortex-M0	16	6	0	UFQFPN48	38	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	2	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F042C6T6	48	Cortex-M0	32	6	0	LQFP48	38	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	2	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F042C6U6	48	Cortex-M0	32	6	0	UFQFPN48	38	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	0	0	2	0	[1]	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F072C8T6	48	Cortex-M0	64	16	0	LQFP48	37	2-3.6	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	4	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F072C8U6	48	Cortex-M0	64	16	0	UFQFPN48	37	2-3.6	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	4	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F072CBY6	48	Cortex-M0	128	16	0	WLCSP49	37	2-3.6	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	4	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F072CBT6	48	Cortex-M0	128	16	0	LQFP48	37	2-3.6	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	4	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F072CBU6	48	Cortex-M0	128	16	0	UFQFPN48	37	2-3.6	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	4	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F072R8T6	48	Cortex-M0	64	16	0	LQFP64	51	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F072RBH6	48	Cortex-M0	128	16	0	UFBGA64	51	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F072RBT6	48	Cortex-M0	128	16	0	LQFP64	51	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F072V8H6	48	Cortex-M0	64	16	0	UFBGA100	87	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F072V8T6	48	Cortex-M0	64	16	0	LQFP100	87	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F072VBH6	48	Cortex-M0	128	16	0	UFBGA100	87	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F072VBT6	48	Cortex-M0	128	16	0	LQFP100	87	2-3.6	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F0x8低电压型 - 48 MHz																																																						
STM32F038F6P6	48	Cortex-M0	32	4	0	TSSOP20	14	1.65-1.95	5	1	1	0	0	1	8	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F038E6Y6	48	Cortex-M0	32	4	0	WLCSP25	19	1.65-1.95	5	1	1	0	0	1	9	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F038G6U6	48	Cortex-M0	32	4	0	UFQFPN28	22	1.65-1.95	5	1	1	0	0	1	9	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F038K6U6	48	Cortex-M0	32	4	0	UFQFPN32	26	1.65-1.95	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F038C6T6	48	Cortex-M0	32	4	0	LQFP48	38	1.65-1.95	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F048G6U6	48	Cortex-M0	32	6	0	UFQFPN28	24	1.65-1.95	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F048T6Y6	48	Cortex-M0	32	6	0	WLCSP36	29	1.65-1.95	5	1	1	0	0	1	10	0	0	0	0	0	0	0	1	0	[1]	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F048C6U6	48	Cortex-M0	32	6	0	UFQFPN48	37	1.65-1.95	5	1	1	0	0	1	10	0	0	0	0	0	0	0	2	0	[1]	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F058T8Y6	48	Cortex-M0	64	8	0	WLCSP36	28	1.65-1.95	7	1	1	0	0	1	10	0	0	1	2	0	0	0	1	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F058C8U6	48	Cortex-M0	64	8	0	UFQFPN48	38	1.65-1.95	7	1	1	0	0	1	10	0	0	1	2	0	0	0	2	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F058R8H6	48	Cortex-M0	64	8	0	UFBGA64	54	1.65-1.95	7	1	1	0	0	1	16	0	0	1	2	0	0	0	2	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F058R8T6	48	Cortex-M0	64	8	0	LQFP64	54	1.65-1.95	7	1	1	0	0	1	16	0	0	1	2	0	0	0	2	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F078CBT6	48	Cortex-M0	128	16	0	LQFP48	36	1.65-1.95	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F078CBU6	48	Cortex-M0	128	16	0	UFQFPN48	36	1.65-1.95	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F078CBY6	48	Cortex-M0	128	16	0	WLCSP49	36	1.65-1.95	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F078RBT6	48	Cortex-M0	128	16	0	LQFP64	50	1.65-1.95	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F078VBH6	48	Cortex-M0	128	16	0	UFBGA100	86	1.65-1.95	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F078VBT6	48	Cortex-M0	128	16	0	LQFP100	86	1.65-1.95	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	4	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F098CCT6	48	Cortex-M0	256	32	0	LQFP48	37	1.65-1.95	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	6	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F098CCU6	48	Cortex-M0	256	32	0	UFQFPN48	37	1.65-1.95	8	1	1	0	0	1	10	0	0	2	2	0	0	0	2	0	[2]	2	6	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F098RCH6	48	Cortex-M0	256	32	0	UFBGA64	51	1.65-1.95	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F098RCT6	48	Cortex-M0	256	32	0	LQFP64	51	1.65-1.95	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F098RCY6	48	Cortex-M0	256	32	0	WLCSP64	51	1.65-1.95	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F098VCT6	48	Cortex-M0	256	32	0	LQFP100	87	1.65-1.95	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F098VCH6	48	Cortex-M0	256	32	0	UFBGA100	87	1.65-1.95	8	1	1	0	0	1	16	0	0	2	2	0	0	0	2	0	[2]	2	8	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F100超值型 - 24 MHz																																																						
STM32F100C4T6	24	Cortex-M3	16	4	0	LQFP48	37	2-3.6	5	0	1	0	0	1	10	0	0	2	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100C6T6	24	Cortex-M3	32	4	0	LQFP48	37	2-3.6	5	0	1	0	0	1	10	0	0	2	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100C8T6	24	Cortex-M3	64	8	0	LQFP48	37	2-3.6	6	0	1	0	0	1	10	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100CBT6	24	Cortex-M3	128	8	0	LQFP48	37	2-3.6	6	0	1	0	0	1	10	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100R4T6	24	Cortex-M3	16	4	0	LQFP64	51	2-3.6	5	0	1	0	0	1	16	0	0	2	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100R4H6	24	Cortex-M3	16	4	0	TFBGA64	51	2-3.6	5	0	1	0	0	1	16	0	0	2	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100R6T6	24	Cortex-M3	32	4	0	LQFP64	51	2-3.6	5	0	1	0	0	1	16	0	0	2	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100R6H6	24	Cortex-M3	32	4	0	TFBGA64	51	2-3.6	5	0	1	0	0	1	16	0	0	2	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100R8T6	24	Cortex-M3	64	8	0	LQFP64	51	2-3.6	6	0	1	0	0	1	16	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100R8H6	24	Cortex-M3	64	8	0	TFBGA64	51	2-3.6	6	0	1	0	0	1	16	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100RBT6	24	Cortex-M3	128	8	0	LQFP64	51	2-3.6	6	0	1	0	0	1	16	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100RBH6	24	Cortex-M3	128	8	0	TFBGA64	51	2-3.6	6	0	1	0	0	1	16	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100V8T6	24	Cortex-M3	64	8	0	LQFP100	80	2-3.6	6	0	1	0	0	1	16	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100VBT6	24	Cortex-M3	128	8	0	LQFP100	80	2-3.6	6	0	1	0	0	1	16	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100RCT6	24	Cortex-M3	256	24	0	LQFP64	51	2-3.6	11	0	1	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100RDT6	24	Cortex-M3	384	32	0	LQFP64	51	2-3.6	11	0	1	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100RET6	24	Cortex-M3	512	32	0	LQFP64	51	2-3.6	11	0	1	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F100VCT6	24	Cortex-M3	256	24	0	LQFP100	80	2-3.6	11	0	1	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100VDT6	24	Cortex-M3	384	32	0	LQFP100	80	2-3.6	11	0	1	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100VET6	24	Cortex-M3	512	32	0	LQFP100	80	2-3.6	11	0	1	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100ZCT6	24	Cortex-M3	256	24	0	LQFP144	112	2-3.6	11	0	1	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100ZDT6	24	Cortex-M3	384	32	0	LQFP144	112	2-3.6	11	0	1	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F100ZET6	24	Cortex-M3	512	32	0	LQFP144	112	2-3.6	11	0	1	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101入门型 - 36 MHz																																																						
STM32F101T4U6	36	Cortex-M3	16	4	0	VFQFPN36	26	2-3.6	2	0	0	0	0	1	10	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101T6U6	36	Cortex-M3	32	6	0	VFQFPN36	26	2-3.6	2	0	0	0	0	1	10	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101C6T6	36	Cortex-M3	32	6	0	LQFP48	37	2-3.6	2	0	0	0	0	1	10	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101R4T6	36	Cortex-M3	16	4	0	LQFP64	51	2-3.6	2	0	0	0	0	1	10	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101R6T6	36	Cortex-M3	32	6	0	LQFP64	51	2-3.6	2	0	0	0	0	1	10	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101T8U6	36	Cortex-M3	64	10	0	VFQFPN36	26	2-3.6	3	0	0	0	0	1	10	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101TBU6	36	Cortex-M3	128	16	0	VFQFPN36	26	2-3.6	3	0	0	0	0	1	10	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101C8T6	36	Cortex-M3	64	10	0	LQFP48	37	2-3.6	3	0	0	0	0	1	10	0	0	0	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101CBT6	36	Cortex-M3	128	16	0	LQFP48	37	2-3.6	3	0	0	0	0	1	10	0	0	0	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101C8U6	36	Cortex-M3	64	10	0	UFQFPN48	37	2-3.6	3	0	0	0	0	1	10	0	0	0	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101CBU6	36	Cortex-M3	128	16	0	UFQFPN48	37	2-3.6	3	0	0	0	0	1	10	0	0	0	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F101R8T6	36	Cortex-M3	64	10	0	LQFP64	51	2-3.6	3	0	0	0	0	1	16	0	0	0	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101RBT6	36	Cortex-M3	128	16	0	LQFP64	51	2-3.6	3	0	0	0	0	1	16	0	0	0	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101V8T6	36	Cortex-M3	64	10	0	LQFP100	80	2-3.6	3	0	0	0	0	1	16	0	0	0	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101VBT6	36	Cortex-M3	128	16	0	LQFP100	80	2-3.6	3	0	0	0	0	1	16	0	0	0	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101RCT6	36	Cortex-M3	256	32	0	LQFP64	51	2-3.6	6	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101RDT6	36	Cortex-M3	384	48	0	LQFP64	51	2-3.6	6	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101RET6	36	Cortex -M3	512	48	0	LQFP64	51	2-3.6	6	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101VCT6	36	Cortex-M3	256	32	0	LQFP100	80	2-3.6	6	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101VDT6	36	Cortex-M3	384	48	0	LQFP100	80	2-3.6	6	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101VET6	36	Cortex-M3	512	48	0	LQFP100	80	2-3.6	6	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101ZCT6	36	Cortex-M3	256	32	0	LQFP144	112	2-3.6	6	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101ZDT6	36	Cortex-M3	384	48	0	LQFP144	112	2-3.6	6	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101ZET6	36	Cortex-M3	512	48	0	LQFP144	112	2-3.6	6	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101RFT6	36	Cortex-M3	768	80	0	LQFP64	51	2-3.6	12	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101RGT6	36	Cortex-M3	1024	80	0	LQFP64	51	2-3.6	12	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101VFT6	36	Cortex-M3	768	80	0	LQFP100	80	2-3.6	12	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101VGT6	36	Cortex-M3	1024	80	0	LQFP100	80	2-3.6	12	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F101ZFT6	36	Cortex-M3	768	80	0	LQFP144	112	2-3.6	12	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F101ZGT6	36	Cortex-M3	1024	80	0	LQFP144	112	2-3.6	12	0	0	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F102 USB型 - 48 MHz																																																						
STM32F102C4T6	48	Cortex-M3	16	4	0	LQFP48	37	2-3.6	2	0	0	0	0	1	10	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F102C6T6	48	Cortex -M3	32	6	0	LQFP48	37	2-3.6	2	0	0	0	0	1	10	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F102R4T6	48	Cortex-M3	16	4	0	LQFP64	51	2-3.6	2	0	0	0	0	1	16	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F102R6T6	48	Cortex-M3	32	6	0	LQFP64	51	2-3.6	2	0	0	0	0	1	16	0	0	0	0	0	0	0	1	0	0	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F102C8T6	48	Cortex-M3	64	10	0	LQFP48	37	2-3.6	3	0	0	0	0	1	10	0	0	0	0	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F102CBT6	48	Cortex-M3	128	16	0	LQFP48	37	2-3.6	3	0	0	0	0	1	10	0	0	0	0	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F102R8T6	48	Cortex-M3	64	10	0	LQFP64	51	2-3.6	3	0	0	0	0	1	16	0	0	0	0	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F102RBT6	48	Cortex-M3	128	16	0	LQFP64	51	2-3.6	3	0	0	0	0	1	16	0	0	0	0	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103性能型 - 72 MHz																																																						
STM32F103T4U6	72	Cortex-M3	16	6	0	VFQFPN36	26	2-3.6	3	0	1	0	0	2	10	0	0	0	0	0	0	0	1	0	0	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103T6U6	72	Cortex-M3	32	10	0	VFQFPN36	26	2-3.6	3	0	1	0	0	2	10	0	0	0	0	0	0	0	1	0	0	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103C4T6	72	Cortex-M3	16	6	0	LQFP48	37	2-3.6	3	0	1	0	0	2	10	0	0	0	0	0	0	0	1	0	0	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103C4U6	72	Cortex-M3	16	6	0	UFQFPN48	37	2-3.6	3	0	1	0	0	2	10	0	0	0	0	0	0	0	1	0	0	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103C6U6	72	Cortex-M3	32	10	0	UFQFPN48	37	2-3.6	3	0	1	0	0	2	10	0	0	0	0	0	0	0	1	0	0	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103C6T6	72	Cortex-M3	32	10	0	LQFP48	37	2-3.6	3	0	1	0	0	2	10	0	0	0	0	0	0	0	1	0	0	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103R4H6	72	Cortex-M3	16	6	0	TFBGA64	51	2-3.6	3	0	1	0	0	2	16	0	0	0	0	0	0	0	1	0	0	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F103R4T6	72	Cortex-M3	16	6	0	LQFP64	51	2-3.6	3	0	1	0	0	2	16	0	0	0	0	0	0	0	1	0	0	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103R6H6	72	Cortex-M3	32	10	0	TFBGA64	51	2-3.6	3	0	1	0	0	2	16	0	0	0	0	0	0	0	1	0	0	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103R6T6	72	Cortex-M3	32	10	0	LQFP64	51	2-3.6	3	0	1	0	0	2	16	0	0	0	0	0	0	0	1	0	0	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103T8U6	72	Cortex-M3	64	20	0	VFQFPN36	26	2-3.6	4	0	1	0	0	2	10	0	0	0	0	0	0	0	1	0	0	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103TBU6	72	Cortex-M3	128	20	0	VFQFPN36	26	2-3.6	4	0	1	0	0	2	10	0	0	0	0	0	0	0	1	0	0	1	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103C8T6	72	Cortex-M3	64	20	0	LQFP48	37	2-3.6	4	0	1	0	0	2	10	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103C8U6	72	Cortex-M3	64	20	0	UFQFPN48	37	2-3.6	4	0	1	0	0	2	10	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103CBT6	72	Cortex-M3	128	20	0	LQFP48	37	2-3.6	4	0	1	0	0	2	10	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103CBU6	72	Cortex-M3	128	20	0	UFQFPN48	37	2-3.6	4	0	1	0	0	2	10	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103R8T6	72	Cortex-M3	64	20	0	LQFP64	51	2-3.6	4	0	1	0	0	2	16	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103R8H6	72	Cortex-M3	64	20	0	TFBGA64	51	2-3.6	4	0	1	0	0	2	16	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103RBT6	72	Cortex-M3	128	20	0	LQFP64	51	2-3.6	4	0	1	0	0	2	16	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103RBH6	72	Cortex-M3	128	20	0	TFBGA64	51	2-3.6	4	0	1	0	0	2	16	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103V8T6	72	Cortex-M3	64	20	0	LQFP100	80	2-3.6	4	0	1	0	0	2	16	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103V8H6	72	Cortex-M3	64	20	0	LFBGA100	80	2-3.6	4	0	1	0	0	2	16	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103V8I6	72	Cortex-M3	64	20	0	UFBGA100	80	2-3.6	4	0	1	0	0	2	16	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103VBT6	72	Cortex-M3	128	20	0	LQFP100	80	2-3.6	4	0	1	0	0	2	16	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103VBH6	72	Cortex-M3	128	20	0	LFBGA100	80	2-3.6	4	0	1	0	0	2	16	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F103VBI6	72	Cortex-M3	128	20	0	UFBGA100	80	2-3.6	4	0	1	0	0	2	16	0	0	0	0	0	0	0	2	0	0	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103RCT6	72	Cortex-M3	256	48	0	LQFP64	51	2-3.6	8	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103RDT6	72	Cortex-M3	384	64	0	LQFP64	51	2-3.6	8	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103RET6	72	Cortex-M3	512	64	0	LQFP64	51	2-3.6	8	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103RCY6	72	Cortex-M3	256	48	0	WLCSP64	51	2-3.6	8	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103RDY6	72	Cortex-M3	384	64	0	WLCSP64	51	2-3.6	8	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103REY6	72	Cortex-M3	512	64	0	WLCSP64	51	2-3.6	8	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103VCT6	72	Cortex-M3	256	48	0	LQFP100	80	2-3.6	8	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103VDT6	72	Cortex-M3	384	64	0	LQFP100	80	2-3.6	8	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103VET6	72	Cortex-M3	512	64	0	LQFP100	80	2-3.6	8	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103VCH6	72	Cortex-M3	256	48	0	LFBGA100	80	2-3.6	8	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103VDH6	72	Cortex-M3	384	64	0	LFBGA100	80	2-3.6	8	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103VEH6	72	Cortex-M3	512	64	0	LFBGA100	80	2-3.6	8	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103ZCT6	72	Cortex-M3	256	48	0	LQFP144	112	2-3.6	8	0	2	0	0	3	21	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103ZDT6	72	Cortex-M3	384	64	0	LQFP144	112	2-3.6	8	0	2	0	0	3	21	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103ZET6	72	Cortex-M3	512	64	0	LQFP144	112	2-3.6	8	0	2	0	0	3	21	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103ZCH6	72	Cortex-M3	256	48	0	LFBGA144	112	2-3.6	8	0	2	0	0	3	21	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103ZDH6	72	Cortex-M3	384	64	0	LFBGA144	112	2-3.6	8	0	2	0	0	3	21	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F103ZEH6	72	Cortex-M3	512	64	0	LFBGA144	112	2-3.6	8	0	2	0	0	3	21	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103RFT6	72	Cortex-M3	768	96	0	LQFP64	51	2-3.6	14	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103RGT6	72	Cortex-M3	1024	96	0	LQFP64	51	2-3.6	14	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103VFT6	72	Cortex-M3	768	96	0	LQFP100	80	2-3.6	14	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103VGT6	72	Cortex-M3	1024	96	0	LQFP100	80	2-3.6	14	0	2	0	0	3	16	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103ZFT6	72	Cortex-M3	768	96	0	LQFP144	112	2-3.6	14	0	2	0	0	3	21	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103ZGT6	72	Cortex-M3	1024	96	0	LQFP144	112	2-3.6	14	0	2	0	0	3	21	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103ZFH6	72	Cortex-M3	768	96	0	LFBGA144	112	2-3.6	14	0	2	0	0	3	21	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F103ZGH6	72	Cortex-M3	1024	96	0	LFBGA144	112	2-3.6	14	0	2	0	0	3	21	0	0	2	0	0	0	0	3	0	2	2	3+2	0	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F105 / 107连接型 – 72 MHz																																																						
STM32F105R8T6	72	Cortex-M3	64	64	0	LQFP64	51	2-3.6	7	0	1	0	0	2	16	0	0	2	0	0	0	0	3	0	[2]	2	3+2	0	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F105RBT6	72	Cortex-M3	128	64	0	LQFP64	51	2-3.6	7	0	1	0	0	2	16	0	0	2	0	0	0	0	3	0	[2]	2	3+2	0	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F105RCT6	72	Cortex-M3	256	64	0	LQFP64	51	2-3.6	7	0	1	0	0	2	16	0	0	2	0	0	0	0	3	0	[2]	2	3+2	0	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F105V8T6	72	Cortex-M3	64	64	0	LQFP100	80	2-3.6	7	0	1	0	0	2	16	0	0	2	0	0	0	0	3	0	[2]	2	3+2	0	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F105VBH6	72	Cortex-M3	128	64	0	LFBGA100	80	2-3.6	7	0	1	0	0	2	16	0	0	2	0	0	0	0	3	0	[2]	2	3+2	0	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F105VBT6	72	Cortex-M3	128	64	0	LQFP100	80	2-3.6	7	0	1	0	0	2	16	0	0	2	0	0	0	0	3	0	[2]	2	3+2	0	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F105VCT6	72	Cortex-M3	256	64	0	LQFP100	80	2-3.6	7	0	1	0	0	2	16	0	0	2	0	0	0	0	3	0	[2]	2	3+2	0	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F107RBT6	72	Cortex-M3	128	64	0	LQFP64	51	2-3.6	7	0	1	0	0	2	16	0	0	2	0	0	0	0	3	0	[2]	1	3+2	0	2	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F107RCT6	72	Cortex-M3	256	64	0	LQFP64	51	2-3.6	7	0	1	0	0	2	16	0	0	2	0	0	0	0	3	0	[2]	1	3+2	0	2	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F107VBT6	72	Cortex-M3	128	64	0	LQFP100	80	2-3.6	7	0	1	0	0	2	16	0	0	2	0	0	0	0	3	0	[2]	1	3+2	0	2	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F107VCT6	72	Cortex-M3	256	64	0	LQFP100	80	2-3.6	7	0	1	0	0	2	16	0	0	2	0	0	0	0	3	0	[2]	1	3+2	0	2	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F107VCH6	72	Cortex-M3	256	64	0	LFBGA100	80	2-3.6	7	0	1	0	0	2	16	0	0	2	0	0	0	0	3	0	[2]	1	3+2	0	2	0	0	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F3系列 – Arm® Cortex®-M4混合信号MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F301入门型 - 72 MHz																																																							
STM32F301K6U6	72	Cortex-M4	32	16	0	UFQFPN32	24	2-3.6	5	1	1	0	0	1	8	0	0	1	2	1	0	0	2	0	[2]	3	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F301K8U6	72	Cortex-M4	64	16	0	UFQFPN32	24	2-3.6	5	1	1	0	0	1	8	0	0	1	2	1	0	0	2	0	[2]	3	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F301C6T6	72	Cortex-M4	32	16	0	LQFP48	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F301C6Y6	72	Cortex-M4	32	16	0	WLCSP49	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F301C8T6	72	Cortex-M4	64	16	0	LQFP48	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F301C8Y6	72	Cortex-M4	64	16	0	WLCSP49	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F301R6T6	72	Cortex-M4	32	16	0	LQFP64	51	2-3.6	5	1	1	0	0	1	15	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F301R8T6	72	Cortex-M4	64	16	0	LQFP64	51	2-3.6	5	1	1	0	0	1	15	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302 USB型 - 72 MHz																																																							
STM32F302K6U6	72	Cortex-M4	32	16	0	UFQFPN32	24	2-3.6	5	1	1	0	0	1	8	0	0	1	2	1	0	0	2	0	[2]	3	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302K8U6	72	Cortex-M4	64	16	0	UFQFPN32	24	2-3.6	5	1	1	0	0	1	8	0	0	1	2	1	0	0	2	0	[2]	3	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302C6T6	72	Cortex-M4	32	16	0	LQFP48	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302C6Y6	72	Cortex-M4	32	16	0	WLCSP49	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302C8T6	72	Cortex-M4	64	16	0	LQFP48	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302C8Y6	72	Cortex-M4	64	16	0	WLCSP49	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302R6T6	72	Cortex-M4	32	16	0	LQFP64	51	2-3.6	5	1	1	0	0	1	15	0	0	1	3	1	0	0	2	0	[2]	3	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302R8T6	72	Cortex-M4	64	16	0	LQFP64	51	2-3.6	5	1	1	0	0	1	15	0	0	1	3	1	0	0	2	0	[2]	3	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302CBT6	72	Cortex-M4	128	32	0	LQFP48	37	2-3.6	7	1	1	0	0	2	9	0	0	1	4	2	0	0	3	0	[2]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F3系列 – Arm® Cortex®-M4混合信号MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F302CCT6	72	Cortex-M4	256	40	0	LQFP48	37	2-3.6	7	1	1	0	0	2	9	0	0	1	4	2	0	0	3	0	[2]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302RBT6	72	Cortex-M4	128	32	0	LQFP64	52	2-3.6	7	1	1	0	0	2	16	0	0	1	4	2	0	0	3	0	[2]	2	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302RCT6	72	Cortex-M4	256	40	0	LQFP64	52	2-3.6	7	1	1	0	0	2	16	0	0	1	4	2	0	0	3	0	[2]	2	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302VBT6	72	Cortex-M4	128	32	0	LQFP100	87	2-3.6	7	1	1	0	0	2	17	0	0	1	4	2	0	0	3	0	[2]	2	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302VCT6	72	Cortex-M4	256	40	0	LQFP100	87	2-3.6	7	1	1	0	0	2	17	0	0	1	4	2	0	0	3	0	[2]	2	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302VCY6	72	Cortex-M4	256	40	0	WLCSP100	77	2-3.6	1	1	1	0	0	2	17	0	0	1	4	2	0	0	3	0	[2]	2	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302RDT6	72	Cortex-M4	384	64	0	LQFP64	51	2-3.6	7	1	1	0	0	2	16	0	0	1	4	2	0	0	4	0	[2]	3	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302RET6	72	Cortex-M4	512	64	0	LQFP64	51	2-3.6	7	1	1	0	0	2	16	0	0	1	4	2	0	0	4	0	[2]	3	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302VDT6	72	Cortex-M4	384	64	0	LQFP100	82	2-3.6	7	1	1	0	0	2	17	0	0	1	4	2	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302VDH6	72	Cortex-M4	384	64	0	UFBGA100	84	2-3.6	7	1	1	0	0	2	17	0	0	1	4	2	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302VDY6	72	Cortex-M4	384	64	0	WLCSP100	77	2-3.6	7	1	1	0	0	2	17	0	0	1	4	2	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302VET6	72	Cortex-M4	512	64	0	LQFP100	82	2-3.6	7	1	1	0	0	2	17	0	0	1	4	2	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302VEH6	72	Cortex-M4	512	64	0	UFBGA100	84	2-3.6	7	1	1	0	0	2	17	0	0	1	4	2	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302VEY6	72	Cortex-M4	512	64	0	WLCSP100	77	2-3.6	7	1	1	0	0	2	17	0	0	1	4	2	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302ZDT6	72	Cortex-M4	384	64	0	LQFP144	115	2-3.6	7	1	1	0	0	2	18	0	0	1	4	2	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F302ZET6	72	Cortex-M4	512	64	0	LQFP144	115	2-3.6	7	1	1	0	0	2	18	0	0	1	4	2	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303性能型 - 72 MHz																																																						
STM32F303K6T6	72	Cortex-M4	32	16	0	LQFP32	25	2-3.6	7	1	1	0	0	2	9	0	0	3	2	1	0	0	1	0	0	1	2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F3系列 – Arm® Cortex®-M4混合信号MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F303K8T6	72	Cortex-M4	64	16	0	LQFP32	25	2-3.6	7	1	1	0	0	2	9	0	0	3	2	1	0	0	1	0	0	1	2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303C6T6	72	Cortex-M4	32	16	0	LQFP48	37	2-3.6	7	1	1	0	0	2	15	0	0	3	3	1	0	0	1	0	0	1	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303C8T6	72	Cortex-M4	64	16	0	LQFP48	37	2-3.6	7	1	1	0	0	2	15	0	0	3	3	1	0	0	1	0	0	1	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303R6T6	72	Cortex-M4	32	16	0	LQFP64	51	2-3.6	7	1	1	0	0	2	21	0	0	3	3	1	0	0	1	0	0	1	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303R8T6	72	Cortex-M4	64	16	0	LQFP64	51	2-3.6	7	1	1	0	0	2	21	0	0	3	3	1	0	0	1	0	0	1	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303CBT6	72	Cortex-M4	128	40	0	LQFP48	37	2-3.6	9	1	2	0	0	4	15	0	0	2	7	4	0	0	3	0	[2]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303CCT6	72	Cortex-M4	256	48	0	LQFP48	37	2-3.6	9	1	2	0	0	4	15	0	0	2	7	4	0	0	3	0	[2]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303RBT6	72	Cortex-M4	128	40	0	LQFP64	52	2-3.6	9	1	2	0	0	4	22	0	0	2	7	4	0	0	3	0	[2]	2	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303RCT6	72	Cortex-M4	256	48	0	LQFP64	52	2-3.6	9	1	2	0	0	4	22	0	0	2	7	4	0	0	3	0	[2]	2	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303VBT6	72	Cortex-M4	128	40	0	LQFP100	87	2-3.6	9	1	2	0	0	4	39	0	0	2	7	4	0	0	3	0	[2]	2	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303VBY6	72	Cortex-M4	128	40	0	WLCSP100	77	2-3.6	9	1	2	0	0	4	32	0	0	2	7	4	0	0	3	0	[2]	2	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303VCT6	72	Cortex-M4	256	48	0	LQFP100	87	2-3.6	9	1	2	0	0	4	39	0	0	2	7	4	0	0	3	0	[2]	2	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303VCY6	72	Cortex-M4	256	48	0	WLCSP100	77	2-3.6	9	1	2	0	0	4	32	0	0	2	7	4	0	0	3	0	[2]	2	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303RDT6	72	Cortex-M4	384	80	0	LQFP64	51	2-3.6	9	1	2	0	0	4	22	0	0	2	7	4	0	0	4	0	[2]	3	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303RET6	72	Cortex-M4	512	80	0	LQFP64	51	2-3.6	9	1	2	0	0	4	22	0	0	2	7	4	0	0	4	0	[2]	3	3+2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303VDT6	72	Cortex-M4	384	80	0	LQFP100	82	2-3.6	10	1	3	0	0	4	39	0	0	2	7	4	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303VDH6	72	Cortex-M4	384	80	0	UFBGA100	84	2-3.6	10	1	3	0	0	4	39	0	0	2	7	4	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303VDY6	72	Cortex-M4	384	80	0	WLCSP100	77	2-3.6	10	1	3	0	0	4	33	0	0	2	7	4	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F3系列 – Arm® Cortex®-M4混合信号MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F303VET6	72	Cortex-M4	512	80	0	LQFP100	82	2-3.6	10	1	3	0	0	4	39	0	0	2	7	4	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303VEH6	72	Cortex-M4	512	80	0	UFBGA100	84	2-3.6	10	1	3	0	0	4	39	0	0	2	7	4	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303VEY6	72	Cortex-M4	512	80	0	WLCSP100	77	2-3.6	10	1	3	0	0	4	33	0	0	2	7	4	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303ZDT6	72	Cortex-M4	384	80	0	LQFP144	115	2-3.6	10	1	3	0	0	4	40	0	0	2	7	4	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F303ZET6	72	Cortex-M4	512	80	0	LQFP144	115	2-3.6	10	1	3	0	0	4	40	0	0	2	7	4	0	0	4	0	[2]	3	3+2	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F373精确测量型 - 72 MHz																																																						
STM32F373C8T6	72	Cortex-M4	64	16	0	LQFP48	36	2-3.6	12	2	0	0	0	1	9	3	8	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F373CBT6	72	Cortex-M4	128	24	0	LQFP48	36	2-3.6	12	2	0	0	0	1	9	3	8	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F373CCT6	72	Cortex-M4	256	32	0	LQFP48	36	2-3.6	12	2	0	0	0	1	9	3	8	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F373R8T6	72	Cortex-M4	64	16	0	LQFP64	52	2-3.6	12	2	0	0	0	1	16	3	8	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F373RBT6	72	Cortex-M4	128	24	0	LQFP64	52	2-3.6	12	2	0	0	0	1	16	3	8	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F373RCT6	72	Cortex-M4	256	32	0	LQFP64	52	2-3.6	12	2	0	0	0	1	16	3	8	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F373V8T6	72	Cortex-M4	64	16	0	LQFP100	84	2-3.6	12	2	0	0	0	1	16	3	21	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F373VBT6	72	Cortex-M4	128	24	0	LQFP100	84	2-3.6	12	2	0	0	0	1	16	3	21	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F373VCT6	72	Cortex-M4	256	32	0	LQFP100	84	2-3.6	12	2	0	0	0	1	16	3	21	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F373VCH6	72	Cortex-M4	256	32	0	UFBGA100	84	2-3.6	12	2	0	0	0	1	16	3	21	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F334数字电源型 - 72 MHz																																																						
STM32F334K4T6	72	Cortex-M4	16	16	0	LQFP32	25	2-3.6	7	1	1	0	1	2	9	0	0	3	2	1	0	0	1	0	0	1	2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F334K6T6	72	Cortex-M4	32	16	0	LQFP32	25	2-3.6	7	1	1	0	1	2	9	0	0	3	2	1	0	0	1	0	0	1	2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F3系列 – Arm® Cortex®-M4混合信号MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F334K8T6	72	Cortex-M4	64	16	0	LQFP32	25	2-3.6	7	1	1	0	1	2	9	0	0	3	2	1	0	0	1	0	0	1	2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F334C4T6	72	Cortex-M4	16	16	0	LQFP48	37	2-3.6	7	1	1	0	1	2	15	0	0	3	3	1	0	0	1	0	0	1	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F334C6T6	72	Cortex-M4	32	16	0	LQFP48	37	2-3.6	7	1	1	0	1	2	15	0	0	3	3	1	0	0	1	0	0	1	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F334C8T6	72	Cortex-M4	64	16	0	LQFP48	37	2-3.6	7	1	1	0	1	2	15	0	0	3	3	1	0	0	1	0	0	1	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F334R4T6	72	Cortex-M4	16	16	0	LQFP64	51	2-3.6	7	1	1	0	1	2	21	0	0	3	3	1	0	0	1	0	0	1	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F334R6T6	72	Cortex-M4	32	16	0	LQFP64	51	2-3.6	7	1	1	0	1	2	21	0	0	3	3	1	0	0	1	0	0	1	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F334R8T6	72	Cortex-M4	64	16	0	LQFP64	51	2-3.6	7	1	1	0	1	2	21	0	0	3	3	1	0	0	1	0	0	1	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F3x8低电压型 - 72 MHz																																																						
STM32F318K8U6	72	Cortex-M4	64	16	0	UFQFPN32	23	1.65-1.95	5	1	1	0	0	1	8	0	0	1	2	1	0	0	2	0	[2]	3	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F318C8Y6	72	Cortex-M4	64	16	0	WLCSP49	36	1.65-1.95	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F318C8T6	72	Cortex-M4	64	16	0	LQFP48	36	1.65-1.95	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F328C8T6	72	Cortex-M4	64	16	0	LQFP48	36	1.65-1.95	7	1	1	0	0	2	14	0	0	3	3	1	0	0	1	0	0	1	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F358CCT6	72	Cortex-M4	256	48	0	LQFP48	36	1.65-1.95	9	1	2	0	0	4	14	0	0	2	7	4	0	0	3	0	[2]	2	3+2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F358RCT6	72	Cortex-M4	256	48	0	LQFP64	51	1.65-1.95	9	1	2	0	0	4	21	0	0	2	7	4	0	0	3	0	[2]	2	3+2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F358VCT6	72	Cortex-M4	256	48	0	LQFP100	86	1.65-1.95	9	1	2	0	0	4	38	0	0	2	7	4	0	0	3	0	[2]	2	3+2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F378CCT6	72	Cortex-M4	256	32	0	LQFP48	36	1.65-1.95	12	2	0	0	0	1	9	3	21	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F378RCT6	72	Cortex-M4	256	32	0	LQFP64	51	1.65-1.95	12	2	0	0	0	1	16	3	21	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F378RCY6	72	Cortex-M4	256	32	0	WLCSP66	51	1.65-1.95	12	2	0	0	0	1	16	3	21	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F3系列 – Arm® Cortex®-M4混合信号MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F378VCT6	72	Cortex-M4	256	32	0	LQFP100	83	1.65-1.95	12	2	0	0	0	1	16	3	21	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F378VCH6	72	Cortex-M4	256	32	0	UFBGA100	83	1.65-1.95	12	2	0	0	0	1	16	3	21	3	2	0	0	0	3	0	[3]	2	3	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F398VET6	72	Cortex-M4	512	80	0	LQFP100	85	1.65-1.95	10	1	3	0	0	4	38	0	0	2	7	4	0	0	4	0	[2]	3	3+2	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F2系列 – Arm® Cortex®-M3高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F2x5基础型 - 120 MHz																																																							
STM32F205RBT6	120	Cortex-M3	128	64	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205RCT6	120	Cortex-M3	256	96	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205RET6	120	Cortex-M3	512	128	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205REY6	120	Cortex-M3	512	128	0	WLCSP66	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205RFT6	120	Cortex-M3	768	128	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205RGT6	120	Cortex-M3	1024	128	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205RGY6	120	Cortex-M3	1024	128	0	WLCSP66	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205VBT6	120	Cortex-M3	128	64	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205VCT6	120	Cortex-M3	256	96	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205VET6	120	Cortex-M3	512	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205VFT6	120	Cortex-M3	768	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205VGT6	120	Cortex-M3	1024	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205ZCT6	120	Cortex-M3	256	96	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205ZET6	120	Cortex-M3	512	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205ZFT6	120	Cortex-M3	768	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205ZGT6	120	Cortex-M3	1024	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F215RET6	120	Cortex-M3	512	128	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1

STM32 F2系列 – Arm® Cortex®-M3高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F215RGT6	120	Cortex-M3	1024	128	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1
STM32F215VET6	120	Cortex-M3	512	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1
STM32F215VGT6	120	Cortex-M3	1024	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1
STM32F215ZET6	120	Cortex-M3	512	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1
STM32F215ZGT6	120	Cortex-M3	1024	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1
STM32F2x7基础型 - 120 MHz																																																						
STM32F207VCT6	120	Cortex-M3	256	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207VET6	120	Cortex-M3	512	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207VFT6	120	Cortex-M3	768	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207VGT6	120	Cortex-M3	1024	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207ZCT6	120	Cortex-M3	256	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207ZET6	120	Cortex-M3	512	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207ZFT6	120	Cortex-M3	768	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207ZGT6	120	Cortex-M3	1024	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207ICT6	120	Cortex-M3	256	128	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207IET6	120	Cortex-M3	512	128	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207IFT6	120	Cortex-M3	768	128	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207IGT6	120	Cortex-M3	1024	128	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0

STM32 F2系列 – Arm® Cortex®-M3高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F207ICH6	120	Cortex-M3	256	128	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207IEH6	120	Cortex-M3	512	128	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207IFH6	120	Cortex-M3	768	128	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207IGH6	120	Cortex-M3	1024	128	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F217VET6	120	Cortex-M3	512	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	2	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F217VGT6	120	Cortex-M3	1024	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	2	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F217ZET6	120	Cortex-M3	512	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	2	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F217ZGT6	120	Cortex-M3	1024	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	2	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F217IET6	120	Cortex-M3	512	128	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	2	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F217IGT6	120	Cortex-M3	1024	128	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	2	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F217IEH6	120	Cortex-M3	512	128	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	2	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F217IGH6	120	Cortex-M3	1024	128	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	2	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1

STM32 F4系列 – Arm® Cortex®-M4高性能MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F401入门型 - 84 MHz																																																						
STM32F401CBU6	84	Cortex-M4	128	64	0	UFQFPN48	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401CBY6	84	Cortex-M4	128	64	0	WLCSP49	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401RBT6	84	Cortex-M4	128	64	0	LQFP64	50	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401VBT6	84	Cortex-M4	128	64	0	LQFP100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401VBH6	84	Cortex-M4	128	64	0	UFBGA100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401CCU6	84	Cortex-M4	256	64	0	UFQFPN48	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401CCY6	84	Cortex-M4	256	64	0	WLCSP49	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401RCT6	84	Cortex-M4	256	64	0	LQFP64	50	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401VCT6	84	Cortex-M4	256	64	0	LQFP100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401VCH6	84	Cortex-M4	256	64	0	UFBGA100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401CDU6	84	Cortex-M4	384	96	0	UFQFPN48	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401CDY6	84	Cortex-M4	384	96	0	WLCSP49	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401RDT6	84	Cortex-M4	384	96	0	LQFP64	50	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401VDT6	84	Cortex-M4	384	96	0	LQFP100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401VDH6	84	Cortex-M4	384	96	0	UFBGA100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401CEU6	84	Cortex-M4	512	96	0	UFQFPN48	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401CEY6	84	Cortex-M4	512	96	0	WLCSP49	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F4系列 – Arm® Cortex®-M4高性能MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F401RET6	84	Cortex-M4	512	96	0	LQFP64	50	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401VET6	84	Cortex-M4	512	96	0	LQFP100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F401VEH6	84	Cortex-M4	512	96	0	UFBGA100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F410入门型 - 100 MHz																																																					
STM32F410T8Y6	100	Cortex-M4	64	32	0	WLCSP36	23	1.7-3.6	4	1	1	1	0	1	4	0	0	1	0	0	0	0	1	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F410TBY6	100	Cortex-M4	128	32	0	WLCSP36	23	1.7-3.6	4	1	1	1	0	1	4	0	0	1	0	0	0	0	1	0	[1]	2	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F410C8U6	100	Cortex-M4	64	32	0	UFQFPN48	36	1.7-3.6	4	1	1	1	0	1	10	0	0	1	0	0	0	0	3	0	[3]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F410CBU6	100	Cortex-M4	128	32	0	UFQFPN48	36	1.7-3.6	4	1	1	1	0	1	10	0	0	1	0	0	0	0	3	0	[3]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F410R8T6	100	Cortex-M4	64	32	0	LQFP64	50	1.7-3.6	4	1	1	1	0	1	16	0	0	1	0	0	0	0	3	0	[3]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F410RBT6	100	Cortex-M4	128	32	0	LQFP64	50	1.7-3.6	4	1	1	1	0	1	16	0	0	1	0	0	0	0	3	0	[3]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F411入门型 - 100 MHz																																																					
STM32F411CCU6	100	Cortex-M4	256	128	0	UFQFPN48	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	5	0	[5]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F411CCY6	100	Cortex-M4	256	128	0	WLCSP49	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	5	0	[5]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F411CEU6	100	Cortex-M4	512	128	0	UFQFPN48	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	5	0	[5]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F411CEY6	100	Cortex-M4	512	128	0	WLCSP49	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	5	0	[5]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F411RCT6	100	Cortex-M4	256	128	0	LQFP64	50	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	5	0	[5]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F411RET6	100	Cortex-M4	512	128	0	LQFP64	50	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	5	0	[5]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F411VCT6	100	Cortex-M4	256	128	0	LQFP100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	5	0	[5]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F411VCH6	100	Cortex-M4	256	128	0	UFBGA100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	5	0	[5]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F4系列 – Arm® Cortex®-M4高性能MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F411VET6	100	Cortex-M4	512	128	0	LQFP100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	5	0	[5]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F411VEH6	100	Cortex-M4	512	128	0	UFBGA100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	5	0	[5]	3	3	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F412入门型 - 100 MHz																																																						
STM32F412CEU6	100	Cortex-M4	512	256	0	UFQFPN48	36	1.7-3.6	12	2	2	0	0	1	10	0	0	0	0	0	0	0	5	0	[5]	4	3	0	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412RET6	100	Cortex-M4	512	256	0	LQFP64	50	1.7-3.6	12	2	2	0	0	1	16	0	0	0	0	0	0	0	5	1 [Quad]	[5]	4	4	0	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412REY6	100	Cortex-M4	512	256	0	WLCSP64	50	1.7-3.6	12	2	2	0	0	1	16	0	0	0	0	0	0	0	5	1 [Quad]	[5]	4	4	0	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412VET6	100	Cortex-M4	512	256	0	LQFP100	81	1.7-3.6	12	2	2	0	0	1	16	0	0	0	0	0	0	0	5	1 [Quad]	[5]	4	4	0	2	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412VEH6	100	Cortex-M4	512	256	0	UFBGA100	81	1.7-3.6	12	2	2	0	0	1	16	0	0	0	0	0	0	0	5	1 [Quad]	[5]	4	4	0	2	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412ZET6	100	Cortex-M4	512	256	0	LQFP144	114	1.7-3.6	12	2	2	0	0	1	16	0	0	0	0	0	0	0	5	1 [Quad]	[5]	4	4	0	2	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412ZEH6	100	Cortex-M4	512	256	0	UFBGA144	114	1.7-3.6	12	2	2	0	0	1	16	0	0	0	0	0	0	0	5	1 [Quad]	[5]	4	4	0	2	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412CGU6	100	Cortex-M4	1024	256	0	UFQFPN48	36	1.7-3.6	12	2	2	0	0	1	10	0	0	0	0	0	0	0	5	0	[5]	4	3	0	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412RGT6	100	Cortex-M4	1024	256	0	LQFP64	50	1.7-3.6	12	2	2	0	0	1	16	0	0	0	0	0	0	0	5	1 [Quad]	[5]	4	4	0	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412RGY6	100	Cortex-M4	1024	256	0	WLCSP64	50	1.7-3.6	12	2	2	0	0	1	16	0	0	0	0	0	0	0	5	1 [Quad]	[5]	4	4	0	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412VGT6	100	Cortex-M4	1024	256	0	LQFP100	81	1.7-3.6	12	2	2	0	0	1	16	0	0	0	0	0	0	0	5	1 [Quad]	[5]	4	4	0	2	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412VGH6	100	Cortex-M4	1024	256	0	UFBGA100	81	1.7-3.6	12	2	2	0	0	1	16	0	0	0	0	0	0	0	5	1 [Quad]	[5]	4	4	0	2	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412ZGT6	100	Cortex-M4	1024	256	0	LQFP144	114	1.7-3.6	12	2	2	0	0	1	16	0	0	0	0	0	0	0	5	1 [Quad]	[5]	4	4	0	2	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F412ZGH6	100	Cortex-M4	1024	256	0	UFBGA144	114	1.7-3.6	12	2	2	0	0	1	16	0	0	0	0	0	0	0	5	1 [Quad]	[5]	4	4	0	2	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32F413/423入门型 - 100 MHz																																																						
STM32F413CGU6	100	Cortex-M4	1024	320	0	UFQFPN48	36	1.7-3.6	12	2	2	1	0	1	10	0	0	2	0	0	0	0	5	0	[5]	4	3+3	0	3	1	0	0	0	1	0	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0

STM32 F4系列 – Arm® Cortex®-M4高性能MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F413RGT6	100	Cortex-M4	1024	320	0	LQFP64	50	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+3	0	3	1	0	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F413VGT6	100	Cortex-M4	1024	320	0	LQFP100	81	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F413VGJ6	100	Cortex-M4	1024	320	0	UFBGA100	81	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F413ZGT6	100	Cortex-M4	1024	320	0	LQFP144	114	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F413ZGJ6	100	Cortex-M4	1024	320	0	UFBGA144	114	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F413CHU6	100	Cortex-M4	1536	320	0	UFQFPN48	36	1.7-3.6	12	2	2	1	0	1	10	0	0	2	0	0	0	0	5	0	[5]	4	3+3	0	3	1	0	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F413RHT6	100	Cortex-M4	1536	320	0	LQFP64	50	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+3	0	3	1	0	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F413VHT6	100	Cortex-M4	1536	320	0	LQFP100	81	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F413VHJ6	100	Cortex-M4	1536	320	0	UFBGA100	81	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F413ZHT6	100	Cortex-M4	1536	320	0	LQFP144	114	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F413ZHJ6	100	Cortex-M4	1536	320	0	UFBGA144	114	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F423CHU6	100	Cortex-M4	1536	320	0	UFQFPN48	36	1.7-3.6	12	2	2	1	0	1	10	0	0	2	0	0	0	0	5	0	[5]	4	3+3	0	3	1	0	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F423RHT6	100	Cortex-M4	1536	320	0	LQFP64	50	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+3	0	3	1	0	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F423VHT6	100	Cortex-M4	1536	320	0	LQFP100	81	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F423VHJ6	100	Cortex-M4	1536	320	0	UFBGA100	81	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F423ZHT6	100	Cortex-M4	1536	320	0	LQFP144	114	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0
STM32F423ZHJ6	100	Cortex-M4	1536	320	0	UFBGA144	114	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0

STM32 F4系列 – Arm® Cortex®-M4高性能MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F405 / 415基本型 - 168 MHz																																																						
STM32F405RGT6	168	Cortex-M4	1024	192	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F405OGY6	168	Cortex-M4	1024	192	0	WLCSP90	72	1.8-3.6	12	2	2	0	0	3	13	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F405OEY6	168	Cortex-M4	512	192	0	WLCSP90	72	1.8-3.6	12	2	2	0	0	3	13	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F405VGT6	168	Cortex-M4	1024	192	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F405ZGT6	168	Cortex-M4	1024	192	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F415RGT6	168	Cortex-M4	1024	192	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1
STM32F415OGY6	168	Cortex-M4	1024	192	0	WLCSP90	72	1.8-3.6	12	2	2	0	0	3	13	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1
STM32F415VGT6	168	Cortex-M4	1024	192	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1
STM32F415ZGT6	168	Cortex-M4	1024	192	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1
STM32F407 / 417基本型 - 168 MHz																																																						
STM32F407VET6	168	Cortex-M4	512	192	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407VGT6	168	Cortex-M4	1024	192	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407ZET6	168	Cortex-M4	512	192	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407ZGT6	168	Cortex-M4	1024	192	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407IET6	168	Cortex-M4	512	192	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407IEH6	168	Cortex-M4	512	192	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407IGT6	168	Cortex-M4	1024	192	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0

STM32 F4系列 – Arm® Cortex®-M4高性能MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F407IGH6	168	Cortex-M4	1024	192	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F417VET6	168	Cortex-M4	512	192	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417VGT6	168	Cortex-M4	1024	192	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417ZET6	168	Cortex-M4	512	192	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417ZGT6	168	Cortex-M4	1024	192	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417IET6	168	Cortex-M4	512	192	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417IEH6	168	Cortex-M4	512	192	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417IGT6	168	Cortex-M4	1024	192	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417IGH6	168	Cortex-M4	1024	192	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F446基本型 - 180 MHz																																																						
STM32F446MCY6	180	Cortex-M4	256	128	0	WLCSP81	63	1.8-3.6	12	2	2	0	0	3	14	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0
STM32F446MEY6	180	Cortex-M4	512	128	0	WLCSP81	63	1.8-3.6	12	2	2	0	0	3	14	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0
STM32F446RCT6	180	Cortex-M4	256	128	0	LQFP64	50	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0
STM32F446RET6	180	Cortex-M4	512	128	0	LQFP64	50	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0
STM32F446VCT6	180	Cortex-M4	256	128	0	LQFP100	81	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0
STM32F446VET6	180	Cortex-M4	512	128	0	LQFP100	81	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0
STM32F446ZCT6	180	Cortex-M4	256	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0
STM32F446ZCH6	180	Cortex-M4	256	128	0	UFBGA144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0

STM32 F4系列 – Arm® Cortex®-M4高性能MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F446ZET6	180	Cortex-M4	512	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0
STM32F446ZEH6	180	Cortex-M4	512	128	0	UFBGA144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0
STM32F427 / 437高级型 - 180 MHz																																																						
STM32F427VGT6	180	Cortex-M4	1024	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F427VIT6	180	Cortex-M4	2048	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F427ZGT6	180	Cortex-M4	1024	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F427ZIT6	180	Cortex-M4	2048	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F427AGH6	180	Cortex-M4	1024	256	0	UFBGA169	130	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F427AIH6	180	Cortex-M4	2048	256	0	UFBGA169	130	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F427IGT6	180	Cortex-M4	1024	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F427IGH6	180	Cortex-M4	1024	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F427IIT6	180	Cortex-M4	2048	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F427IIH6	180	Cortex-M4	2048	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F437VGT6	180	Cortex-M4	1024	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F437VIT6	180	Cortex-M4	2048	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F437ZGT6	180	Cortex-M4	1024	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F437ZIT6	180	Cortex-M4	2048	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F437AIH6	180	Cortex-M4	2048	256	0	UFBGA169	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1

STM32 F4系列 – Arm® Cortex®-M4高性能MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F437IGT6	180	Cortex-M4	1024	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F437IGH6	180	Cortex-M4	1024	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F437IIT6	180	Cortex-M4	2048	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F437IIH6	180	Cortex-M4	2048	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F429 / 439高级型 - 180 MHz																																																						
STM32F429VET6	180	Cortex-M4	512	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429VGT6	180	Cortex-M4	1024	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429VIT6	180	Cortex-M4	2048	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429ZET6	180	Cortex-M4	512	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429ZGT6	180	Cortex-M4	1024	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429ZIT6	180	Cortex-M4	2048	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429ZEY6	180	Cortex-M4	512	256	0	WLCSP143	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429ZGY6	180	Cortex-M4	1024	256	0	WLCSP143	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429ZIY6	180	Cortex-M4	2048	256	0	WLCSP143	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429AGH6	180	Cortex-M4	1024	256	0	UFBGA169	130	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429AIH6	180	Cortex-M4	2048	256	0	UFBGA169	130	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429IEH6	180	Cortex-M4	512	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429IET6	180	Cortex-M4	512	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0

STM32 F4系列 – Arm® Cortex®-M4高性能MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F429IGH6	180	Cortex-M4	1024	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429IGT6	180	Cortex-M4	1024	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429IIH6	180	Cortex-M4	2048	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429IIT6	180	Cortex-M4	2048	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429BET6	180	Cortex-M4	512	256	0	LQFP208	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429BGT6	180	Cortex-M4	1024	256	0	LQFP208	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429BIT6	180	Cortex-M4	2048	256	0	LQFP208	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429NEH6	180	Cortex-M4	512	256	0	TFBGA216	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429NGH6	180	Cortex-M4	1024	256	0	TFBGA216	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429NIH6	180	Cortex-M4	2048	256	0	TFBGA216	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F439VGT6	180	Cortex-M4	1024	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439VIT6	180	Cortex-M4	2048	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439AIH6	180	Cortex-M4	2048	256	0	UFBGA169	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439ZGT6	180	Cortex-M4	1024	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439ZIT6	180	Cortex-M4	2048	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439ZIY6	180	Cortex-M4	2048	256	0	WLCSP143	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439IGT6	180	Cortex-M4	1024	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439IGH6	180	Cortex-M4	1024	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1

STM32 F4系列 – Arm® Cortex®-M4高性能MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F439IIT6	180	Cortex-M4	2048	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439IIH6	180	Cortex-M4	2048	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439BGT6	180	Cortex-M4	1024	256	0	LQFP208	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439BIT6	180	Cortex-M4	2048	256	0	LQFP208	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439NGH6	180	Cortex-M4	1024	256	0	TFBGA216	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439NIH6	180	Cortex-M4	2048	256	0	TFBGA216	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F469 / 479高级型 - 180 MHz																																																						
STM32F469VET6	180	Cortex-M4	1024	384	0	LQFP00	71	1.7-3.6	12	2	2	0	0	3	14	0	0	2	0	0	0	0	4	1 [Quad]	[2]	3	4+3	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469VGT6	180	Cortex-M4	1024	384	0	LQFP00	71	1.7-3.6	12	2	2	0	0	3	14	0	0	2	0	0	0	0	4	1 [Quad]	[2]	3	4+3	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469VIT6	180	Cortex-M4	1024	384	0	LQFP00	71	1.7-3.6	12	2	2	0	0	3	14	0	0	2	0	0	0	0	4	1 [Quad]	[2]	3	4+3	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469ZET6	180	Cortex-M4	1024	384	0	LQFP144	106	1.7-3.6	12	2	2	0	0	3	20	0	0	2	0	0	0	0	4	1 [Quad]	[2]	3	4+3	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469ZGT6	180	Cortex-M4	1024	384	0	LQFP144	106	1.7-3.6	12	2	2	0	0	3	20	0	0	2	0	0	0	0	4	1 [Quad]	[2]	3	4+3	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469ZIT6	180	Cortex-M4	1024	384	0	LQFP144	106	1.7-3.6	12	2	2	0	0	3	20	0	0	2	0	0	0	0	4	1 [Quad]	[2]	3	4+3	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469AEH6	180	Cortex-M4	512	384	0	UFBGA169	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469AGH6	180	Cortex-M4	1024	384	0	UFBGA169	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469AIH6	180	Cortex-M4	2048	384	0	UFBGA169	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469AEY6	180	Cortex-M4	512	384	0	WLCSP168	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469AGY6	180	Cortex-M4	1024	384	0	WLCSP168	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0

STM32 F4系列 – Arm® Cortex®-M4高性能MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F469AIY6	180	Cortex-M4	2048	384	0	WLCSP168	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469IET6	180	Cortex-M4	512	384	0	LQFP176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469IEH6	180	Cortex-M4	512	384	0	UFBGA176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469IGT6	180	Cortex-M4	1024	384	0	LQFP176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469IGH6	180	Cortex-M4	1024	384	0	UFBGA176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469IIT6	180	Cortex-M4	2048	384	0	LQFP176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469IIH6	180	Cortex-M4	2048	384	0	UFBGA176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469BET6	180	Cortex-M4	512	384	0	LQFP208	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469BGT6	180	Cortex-M4	1024	384	0	LQFP208	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469BIT6	180	Cortex-M4	2048	384	0	LQFP208	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469NEH6	180	Cortex-M4	512	384	0	TFBGA216	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469NGH6	180	Cortex-M4	1024	384	0	TFBGA216	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469NIH6	180	Cortex-M4	2048	384	0	TFBGA216	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F479AGH6	180	Cortex-M4	1024	384	0	UFBGA169	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1
STM32F479AIH6	180	Cortex-M4	2048	384	0	UFBGA169	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1
STM32F479AGY6	180	Cortex-M4	1024	384	0	WLCSP168	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1
STM32F479AIY6	180	Cortex-M4	2048	384	0	WLCSP168	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1
STM32F479IGT6	180	Cortex-M4	1024	384	0	LQFP176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1

STM32 F4系列 – Arm® Cortex®-M4高性能MCU(附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F479IGH6	180	Cortex-M4	1024	384	0	UFBGA176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1
STM32F479IIT6	180	Cortex-M4	2048	384	0	LQFP176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1
STM32F479IIH6	180	Cortex-M4	2048	384	0	UFBGA176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1
STM32F479BGT6	180	Cortex-M4	1024	384	0	LQFP208	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1
STM32F479BIT6	180	Cortex-M4	2048	384	0	LQFP208	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1
STM32F479NGH6	180	Cortex-M4	1024	384	0	TFBGA216	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1
STM32F479NIH6	180	Cortex-M4	2048	384	0	TFBGA216	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F7x0超值型 - 216 MHz																																																						
STM32F730R8T6	216	Cortex-M7	64	256+16	0	LQFP64	50	1.7-3.6	11	2	2	0	0	3	16	0	0	2	0	0	0	0	3	1 [QUAD]	[3]	3	4+2	0	1	1	0	0	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F730V8T6	216	Cortex-M7	64	256+16	0	LQFP100	82	1.7-3.6	11	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F730Z8T6	216	Cortex-M7	64	256+16	0	LQFP144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F730I8K6	216	Cortex-M7	64	256+16	0	UFBGA176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F750V8T6	216	Cortex-M7	64	320+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	1	1
STM32F750Z8T6	216	Cortex-M7	64	320+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	1	1
STM32F750N8H6	216	Cortex-M7	64	320+16	0	TFBGA216	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	1	1
STM32F7x2基础型 - 216 MHz																																																						
STM32F722RCT6	216	Cortex-M7	256	256+16	0	LQFP64	50	1.7-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	1 [QUAD]	?3?	3	4+2	0	1	1	0	0	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F722RET6	216	Cortex-M7	512	256+16	0	LQFP64	50	1.7-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	1 [QUAD]	?3?	3	4+2	0	1	1	0	0	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F722VCT6	216	Cortex-M7	256	256+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	?3?	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F722VET6	216	Cortex-M7	512	256+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	?3?	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F722ZCT6	216	Cortex-M7	256	256+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	?3?	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F722ZET6	216	Cortex-M7	512	256+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	?3?	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F722IET6	216	Cortex-M7	512	256+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	?3?	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F722IEK6	216	Cortex-M7	512	256+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	?3?	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F732RET6	216	Cortex-M7	512	256+16	0	LQFP64	50	1.7-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	1 [QUAD]	?3?	3	4+2	0	1	1	0	0	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0
STM32F732VET6	216	Cortex-M7	512	256+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	?3?	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F732ZET6	216	Cortex-M7	512	256 +16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	?3?	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0
STM32F732IET6	216	Cortex-M7	512	256 +16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	?3?	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0
STM32F732IEK6	216	Cortex-M7	512	256 +16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	?3?	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0
STM32F7x3基础型 - 216 MHz																																																					
STM32F723VEY6	216	Cortex-M7	512	256 +16	0	WLCSP100	79	1.7-3.6	11	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F723ZCT6	216	Cortex-M7	256	256 +16	0	LQFP144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F723ZCI6	216	Cortex-M7	256	256 +16	0	UFBGA144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F723ZET6	216	Cortex-M7	512	256 +16	0	LQFP144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F723ZEI6	216	Cortex-M7	512	256 +16	0	UFBGA144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F723ICT6	216	Cortex-M7	256	256 +16	0	LQFP176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F723ICK6	216	Cortex-M7	256	256 +16	0	UFBGA176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F723IET6	216	Cortex-M7	512	256 +16	0	LQFP176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F723IEK6	216	Cortex-M7	512	256 +16	0	UFBGA176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F733VEY6	216	Cortex-M7	512	256 +16	0	WLCSP100	79	1.7-3.6	11	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0
STM32F733ZET6	216	Cortex-M7	512	256 +16	0	LQFP144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0
STM32F733ZEI6	216	Cortex-M7	512	256 +16	0	UFBGA144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0
STM32F733IET6	216	Cortex-M7	512	256 +16	0	LQFP176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0
STM32F733IEK6	216	Cortex-M7	512	256 +16	0	UFBGA176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F7x5高级型 - 216 MHz																																																						
STM32F745VET6	216	Cortex-M7	512	320+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	0	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F745VGT6	216	Cortex-M7	1024	320+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	0	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F745VEH6	216	Cortex-M7	512	320+16	0	TFBGA100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	0	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F745VGH6	216	Cortex-M7	1024	320+16	0	TFBGA100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	0	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F745ZET6	216	Cortex-M7	512	320+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	0	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F745ZGT6	216	Cortex-M7	1024	320+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	0	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F745ZEY6	216	Cortex-M7	512	320+16	0	WLCSP143	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	0	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F745ZGY6	216	Cortex-M7	1024	320+16	0	WLCSP143	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	0	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F745IET6	216	Cortex-M7	512	320+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	0	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F745IGT6	216	Cortex-M7	1024	320+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	0	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F745IEK6	216	Cortex-M7	512	320+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	0	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F745IGK6	216	Cortex-M7	1024	320+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	0	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F765VGT6	216	Cortex-M7	1024	512+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	1	0	0	0	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F765VIT6	216	Cortex-M7	2048	512+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	1	0	0	0	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F765ZGT6	216	Cortex-M7	1024	512+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	1	0	0	0	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F765ZIT6	216	Cortex-M7	2048	512+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	1	0	0	0	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F765IGT6	216	Cortex-M7	1024	512+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	1	0	0	0	0	0	2	4	1	1	0	0	1	0	0	0	0

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F765IIT6	216	Cortex-M7	2048	512 +16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	1	0	0	0	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F765IGK6	216	Cortex-M7	1024	512 +16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	1	0	0	0	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F765IIK6	216	Cortex-M7	2048	512 +16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	1	0	0	0	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F765BGT6	216	Cortex M7	1024	512 +16	0	LQFP208	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	1	0	0	0	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F765BIT6	216	Cortex-M7	2048	512 +16	0	LQFP208	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	1	0	0	0	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F765NGH6	216	Cortex-M7	1024	512 +16	0	TFBGA216	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	1	0	0	0	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F765NIH6	216	Cortex-M7	2048	512 +16	0	TFBGA216	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	1	0	0	0	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F7x6高级型 - 216 MHz																																																						
STM32F746VET6	216	Cortex-M7	512	320 +16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746VGT6	216	Cortex-M7	1024	320 +16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746VEH6	216	Cortex-M7	512	320 +16	0	TFBGA100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746VGH6	216	Cortex -M7	1024	320 +16	0	TFBGA100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746ZET6	216	Cortex-M7	512	320 +16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746ZGT6	216	Cortex-M7	1024	320 +16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746ZEY6	216	Cortex-M7	512	320 +16	0	WLCSP143	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746ZGY6	216	Cortex-M7	1024	320 +16	0	WLCSP143	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746IET6	216	Cortex-M7	512	320 +16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746IGT6	216	Cortex-M7	1024	320 +16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F746IEK6	216	Cortex-M7	512	320+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746IGK6	216	Cortex-M7	1024	320+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746BET6	216	Cortex-M7	512	320+16	0	LQFP208	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746BGT6	216	Cortex-M7	1024	320+16	0	LQFP208	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746NEH6	216	Cortex-M7	512	320+16	0	TFBGA216	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F746NGH6	216	Cortex-M7	1024	320+16	0	TFBGA216	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	0	0
STM32F756VGT6	216	Cortex-M7	1024	320+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	1	1
STM32F756ZGT6	216	Cortex-M7	1024	320+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	1	1
STM32F756ZGY6	216	Cortex-M7	1024	320+16	0	WLCSP143	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	1	1
STM32F756IGT6	216	Cortex-M7	1024	320+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	1	1
STM32F756IGK6	216	Cortex-M7	1024	320+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	1	1
STM32F756BGT6	216	Cortex-M7	1024	320+16	0	LQFP208	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	1	1
STM32F756NGH6	216	Cortex-M7	1024	320+16	0	TFBGA216	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	4	0	1	0	0	1	0	0	1	1
STM32F7x7高级型 - 216 MHz																																																						
STM32F767VGT6	216	Cortex-M7	1024	512+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F767VIT6	216	Cortex-M7	2048	512+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F767ZGT6	216	Cortex-M7	1024	512+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F767ZIT6	216	Cortex-M7	2048	512+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	0	0

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F767IGT6	216	Cortex-M7	1024	512 +16	0	LQFP176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F767IIT6	216	Cortex-M7	2048	512 +16	0	LQFP176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F767IGK6	216	Cortex-M7	1024	512 +16	0	UFBGA176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F767IIK6	216	Cortex-M7	2048	512 +16	0	UFBGA176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F767BGT6	216	Cortex-M7	1024	512 +16	0	LQFP208	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F767BIT6	216	Cortex-M7	2048	512 +16	0	LQFP208	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F767NGH6	216	Cortex-M7	1024	512 +16	0	TFBGA216	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F767NIH6	216	Cortex-M7	2048	512 +16	0	TFBGA216	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	0	0
STM32F777VIT6	216	Cortex-M7	2048	512 +16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	1	1
STM32F777ZIT6	216	Cortex-M7	2048	512 +16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	1	1
STM32F777IIT6	216	Cortex-M7	2048	512 +16	0	LQFP176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	1	1
STM32F777IIK6	216	Cortex-M7	2048	512 +16	0	UFBGA176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	1	1
STM32F777BIT6	216	Cortex-M7	2048	512 +16	0	LQFP208	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	1	1
STM32F777NIH6	216	Cortex-M7	2048	512 +16	0	TFBGA216	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	4	1	1	0	0	1	0	0	1	1
STM32F7x9高级型 - 216 MHz																																																						
STM32F769AGY6	216	Cortex-M7	1024	512 +16	0	WLCSP180	129	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	0	1	0	1	1	0	1	2	4	1	1	0	0	1	0	0	0	0
STM32F769AIY6	216	Cortex-M7	2048	512 +16	0	WLCSP180	129	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	0	1	0	1	1	0	1	2	4	1	1	0	0	1	0	0	0	0
STM32F769BGT6	216	Cortex-M7	1024	512 +16	0	LQFP208	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	1	2	4	1	1	0	0	1	0	0	0	0

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32F769BIT6	216	Cortex-M7	2048	512+16	0	LQFP208	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	1	2	4	1	1	0	0	1	0	0	0	0
STM32F769IGT6	216	Cortex-M7	1024	512+16	0	LQFP176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	1	2	4	1	1	0	0	1	0	0	0	0
STM32F769IIT6	216	Cortex-M7	2048	512+16	0	LQFP176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	1	2	4	1	1	0	0	1	0	0	0	0
STM32F769NGH6	216	Cortex-M7	1024	512+16	0	TFBGA216	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	1	2	4	1	1	0	0	1	0	0	0	0
STM32F769NIH6	216	Cortex-M7	2048	512+16	0	TFBGA216	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	1	2	4	1	1	0	0	1	0	0	0	0
STM32F779AIY6	216	Cortex-M7	2048	512+16	0	WLCSP180	129	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	0	1	0	1	1	0	1	2	4	1	1	0	0	1	0	0	1	1
STM32F779BIT6	216	Cortex-M7	2048	512+16	0	LQFP208	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	1	2	4	1	1	0	0	1	0	0	1	1
STM32F779IIT6	216	Cortex-M7	2048	512+16	0	LQFP176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	1	2	4	1	1	0	0	1	0	0	1	1
STM32F779NIH6	216	Cortex-M7	2048	512+16	0	TFBGA216	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	1	2	4	1	1	0	0	1	0	0	1	1

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32H750超值型 - 480MHz																																																						
STM32H750VBT6	480	Cortex-M7	128	1056	0	LQFP100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [Quad]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	1	1
STM32H750IBK6	480	Cortex-M7	128	1056	0	UFBGA176 +25	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [Quad]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	1	1
STM32H750XBH6	480	Cortex-M7	128	1056	0	TFBGA240 +25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [Quad]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	1	1
STM32H742 Arm Cortex-M7 - 480MHz																																																						
STM32H742VGT6	480	Cortex-M7	1024	1056	0	LQFP100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742VGH6	480	Cortex-M7	1024	1056	0	TFBGA100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742ZGT6	480	Cortex-M7	1024	1056	0	LQFP144	114	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742AGI6	480	Cortex-M7	1024	1056	0	UFBGA169	131	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742IGT6	480	Cortex-M7	1024	1056	0	LQFP176	140	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742IGI6	480	Cortex-M7	1024	1056	0	UFBGA176 +25	140	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742BGT6	480	Cortex-M7	1024	1056	0	LQFP208	168	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742XGH6	480	Cortex-M7	1024	1056	0	TFBGA240 +25	168	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742VIT6	480	Cortex-M7	2048	1056	0	LQFP100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32H742VIH6	480	Cortex-M7	2048	1056	0	TFBGA100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742ZIT6	480	Cortex-M7	2048	1056	0	LQFP144	114	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742AII6	480	Cortex-M7	2048	1056	0	UFBGA169	131	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742IIT6	480	Cortex-M7	2048	1056	0	LQFP176	140	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742III6	480	Cortex-M7	2048	1056	0	UFBGA176 +25	140	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742BIT6	480	Cortex-M7	2048	1056	0	LQFP208	168	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H742XIH6	480	Cortex-M7	2048	1056	0	TFBGA240 +25	168	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	0	0	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743/753基础型 - 480MHz																																																						
STM32H743VGT6	480	Cortex-M7	1024	1056	0	LQFP100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743VGH6	480	Cortex-M7	1024	1056	0	TFBGA100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743ZGT6	480	Cortex-M7	1024	1056	0	LQFP144	114	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743AGI6	480	Cortex-M7	1024	1056	0	UFBGA169	131	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743IGT6	480	Cortex-M7	1024	1056	0	LQFP176	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743IGI6	480	Cortex-M7	1024	1056	0	UFBGA176 +25	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32H743BGT6	480	Cortex-M7	1024	1056	0	LQFP208	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743XGH6	480	Cortex-M7	1024	1056	0	TFBGA240 +25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743VIT6	480	Cortex-M7	2048	1056	0	LQFP100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743VIH6	480	Cortex-M7	2048	1056	0	TFBGA100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743ZIT6	480	Cortex-M7	2048	1056	0	LQFP144	114	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743AII6	480	Cortex-M7	2048	1056	0	UFBGA169	131	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743IIT6	480	Cortex-M7	2048	1056	0	LQFP176	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743III6	480	Cortex-M7	2048	1056	0	UFBGA176 +25	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743BIT6	480	Cortex-M7	2048	1056	0	LQFP208	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H743XIH6	480	Cortex-M7	2048	1056	0	TFBGA240 +25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H753VIT6	480	Cortex-M7	2048	1056	0	LQFP100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1
STM32H753VIH6	480	Cortex-M7	2048	1056	0	TFBGA100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1
STM32H753ZIT6	480	Cortex-M7	2048	1056	0	LQFP144	114	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32H753AII6	480	Cortex-M7	2048	1056	0	UFBGA169	131	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1
STM32H753IIT6	480	Cortex-M7	2048	1056	0	LQFP176	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1
STM32H753III6	480	Cortex-M7	2048	1056	0	UFBGA176 +25	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1
STM32H753BIT6	480	Cortex-M7	2048	1056	0	LQFP208	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1
STM32H753XIH6	480	Cortex-M7	2048	1056	0	TFBGA240 +25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1
STM32H745/755双核Arm Cortex-M7 + Cortex M4 - 480MHz+240MHz																																																					
STM32H745ZGT6	480	M7 & M4	1024	1056	0	LQFP144	97	1.62-3.6	12	2	2	5	1	0	0	3	23	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H745IGT6	480	M7 & M4	1024	1056	0	LQFP176	119	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H745IGK6	480	M7 & M4	1024	1056	0	UFBGA176 +25	128	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H745BGT6	480	M7 & M4	1024	1056	0	LQFP208	148	1.62-3.6	12	2	2	5	1	0	0	3	32	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H745XGH6	480	M7 & M4	1024	1056	0	TFBGA240 +25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H745ZIT6	480	M7 & M4	2048	1056	0	LQFP144	97	1.62-3.6	12	2	2	5	1	0	0	3	23	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H745IIT6	480	M7 & M4	2048	1056	0	LQFP176	119	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H745IIK6	480	M7 & M4	2048	1056	0	UFBGA176 +25	128	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32H745BIT6	480	M7 & M4	2048	1056	0	LQFP208	148	1.62-3.6	12	2	2	5	1	0	0	3	32	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H745XIH6	480	M7 & M4	2048	1056	0	TFBGA240 +25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	0	0
STM32H755ZIT6	480	M7 & M4	2048	1056	0	LQFP144	97	1.62-3.6	12	2	2	5	1	0	0	3	23	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1
STM32H755IIT6	480	M7 & M4	2048	1056	0	LQFP176	119	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1
STM32H755IIK6	480	M7 & M4	2048	1056	0	UFBGA176 +25	128	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1
STM32H755BIT6	480	M7 & M4	2048	1056	0	LQFP208	148	1.62-3.6	12	2	2	5	1	0	0	3	32	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1
STM32H755XIH6	480	M7 & M4	2048	1056	0	TFBGA240 +25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	4	1	1	1	0	1	0	0	1	1
STM32H747/757双核Arm Cortex-M7 + Cortex M4 - 480MHz+240MHz																																																					
STM32H747AGI6	480 & 240	M7 & M4	1024	1056	0	UFBGA169	112	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	0	0
STM32H747IGT6	480 & 240	M7 & M4	1024	1056	0	LQFP176	119	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	0	0
STM32H747BGT6	480 & 240	M7 & M4	1024	1056	0	LQFP208	148	1.62-3.6	12	2	2	5	1	0	0	3	32	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	0	0
STM32H747XGH6	480 & 240	M7 & M4	1024	1056	0	TFBGA240 +25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	0	0
STM32H747ZIY6	480 & 240	M7 & M4	2048	1056	0	WLCSP156	99	1.62-3.6	12	2	2	5	1	0	0	3	23	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	0	0
STM32H747AII6	480 & 240	M7 & M4	2048	1056	0	UFBGA169	112	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	0	0

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32H747IIT6	480 & 240	M7 & M4	2048	1056	0	LQFP176	119	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	0	0
STM32H747BIT6	480 & 240	M7 & M4	2048	1056	0	LQFP208	148	1.62-3.6	12	2	2	5	1	0	0	3	32	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	0	0
STM32H747XIH6	480 & 240	M7 & M4	2048	1056	0	TFBGA240 +25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	0	0
STM32H757ZIY6	480 & 240	M7 & M4	2048	1056	0	WLCSP156	99	1.62-3.6	12	2	2	5	1	0	0	3	23	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	1	1
STM32H757AII6	480 & 240	M7 & M4	2048	1056	0	UFBGA169	112	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	1	1
STM32H757IIT6	480 & 240	M7 & M4	2048	1056	0	LQFP176	119	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	1	1
STM32H757BIT6	480 & 240	M7 & M4	2048	1056	0	LQFP208	148	1.62-3.6	12	2	2	5	1	0	0	3	32	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	1	1
STM32H757XIH6	480 & 240	M7 & M4	2048	1056	0	TFBGA240 +25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	4	1	1	1	0	1	0	0	1	1
STM32H7A3双核Arm Cortex-M7 + Cortex M4 - 480MHz+240MHz																																																					
STM32H7A3LIH6(*)	280	Cortex-M7	2048	1380	0	TFBGA225	168	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3LGH6(*)	280	Cortex-M7	1024	1380	0	TFBGA225	168	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3IIK6(*)	280	Cortex-M7	2048	1380	0	UFBGA176 +25	128	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3IGK6(*)	280	Cortex-M7	1024	1380	0	UFBGA176 +25	128	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0

注： (*) 为内置SMPS封装可选

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32H7A3IIT6(*)	280	Cortex-M7	2048	1380	0	LQFP176	119	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3IGT6(*)	280	Cortex-M7	1024	1380	0	LQFP176	119	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3AII6(*)	280	Cortex-M7	2048	1380	0	UFBGA169	121	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3AGI6(*)	280	Cortex-M7	1024	1380	0	UFBGA169	121	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3ZIT6(*)	280	Cortex-M7	2048	1380	0	LQFP144	97	1.62-3.6	12	2	2	3	0	0	0	2	18	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3ZGT6(*)	280	Cortex-M7	1024	1380	0	LQFP144	97	1.62-3.6	12	2	2	3	0	0	0	2	18	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3QIY6(*)	280	Cortex-M7	2048	1380	0	WLCSP132	87	1.62-3.6	12	2	2	3	0	0	0	2	17	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3VIH6(*)	280	Cortex-M7	2048	1380	0	TFBGA100	75	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3VGH6(*)	280	Cortex-M7	1024	1380	0	TFBGA100	75	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3VIT6(*)	280	Cortex-M7	2048	1380	0	LQFP100	68	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	4+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	0	2	1	1	0	1	0	0	0	0
STM32H7A3VGT6(*)	280	Cortex-M7	1024	1380	0	LQFP100	68	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	4+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	0	2	1	1	0	1	0	0	0	0
STM32H7A3NIH6	280	Cortex-M7	2048	1380	0	TFBGA216	166	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3NGH6	280	Cortex-M7	1024	1380	0	TFBGA216	166	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0

注： (*) 为内置SMPS封装可选

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32H7A3IIK6	280	Cortex-M7	2048	1380	0	UFBGA176+25	138	1.62-3.6	12	2	2	3	0	0	0	2	20	2	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3IGK6	280	Cortex-M7	1024	1380	0	UFBGA176+25	138	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3IIT6	280	Cortex-M7	2048	1380	0	LQFP176	138	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3IGT6	280	Cortex-M7	1024	1380	0	LQFP176	138	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3ZIT6	280	Cortex-M7	2048	1380	0	LQFP144	112	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3ZGT6	280	Cortex-M7	1024	1380	0	LQFP144	112	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3VIH6	280	Cortex-M7	2048	1380	0	TFBGA100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3VGH6	280	Cortex-M7	1024	1380	0	TFBGA100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3VIT6	280	Cortex-M7	2048	1380	0	LQFP100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3VGT6	280	Cortex-M7	1024	1380	0	LQFP100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7A3RIT6	280	Cortex-M7	2048	1380	0	LQFP64	49	1.62-3.6	12	2	2	3	0	0	0	2	16	3	1	1	0	0	4	2 [Octo]	[4]	3	4+3	1	1FD + 1TT	2	0	0	0	[1]	1	0	1	0	1	1	0	0	1	4	2	1	1	0	1	0	0	0	0
STM32H7A3RGT6	280	Cortex-M7	1024	1380	0	LQFP64	49	1.62-3.6	12	2	2	3	0	0	0	2	16	3	1	1	0	0	4	2 [Octo]	[4]	3	4+3	1	1FD + 1TT	2	0	0	0	[1]	1	0	1	0	1	1	0	0	1	4	2	1	1	0	1	0	0	0	0

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32H7B3双核Arm Cortex-M7 + Cortex M4 - 480MHz+240MHz																																																					
STM32H7B0IBK6(*)	280	Cortex-M7	128	1380	0	UFBGA176+25	128	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7B0ABI6(*)	280	Cortex-M7	128	1380	0	UFBGA169	121	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7B0IBT6	280	Cortex-M7	128	1380	0	LQFP176	138	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7B0ZBT6	280	Cortex-M7	128	1380	0	LQFP144	112	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7B0VBT6	280	Cortex-M7	128	1380	0	LQFP100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	0	0
STM32H7B0RBT6	280	Cortex-M7	128	1380	0	LQFP64	49	1.62-3.6	12	2	2	3	0	0	0	2	16	3	1	1	0	0	4	2 [Octo]	[4]	3	4+3	1	1FD + 1TT	2	0	0	0	[1]	1	0	1	0	1	1	0	0	1	4	2	1	1	0	1	0	0	0	0
STM32H7B3LIH6(*)	280	Cortex-M7	2048	1380	0	TFBGA225	168	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1
STM32H7B3IIK6(*)	280	Cortex-M7	2048	1380	0	UFBGA176+25	128	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1
STM32H7B3IIT6(*)	280	Cortex-M7	2048	1380	0	LQFP176	119	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1
STM32H7B3AII6(*)	280	Cortex-M7	2048	1380	0	UFBGA169	121	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1
STM32H7B3ZIT6(*)	280	Cortex-M7	2048	1380	0	LQFP144	97	1.62-3.6	12	2	2	3	0	0	0	2	18	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1
STM32H7B3QIY6(*)	280	Cortex-M7	2048	1380	0	WLCSP132	87	1.62-3.6	12	2	2	3	0	0	0	2	17	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1

注： (*) 为内置SMPS封装可选

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32H7B3VIH6(*)	280	Cortex-M7	2048	1380	0	TFBGA100	75	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1
STM32H7B3VIT6(*)	280	Cortex-M7	2048	1380	0	LQFP100	68	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	4+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	0	2	1	1	0	1	0	0	1	1
STM32H7B3NIH6	280	Cortex-M7	2048	1380	0	TFBGA216	166	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1
STM32H7B3IIK6	280	Cortex-M7	2048	1380	0	UFBGA176 +25	138	1.62-3.6	12	2	2	3	0	0	0	2	20	2	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1
STM32H7B3IIT6	280	Cortex-M7	2048	1380	0	LQFP176	138	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1
STM32H7B3ZIT6	280	Cortex-M7	2048	1380	0	LQFP144	112	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1
STM32H7B3VIH6	280	Cortex-M7	2048	1380	0	TFBGA100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1
STM32H7B3VIT6	280	Cortex-M7	2048	1380	0	LQFP100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	4	2	1	1	0	1	0	0	1	1
STM32H7B3RIT6	280	Cortex-M7	2048	1380	0	LQFP64	49	1.62-3.6	12	2	2	3	0	0	0	2	16	3	1	1	0	0	4	2 [Octo]	[4]	3	4+3	1	1FD + 1TT	2	0	0	0	[1]	1	0	1	0	1	1	0	0	1	4	2	1	1	0	1	0	0	1	1

注：(*) 为内置SMPS封装可选

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L0x0超值型 - 32 MHz																																																							
STM32L010F4P6	32	Cortex-M0+	16	2	128	TSSOP20	16	1.8-3.6	2	0	0	1	0	1	7	0	0	0	0	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L010K4T6	32	Cortex-M0+	16	2	128	LQFP32	26	1.8-3.6	2	0	0	1	0	1	10	0	0	0	0	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L010K8T6	32	Cortex-M0+	64	8	256	LQFP32	25	1.8-3.6	2	0	0	1	0	1	10	0	0	0	0	0	0	0	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L010R8T6	32	Cortex-M0+	64	8	256	LQFP64	51	1.8-3.6	2	0	0	1	0	1	16	0	0	0	0	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L010C6T6	32	Cortex-M0+	32	8	256	LQFP48	38	1.8-3.6	2	0	0	1	0	1	10	0	0	0	0	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L010R8T6	32	Cortex-M0+	64	8	256	LQFP64	51	1.8-3.6	2	0	0	1	0	1	16	0	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L010RBT6	32	Cortex-M0+	128	20	512	LQFP64	51	1.8-3.6	3	0	0	1	0	1	16	0	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L0x1入门型 - 32 MHz																																																							
STM32L011D3P6	32	Cortex-M0+	8	2	512	TSSOP14	11	1.65-3.6	2	0	0	1	0	1	4	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L011D4P6	32	Cortex-M0+	16	2	512	TSSOP14	11	1.65-3.6	2	0	0	1	0	1	4	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L011F3P6	32	Cortex-M0+	8	2	512	TSSOP20	16	1.65-3.6	2	0	0	1	0	1	9	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L011F3U6	32	Cortex-M0+	8	2	512	UFQFPN20	16	1.65-3.6	2	0	0	1	0	1	7	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L011F4P6	32	Cortex-M0+	16	2	512	TSSOP20	16	1.65-3.6	2	0	0	1	0	1	9	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L011F4U6	32	Cortex-M0+	16	2	512	UFQFPN20	16	1.65-3.6	2	0	0	1	0	1	7	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L011E3Y6	32	Cortex-M0+	8	2	512	WLCSP25	21	1.65-3.6	2	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L011E4Y6	32	Cortex-M0+	16	2	512	WLCSP25	21	1.65-3.6	2	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L011G3U6	32	Cortex-M0+	8	2	512	UFQFPN28	24	1.65-3.6	2	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L011G4U6	32	Cortex-M0+	16	2	512	UFQFPN28	24	1.65-3.6	2	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L011K3T6	32	Cortex-M0+	8	2	512	LQFP32	26	1.65-3.6	2	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L011K3U6	32	Cortex-M0+	8	2	512	UFQFPN32	28	1.65-3.6	2	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L011K4T6	32	Cortex-M0+	16	2	512	LQFP32	26	1.65-3.6	2	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L011K4U6	32	Cortex-M0+	16	2	512	UFQFPN32	28	1.65-3.6	2	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L021D4P6	32	Cortex-M0+	16	2	512	TSSOP14	11	1.65-3.6	2	0	0	1	0	1	4	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L021F4P6	32	Cortex-M0+	16	2	512	TSSOP20	16	1.65-3.6	2	0	0	1	0	1	9	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L021F4U6	32	Cortex-M0+	16	2	512	UFQFPN20	16	1.65-3.6	2	0	0	1	0	1	7	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L021G4U6	32	Cortex-M0+	16	2	512	UFQFPN28	24	1.65-3.6	2	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L021K4T6	32	Cortex-M0+	16	2	512	LQFP32	26	1.65-3.6	2	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L021K4U6	32	Cortex-M0+	16	2	512	UFQFPN32	28	1.65-3.6	2	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L031F4P6	32	Cortex-M0+	16	8	1024	TSSOP20	15	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L031F6P6	32	Cortex-M0+	32	8	1024	TSSOP20	15	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L031E4Y6	32	Cortex-M0+	16	8	1024	WLCSP25	20	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L031E6Y6	32	Cortex-M0+	32	8	1024	WLCSP25	20	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L031G4U6	32	Cortex-M0+	16	8	1024	UFQFPN28	21	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L031G6U6	32	Cortex-M0+	32	8	1024	UFQFPN28	21	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L031K4T6	32	Cortex-M0+	16	8	1024	LQFP32	25	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L031K4U6	32	Cortex-M0+	16	8	1024	UFQFPN32	27	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L031K6T6	32	Cortex-M0+	32	8	1024	LQFP32	25	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L031K6U6	32	Cortex-M0+	32	8	1024	UFQFPN32	27	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L031C6T6	32	Cortex-M0+	32	8	1024	LQFP48	38	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L041E6Y6	32	Cortex-M0+	32	8	1024	WLCSP25	20	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L041F6P6	32	Cortex-M0+	32	8	1024	TSSOP20	15	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L041G6U6	32	Cortex-M0+	32	8	1024	UFQFPN28	21	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L041K6T6	32	Cortex-M0+	32	8	1024	LQFP32	25	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L041K6U6	32	Cortex-M0+	32	8	1024	UFQFPN32	27	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L041C6T6	32	Cortex-M0+	32	8	1024	LQFP48	38	1.65-3.6	3	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L051K6T6	32	Cortex-M0+	32	8	2048	LQFP32	25	1.65-3.6	4	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L051K6U6	32	Cortex-M0+	32	8	2048	UFQFPN32	27	1.65-3.6	4	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L051K8T6	32	Cortex-M0+	64	8	2048	LQFP32	25	1.65-3.6	4	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L051K8U6	32	Cortex-M0+	64	8	2048	UFQFPN32	27	1.65-3.6	4	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L051T6Y6	32	Cortex-M0+	32	8	2048	WLCSP36	29	1.65-3.6	4	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L051T8Y6	32	Cortex-M0+	64	8	2048	WLCSP36	29	1.65-3.6	4	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L051C6T6	32	Cortex-M0+	32	8	2048	LQFP48	37	1.65-3.6	4	0	0	1	0	1	10	0	0	0	2	0	0	0	2	0	1	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L051C8T6	32	Cortex-M0+	64	8	2048	LQFP48	37	1.65-3.6	4	0	0	1	0	1	10	0	0	0	2	0	0	0	2	0	1	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L051R6T6	32	Cortex-M0+	32	8	2048	LQFP64	51	1.65-3.6	4	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L051R6H6	32	Cortex-M0+	32	8	2048	TFBGA64	51	1.65-3.6	4	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L051R8T6	32	Cortex-M0+	64	8	2048	LQFP64	51	1.65-3.6	4	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L051R8H6	32	Cortex-M0+	64	8	2048	TFBGA64	51	1.65-3.6	4	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071K8U6	32	Cortex-M0+	64	20	3072	UFQFPN32	23	1.65-3.6	6	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	2	3	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071KBT6	32	Cortex-M0+	128	20	6144	LQFP32	25	1.65-3.6	6	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	2	3	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071KBU6	32	Cortex-M0+	128	20	6144	UFQFPN32	23	1.65-3.6	6	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	2	3	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071KZT6	32	Cortex-M0+	192	20	6144	LQFP32	25	1.65-3.6	6	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	2	3	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071KZU6	32	Cortex-M0+	192	20	6144	UFQFPN32	23	1.65-3.6	6	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	2	3	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071C8T6	32	Cortex-M0+	64	20	3072	LQFP48	37	1.65-3.6	6	0	0	1	0	1	13	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071CBT6	32	Cortex-M0+	128	20	6144	LQFP48	37	1.65-3.6	6	0	0	1	0	1	13	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071CBY6	32	Cortex-M0+	128	20	6144	WLCSP49	40	1.65-3.6	6	0	0	1	0	1	13	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071CZT6	32	Cortex-M0+	192	20	6144	LQFP48	37	1.65-3.6	6	0	0	1	0	1	13	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071CZY6	32	Cortex-M0+	192	20	6144	WLCSP49	40	1.65-3.6	6	0	0	1	0	1	13	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071RBT6	32	Cortex-M0+	128	20	6144	LQFP64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071RBH6	32	Cortex-M0+	128	20	6144	TFBGA64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071RZT6	32	Cortex-M0+	192	20	6144	LQFP64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071RZH6	32	Cortex-M0+	192	20	6144	TFBGA64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071V8T6	32	Cortex-M0+	64	20	3072	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L071V8I6	32	Cortex-M0+	64	20	3072	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071VBT6	32	Cortex-M0+	128	20	6144	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071VBI6	32	Cortex-M0+	128	20	6144	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071VZT6	32	Cortex-M0+	192	20	6144	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L071VZI6	32	Cortex-M0+	192	20	6144	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L081KZT6	32	Cortex-M0+	192	20	6144	LQFP32	25	1.65-3.6	6	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	2	3	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L081KZU6	32	Cortex-M0+	192	20	6144	UFQFPN32	23	1.65-3.6	6	0	0	1	0	1	10	0	0	0	2	0	0	0	1	0	0	2	3	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L081CBT6	32	Cortex-M0+	128	20	6144	LQFP48	40	1.65-3.6	6	0	0	1	0	1	13	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L081CZT6	32	Cortex-M0+	192	20	6144	LQFP48	40	1.65-3.6	6	0	0	1	0	1	13	0	0	0	2	0	0	0	2	0	1	3	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L0x2 USB型 - 32 MHz																																																						
STM32L052K6T6	32	Cortex-M0+	32	8	2048	LQFP32	25	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	1	0	0	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L052K6U6	32	Cortex-M0+	32	8	2048	UFQFPN32	27	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	1	0	0	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L052K8T6	32	Cortex-M0+	64	8	2048	LQFP32	25	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	1	0	0	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L052K8U6	32	Cortex-M0+	64	8	2048	UFQFPN32	27	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	1	0	0	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L052T6Y6	32	Cortex-M0+	32	8	2048	WLCSP36	29	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	1	0	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L052T8Y6	32	Cortex-M0+	64	8	2048	WLCSP36	29	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	1	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L052C6T6	32	Cortex-M0+	32	8	2048	LQFP48	37	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L052C8T6	32	Cortex-M0+	64	8	2048	LQFP48	37	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L052R6T6	32	Cortex-M0+	32	8	2048	LQFP64	51	1.65-3.6	4	0	0	1	0	1	16	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L052R6H6	32	Cortex-M0+	32	8	2048	TFBGA64	50	1.65-3.6	4	0	0	1	0	1	15	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L052R8T6	32	Cortex-M0+	64	8	2048	LQFP64	51	1.65-3.6	4	0	0	1	0	1	16	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L052R8H6	32	Cortex-M0+	64	8	2048	TFBGA64	50	1.65-3.6	4	0	0	1	0	1	15	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L062K8T6	32	Cortex-M0+	64	8	2048	LQFP32	25	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	1	0	0	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L062K8U6	32	Cortex-M0+	64	8	2048	UFQFPN32	27	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	1	0	0	1	2	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L062T8Y6	32	Cortex-M0+	64	8	2048	WLCSP36	29	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	1	0	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L072KBT6	32	Cortex-M0+	128	20	6144	LQFP32	25	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	1	0	0	2	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072KBU6	32	Cortex-M0+	128	20	6144	UFQFPN32	23	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	1	0	0	2	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072KZT6	32	Cortex-M0+	192	20	6144	LQFP32	25	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	1	0	0	2	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072KZU6	32	Cortex-M0+	192	20	6144	UFQFPN32	23	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	1	0	0	2	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072CBT6	32	Cortex-M0+	128	20	6144	LQFP48	37	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072CBY6	32	Cortex-M0+	128	20	6144	WLCSP49	40	1.65-3.6	6	0	0	1	0	1	13	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072CZT6	32	Cortex-M0+	192	20	6144	LQFP48	37	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072CZY6	32	Cortex-M0+	192	20	6144	WLCSP49	40	1.65-3.6	6	0	0	1	0	1	13	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072RBT6	32	Cortex-M0+	128	20	6144	LQFP64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072RBH6	32	Cortex-M0+	128	20	6144	TFBGA64	50	1.65-3.6	6	0	0	1	0	1	15	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072RZT6	32	Cortex-M0+	192	20	6144	LQFP64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L072RZH6	32	Cortex-M0+	192	20	6144	TFBGA64	50	1.65-3.6	6	0	0	1	0	1	15	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072RZI6	32	Cortex-M0+	192	20	6144	UFBGA64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072V8T6	32	Cortex-M0+	64	20	3072	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072V8I6	32	Cortex-M0+	64	20	3072	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072VBT6	32	Cortex-M0+	128	20	6144	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072VBI6	32	Cortex-M0+	128	20	6144	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072VZT6	32	Cortex-M0+	192	20	6144	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L072VZI6	32	Cortex-M0+	192	20	6144	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L082KBT6	32	Cortex-M0+	128	20	6144	LQFP32	25	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	1	0	0	2	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L082KBU6	32	Cortex-M0+	128	20	6144	UFQFPN32	23	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	1	0	0	2	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L082KZT6	32	Cortex-M0+	192	20	6144	LQFP32	25	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	1	0	0	2	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L082KZU6	32	Cortex-M0+	192	20	6144	UFQFPN32	23	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	1	0	0	2	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L082CZY6	32	Cortex-M0+	192	20	6144	WLCSP49	40	1.65-3.6	6	0	0	1	0	1	13	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L0x3 USB & LCD功能型 - 32 MHz																																																					
STM32L053C6T6	32	Cortex -M0+	32	8	2048	LQFP48	37	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L053C8T6	32	Cortex-M0+	64	8	2048	LQFP48	37	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L053R6T6	32	Cortex-M0+	32	8	2048	LQFP64	51	1.65-3.6	4	0	0	1	0	1	16	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	8x28/ 4x32	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L053R6H6	32	Cortex-M0+	32	8	2048	TFBGA64	50	1.65-3.6	4	0	0	1	0	1	15	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	8x28/ 4x32	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L053R8T6	32	Cortex-M0+	64	8	2048	LQFP64	51	1.65-3.6	4	0	0	1	0	1	16	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L053R8H6	32	Cortex-M0+	64	8	2048	TFBGA64	50	1.65-3.6	4	0	0	1	0	1	15	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L063C8T6	32	Cortex-M0+	64	8	2048	LQFP48	37	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L063R8T6	32	Cortex-M0+	64	8	2048	LQFP64	51	1.65-3.6	4	0	0	1	0	1	16	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L073CBT6	32	Cortex-M0+	128	20	6144	LQFP48	37	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L073CZT6	32	Cortex-M0+	192	20	6144	LQFP48	37	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L073RBT6	32	Cortex-M0+	128	20	6144	LQFP64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L073RBH6	32	Cortex-M0+	128	20	6144	TFBGA64	50	1.65-3.6	6	0	0	1	0	1	15	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L073RZT6	32	Cortex-M0+	192	20	6144	LQFP64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L073RZH6	32	Cortex-M0+	192	20	6144	TFBGA64	50	1.65-3.6	6	0	0	1	0	1	15	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L073V8T6	32	Cortex-M0+	64	20	3072	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L073V8I6	32	Cortex-M0+	64	20	3072	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L073VBT6	32	Cortex-M0+	128	20	6144	LQFP100	84	1.65-3.6	6	0	0	1	0	1	15	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L073VBI6	32	Cortex-M0+	128	20	6144	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L073VZT6	32	Cortex-M0+	192	20	6144	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L073VZI6	32	Cortex-M0+	192	20	6144	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	15	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L083CBT6	32	Cortex-M0+	128	20	6144	LQFP48	40	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L083CZT6	32	Cortex-M0+	192	20	6144	LQFP48	40	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L083RBT6	32	Cortex-M0+	128	20	6144	LQFP64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L083RZT6	32	Cortex-M0+	192	20	6144	LQFP64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L083RZH6	32	Cortex-M0+	192	20	6144	TFBGA64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L083V8T6	32	Cortex-M0+	64	20	3072	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L083V8I6	32	Cortex-M0+	64	20	3072	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L083VBT6	32	Cortex-M0+	128	20	6144	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L083VBI6	32	Cortex-M0+	128	20	6144	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L083VZT6	32	Cortex-M0+	192	20	6144	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L083VZI6	32	Cortex-M0+	192	20	6144	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0

STM32 L1系列 – Arm® Cortex®-M3超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L100超值型 - 32 MHz																																																					
STM32L100C6U6-A	32	Cortex-M3	32	4	2048	UFQFPN48	37	1.8-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x16	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L100R8T6-A	32	Cortex-M3	64	8	2048	LQFP64	51	1.8-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L100RBT6-A	32	Cortex-M3	128	16	2048	LQFP64	51	1.8-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L100RCT6	32	Cortex-M3	256	16	4096	LQFP64	51	1.8-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	3	0	[2]	2	3	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151入门型 - 32 MHz																																																					
STM32L151C6T6-A	32	Cortex-M3	32	16	4096	LQFP48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151C6U6-A	32	Cortex-M3	32	16	4096	UFQFPN48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151C8T6-A	32	Cortex-M3	64	32	4096	LQFP48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151C8U6-A	32	Cortex-M3	64	32	4096	UFQFPN48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151CBT6-A	32	Cortex-M3	128	32	4096	LQFP48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151CBU6-A	32	Cortex-M3	128	32	4096	UFQFPN48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151R6T6-A	32	Cortex-M3	32	16	4096	LQFP64	51	1.65-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151R6H6-A	32	Cortex-M3	32	16	4096	TFBGA64	50	1.65-3.6	8	0	0	0	0	1	19	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151R8T6-A	32	Cortex-M3	64	32	4096	LQFP64	51	1.65-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151R8H6-A	32	Cortex-M3	64	32	4096	TFBGA64	50	1.65-3.6	8	0	0	0	0	1	19	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151RBT6-A	32	Cortex-M3	128	32	4096	LQFP64	51	1.65-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151RBH6-A	32	Cortex-M3	128	32	4096	TFBGA64	50	1.65-3.6	8	0	0	0	0	1	19	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151V8T6-A	32	Cortex-M3	64	32	4096	LQFP100	83	1.65-3.6	8	0	0	0	0	1	24	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 L1系列 – Arm® Cortex®-M3超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L151V8H6-A	32	Cortex-M3	64	32	4096	UFBGA100	83	1.65-3.6	8	0	0	0	0	1	24	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151VBT6-A	32	Cortex-M3	128	32	4096	LQFP100	83	1.65-3.6	8	0	0	0	0	1	24	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151VBH6-A	32	Cortex-M3	128	32	4096	UFBGA100	83	1.65-3.6	8	0	0	0	0	1	24	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151CCT6	32	Cortex-M3	256	32	8192	LQFP48	37	1.65-3.6	8	1	0	0	0	1	14	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151CCU6	32	Cortex-M3	256	32	8192	UFQFPN48	37	1.65-3.6	8	1	0	0	0	1	14	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151UCY6	32	Cortex-M3	256	32	8192	WLCSP63	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151RCT6	32	Cortex-M3	256	32	8192	LQFP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151VCT6	32	Cortex-M3	256	32	8192	LQFP100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151VCH6	32	Cortex-M3	256	32	8192	UFBGA100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151QCH6	32	Cortex-M3	256	32	8192	UFBGA132	109	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151ZCT6	32	Cortex-M3	256	32	8192	LQFP144	115	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151RDT6	32	Cortex-M3	384	48	12288	LQFP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151RDY6	32	Cortex-M3	384	48	12288	WLCSP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151VDT6	32	Cortex-M3	384	48	12288	LQFP100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151QDH6	32	Cortex-M3	384	48	12288	UFBGA132	109	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151ZDT6	32	Cortex-M3	384	48	12288	LQFP144	115	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151VDT6-x	32	Cortex-M3	384	80	16384	LQFP100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151VDY6-x	32	Cortex-M3	384	80	16384	WLCSP104	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 L1系列 – Arm® Cortex®-M3超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L151RET6	32	Cortex-M3	512	80	16384	LQFP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151VET6	32	Cortex-M3	512	80	16384	LQFP100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151VEY6	32	Cortex-M3	512	80	16384	WLCSP104	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151QEH6	32	Cortex-M3	512	80	16384	UFBGA132	109	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151ZET6	32	Cortex-M3	512	80	16384	LQFP144	115	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152USB&LCD功能型 - 32 MHz																																																						
STM32L152C6T6-A	32	Cortex-M3	32	16	4096	LQFP48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x16	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152C6U6-A	32	Cortex-M3	32	16	4096	UFQFPN48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x16	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152C8T6-A	32	Cortex-M3	64	32	4096	LQFP48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x16	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152C8U6-A	32	Cortex-M3	64	32	4096	UFQFPN48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x16	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152CBT6-A	32	Cortex-M3	128	32	4096	LQFP48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x16	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152CBU6-A	32	Cortex-M3	128	32	4096	UFQFPN48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x16	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152R6T6-A	32	Cortex-M3	32	16	4096	LQFP64	51	1.65-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152R6H6-A	32	Cortex-M3	32	16	4096	TFBGA64	50	1.65-3.6	8	0	0	0	0	1	19	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x31/8x27	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152R8T6-A	32	Cortex-M3	64	32	4096	LQFP64	51	1.65-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152R8H6-A	32	Cortex-M3	64	32	4096	TFBGA64	50	1.65-3.6	8	0	0	0	0	1	19	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x31/8x27	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152RBT6-A	32	Cortex-M3	128	32	4096	LQFP64	51	1.65-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152RBH6-A	32	Cortex-M3	128	32	4096	TFBGA64	50	1.65-3.6	8	0	0	0	0	1	19	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x31/8x27	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 L1系列 – Arm® Cortex®-M3超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L152V8T6-A	32	Cortex-M3	64	32	4096	LQFP100	83	1.65-3.6	8	0	0	0	0	1	24	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152V8H6-A	32	Cortex-M3	64	32	4096	UFBGA100	83	1.65-3.6	8	0	0	0	0	1	24	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152VBT6-A	32	Cortex-M3	128	32	4096	LQFP100	83	1.65-3.6	8	0	0	0	0	1	24	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152VBH6-A	32	Cortex-M3	128	32	4096	UFBGA100	83	1.65-3.6	8	0	0	0	0	1	24	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152CCT6	32	Cortex-M3	256	32	8192	LQFP48	37	1.65-3.6	8	1	0	0	0	1	14	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152CCU6	32	Cortex-M3	256	32	8192	UFQFPN48	37	1.65-3.6	8	1	0	0	0	1	14	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152UCY6	32	Cortex-M3	256	32	8192	WLCSP63	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152RCT6	32	Cortex-M3	256	32	8192	LQFP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152VCT6	32	Cortex-M3	256	32	8192	LQFP100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152VCH6	32	Cortex-M3	256	32	8192	UFBGA100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152ZCT6	32	Cortex-M3	256	32	8192	LQFP144	115	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152QCH6	32	Cortex-M3	256	32	8192	UFBGA132	109	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152RDT6	32	Cortex-M3	384	48	12288	LQFP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152RDY6	32	Cortex-M3	384	48	12288	WLCSP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152VDT6	32	Cortex-M3	384	48	12288	LQFP100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152QDH6	32	Cortex-M3	384	48	12288	UFBGA132	109	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152ZDT6	32	Cortex-M3	384	48	12288	LQFP144	115	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152VDT6-x	32	Cortex-M3	384	80	16384	LQFP100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 L1系列 – Arm® Cortex®-M3超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L152VDY6-x	32	Cortex-M3	384	80	16384	WLCSP104	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152RET6	32	Cortex-M3	512	80	16384	LQFP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152VET6	32	Cortex-M3	512	80	16384	LQFP100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152VEY6	32	Cortex-M3	512	80	16384	WLCSP104	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152QEH6	32	Cortex-M3	512	80	16384	UFBGA132	109	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L152ZET6	32	Cortex-M3	512	80	16384	LQFP144	115	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L162USB&LCD&AES功能型 - 32 MHz																																																					
STM32L162RCT6	32	Cortex-M3	256	32	8192	LQFP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162VCT6	32	Cortex-M3	256	32	8192	LQFP100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162VCH6	32	Cortex-M3	256	32	8192	UFBGA100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162QCH6	32	Cortex-M3	256	32	8192	UFBGA132	109	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162ZCT6	32	Cortex-M3	256	32	8192	LQFP144	115	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162RDT6	32	Cortex-M3	384	48	12288	LQFP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162RDY6	32	Cortex-M3	384	48	12288	WLCSP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162VDT6	32	Cortex-M3	384	48	12288	LQFP100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162QDH6	32	Cortex-M3	384	48	12288	UFBGA132	109	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162ZDT6	32	Cortex-M3	384	48	12288	LQFP144	115	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162VDY6-X	32	Cortex-M3	384	80	16384	WLCSP104	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0

STM32 L1系列 – Arm® Cortex®-M3超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L162RET6	32	Cortex-M3	512	80	16384	LQFP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162VET6	32	Cortex-M3	512	80	16384	LQFP100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162VEY6	32	Cortex-M3	512	80	16384	WLCSP104	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162ZET6	32	Cortex-M3	512	80	16384	LQFP144	115	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L4x1入门型 - 80 MHz																																																						
STM32L431KBU6	80	Cortex-M4	128	64	0	UFQFPN32	26	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	2	1 [Quad]	0	2	2	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431KCU6	80	Cortex-M4	256	64	0	UFQFPN32	26	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	2	1 [Quad]	0	2	2	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431CBT6	80	Cortex-M4	128	64	0	LQFP48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431CCT6	80	Cortex-M4	256	64	0	LQFP48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431CBU6	80	Cortex-M4	128	64	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431CCU6	80	Cortex-M4	256	64	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431CBY6	80	Cortex-M4	128	64	0	WLCSP49	39	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431CCY6	80	Cortex-M4	256	64	0	WLCSP49	39	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431RBT6	80	Cortex-M4	128	64	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431RCT6	80	Cortex-M4	256	64	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431RBI6	80	Cortex-M4	128	64	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431RCI6	80	Cortex-M4	256	64	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431RBY6	80	Cortex-M4	128	64	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431RCY6	80	Cortex-M4	256	64	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431VCT6	80	Cortex-M4	256	64	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L431VCI6	80	Cortex-M4	256	64	0	UFBGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L451CCU6	80	Cortex-M4	256	160	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L451CEU6	80	Cortex-M4	512	160	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L451RCT6	80	Cortex-M4	256	160	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L451RCI6	80	Cortex-M4	256	160	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L451REY6	80	Cortex-M4	512	160	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L451REI6	80	Cortex-M4	512	160	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L451RET6	80	Cortex-M4	512	160	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L451VCT6	80	Cortex-M4	256	160	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L451VCI6	80	Cortex-M4	256	160	0	UFBGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L451VET6	80	Cortex-M4	512	160	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L451VEI6	80	Cortex-M4	512	160	0	UFBGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L471RET6	80	Cortex-M4	512	128	0	LQFP64	51	1.71-3.6	9	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L471RGT6	80	Cortex-M4	1024	128	0	LQFP64	51	1.71-3.6	9	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L471VET6	80	Cortex-M4	512	128	0	LQFP100	82	1.71-3.6	9	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L471VGT6	80	Cortex-M4	1024	128	0	LQFP100	82	1.71-3.6	9	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L471QEI6	80	Cortex-M4	512	128	0	UFBGA132	109	1.71-3.6	9	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L471QGI6	80	Cortex-M4	1024	128	0	UFBGA132	109	1.71-3.6	9	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L471ZET6	80	Cortex-M4	512	128	0	LQFP144	114	1.71-3.6	9	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L471ZGT6	80	Cortex-M4	1024	128	0	LQFP144	114	1.71-3.6	9	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L4x2 USB FS产品线 - 80 MHz																																																					
STM32L412KBT6	80	Cortex-M4	128	40	0	LQFP32	26	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	1	1 [Quad]	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L412KBU6	80	Cortex-M4	128	40	0	UFQFPN32	26	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	1	1 [Quad]	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L412TBY6	80	Cortex-M4	128	40	0	WLCSP36	30	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	1	1 [Quad]	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L412CBT6	80	Cortex-M4	128	40	0	LQFP48	38	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L412CBU6	80	Cortex-M4	128	40	0	UFQFPN48	38	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L412RBT6	80	Cortex-M4	128	40	0	LQFP64	52	1.71-3.6	4	1	1	2	0	2	16	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L412RBI6	80	Cortex-M4	128	40	0	UFBGA64	52	1.71-3.6	4	1	1	2	0	2	16	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32L422KBT6	80	Cortex-M4	128	40	0	LQFP32	26	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	1	1 [Quad]	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L422KBU6	80	Cortex-M4	128	40	0	UFQFPN32	26	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	1	1 [Quad]	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L422TBY6	80	Cortex-M4	128	40	0	WLCSP36	30	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	1	1 [Quad]	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L422CBT6	80	Cortex-M4	128	40	0	LQFP48	26	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L422CBU6	80	Cortex-M4	128	40	0	UFQFPN48	26	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L422RBT6	80	Cortex-M4	128	40	0	LQFP64	26	1.71-3.6	4	1	1	2	0	2	16	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L422RBI6	80	Cortex-M4	128	40	0	UFBGA64	26	1.71-3.6	4	1	1	2	0	2	16	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0
STM32L432KBU6	80	Cortex-M4	128	64	0	UFQFPN32	26	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	2	1 [Quad]	0	2	2	1	1	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L432KCU6	80	Cortex-M4	256	64	0	UFQFPN32	26	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	2	1 [Quad]	0	2	2	1	1	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L442KCU6	80	Cortex-M4	256	64	0	UFQFPN32	26	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	2	1 [Quad]	0	2	2	1	1	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L452CCU6	80	Cortex-M4	256	160	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L452CEU6	80	Cortex-M4	512	160	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L452RCT6	80	Cortex-M4	256	160	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L452RCI6	80	Cortex-M4	256	160	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L452RET6	80	Cortex-M4	512	160	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L452REI6	80	Cortex-M4	512	160	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L452REY6	80	Cortex-M4	512	160	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L452VCT6	80	Cortex-M4	256	160	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L452VCI6	80	Cortex-M4	256	160	0	UFBGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L452VET6	80	Cortex-M4	512	160	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L452VEI6	80	Cortex-M4	512	160	0	UFBGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0
STM32L462CEU6	80	Cortex-M4	512	160	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	1	0
STM32L462RET6	80	Cortex-M4	512	160	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	1	0
STM32L462REI6	80	Cortex-M4	512	160	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	1	0
STM32L462REY6	80	Cortex-M4	512	160	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	1	0
STM32L462VET6	80	Cortex-M4	512	160	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	1	0
STM32L462VEI6	80	Cortex-M4	512	160	0	UFBGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	1	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L4x3 USB FS & LCD 产品线 - 80 MHz																																																					
STM32L433CBY6	80	Cortex-M4	128	64	0	WLCSP49	39	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L433CCT6	80	Cortex-M4	256	64	0	LQFP48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L433CCU6	80	Cortex-M4	256	64	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L433CCY6	80	Cortex-M4	256	64	0	WLCSP49	39	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L433RBT6	80	Cortex-M4	128	64	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L433RBI6	80	Cortex-M4	128	64	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L433RBY6	80	Cortex-M4	128	64	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L433RCT6	80	Cortex-M4	256	64	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L433RCI6	80	Cortex-M4	256	64	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L433RCY6	80	Cortex-M4	256	64	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L433VCT6	80	Cortex-M4	256	64	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L433VCI6	80	Cortex-M4	256	64	0	UFBGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x44/8x40	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L443CCT6	80	Cortex-M4	256	64	0	LQFP48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0
STM32L443CCU6	80	Cortex-M4	256	64	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0
STM32L443CCY6	80	Cortex-M4	256	64	0	WLCSP49	39	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0
STM32L443RCT6	80	Cortex-M4	256	64	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0
STM32L443RCI6	80	Cortex-M4	256	64	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L443RCY6	80	Cortex-M4	256	64	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x32/ 8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0
STM32L443VCT6	80	Cortex-M4	256	64	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x44/ 8x40	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0
STM32L443VCI6	80	Cortex-M4	256	64	0	UFBGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	4x44/ 8x40	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0
STM32L4x5 USB OTG产品线 - 80 MHz																																																					
STM32L475RCT6	80	Cortex-M4	256	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L475RET6	80	Cortex-M4	512	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L475RGT6	80	Cortex-M4	1024	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L475VCT6	80	Cortex-M4	256	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L475VET6	80	Cortex-M4	512	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L475VGT6	80	Cortex-M4	1024	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L4x6 USB OTG & LCD产品线 - 80 MHz																																																					
STM32L476RCT6	80	Cortex-M4	256	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	8x28/ 4x32	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L476RET6	80	Cortex-M4	512	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	8x28/ 4x32	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L476RGT6	80	Cortex-M4	1024	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	8x28/ 4x32	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L476JEY6	80	Cortex-M4	512	128	0	WLCSP72	57	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	8x28/ 4x32	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L476JGY6	80	Cortex-M4	1024	128	0	WLCSP72	57	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	8x28/ 4x32	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L476MEY6	80	Cortex-M4	512	128	0	WLCSP81	65	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	8x30/ 4x32	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L476MGY6	80	Cortex-M4	1024	128	0	WLCSP81	65	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	8x30/ 4x32	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L476VCT6	80	Cortex-M4	256	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	8x40/ 4x44	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L476VET6	80	Cortex-M4	512	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L476VGT6	80	Cortex-M4	1024	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L476QEI6	80	Cortex-M4	512	128	0	UFBGA132	109	1.71-3.6	11	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L476QGI6	80	Cortex-M4	1024	128	0	UFBGA132	109	1.71-3.6	11	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L476ZET6	80	Cortex-M4	512	128	0	LQFP144	114	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L476ZGT6	80	Cortex-M4	1024	128	0	LQFP144	114	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	1	0	0	0	0
STM32L486RGT6	80	Cortex-M4	1024	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	8x28/4x32	0	0	0	0	2	0	1	0	1	0	1	0	0	1	0
STM32L486JGY6	80	Cortex-M4	1024	128	0	WLCSP72	57	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	8x28/4x32	0	0	0	0	2	0	1	0	1	0	1	0	0	1	0
STM32L486VGT6	80	Cortex-M4	1024	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	1	0	0	1	0
STM32L486QGI6	80	Cortex-M4	1024	128	0	UFBGA132	109	1.71-3.6	11	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	1	0	0	1	0
STM32L486ZGT6	80	Cortex-M4	1024	128	0	LQFP144	114	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	1	0	0	1	0
STM32L496RET6	80	Cortex-M4	512	320	0	LQFP64	52	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496RGT6	80	Cortex-M4	1024	320	0	LQFP64	52	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496VET6	80	Cortex-M4	512	320	0	LQFP100	83	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496VGT6	80	Cortex-M4	1024	320	0	LQFP100	83	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496VGY6	80	Cortex-M4	1024	320	0	WLCSP100	83	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496QEI6	80	Cortex-M4	512	320	0	UFBGA132	110	1.71-3.6	11	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496QGI6	80	Cortex-M4	1024	320	0	UFBGA132	110	1.71-3.6	11	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L496ZET6	80	Cortex-M4	512	320	0	LQFP144	115	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496ZGT6	80	Cortex-M4	1024	320	0	LQFP144	115	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496AEI6	80	Cortex-M4	512	320	0	UFBGA169	136	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496AGI6	80	Cortex-M4	1024	320	0	UFBGA169	136	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L4A6RGT6	80	Cortex-M4	1024	320	0	LQFP64	52	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	1	1
STM32L4A6VGT6	80	Cortex-M4	1024	320	0	LQFP100	83	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	1	1
STM32L4A6VGY6	80	Cortex-M4	1024	320	0	WLCSP100	83	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	1	1
STM32L4A6QGI6	80	Cortex-M4	1024	320	0	UFBGA132	110	1.71-3.6	11	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	1	1
STM32L4A6ZGT6	80	Cortex-M4	1024	320	0	LQFP144	115	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	1	1
STM32L4A6AGI6	80	Cortex-M4	1024	320	0	UFBGA169	136	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	1	1

STM32 L4+系列 – Arm® Cortex®-M4超低功耗高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L4P5/4Q5系列：带USB OTG - 120 MHz																																																							
STM32L4P5CGT6	120	Cortex-M4	1024	320	0	LQFP48	38	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5CGU6	120	Cortex-M4	1024	320	0	UFQFPN48	38	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5CET6	120	Cortex-M4	512	320	0	LQFP48	38	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5CEU6	120	Cortex-M4	512	320	0	UFQFPN48	38	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5RGT6	120	Cortex-M4	1024	320	0	LQFP64	52	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5RET6	120	Cortex-M4	512	320	0	LQFP64	52	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5VGT6	120	Cortex-M4	1024	320	0	LQFP100	83	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5VGY6	120	Cortex-M4	1024	320	0	WLCSP100	83	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5VET6	120	Cortex-M4	512	320	0	LQFP100	83	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5VEY6	120	Cortex-M4	512	320	0	WLCSP100	83	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5QGI6	120	Cortex-M4	1024	320	0	UFBGA132	110	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5QEI6	120	Cortex-M4	512	320	0	UFBGA132	110	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5ZGT6	120	Cortex-M4	1024	320	0	LQFP144	115	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5ZET6	120	Cortex-M4	512	320	0	LQFP144	115	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5AGI6	120	Cortex-M4	1024	320	0	UFBGA169	136	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4P5AEI6	120	Cortex-M4	512	320	0	UFBGA169	136	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4Q5CGT6	120	Cortex-M4	1024	320	0	LQFP48	38	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	1	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1

STM32 L4+系列 – Arm® Cortex®-M4超低功耗高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L4Q5CGU6	120	Cortex-M4	1024	320	0	UFQFPN48	38	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4Q5RGT6	120	Cortex-M4	1024	320	0	LQFP64	52	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4Q5VGT6	120	Cortex-M4	1024	320	0	LQFP100	83	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4Q5VGY6	120	Cortex-M4	1024	320	0	WLCSP100	83	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4Q5QGI6	120	Cortex-M4	1024	320	0	UFBGA132	110	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4Q5ZGT6	120	Cortex-M4	1024	320	0	LQFP144	115	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4Q5AGI6	120	Cortex-M4	1024	320	0	UFBGA169	136	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4R5/4S5系列：带USB OTG - 120 MHz																																																					
STM32L4R5VGT6	120	Cortex-M4	1024	640	0	LQFP100	83	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	0	0
STM32L4R5VIT6	120	Cortex-M4	2048	640	0	LQFP100	83	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	0	0
STM32L4R5QGI6	120	Cortex-M4	1024	640	0	UFBGA132	110	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	0	0
STM32L4R5QII6	120	Cortex-M4	2048	640	0	UFBGA132	110	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	0	0
STM32L4R5ZGT6	120	Cortex-M4	1024	640	0	LQFP144	115	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	0	0
STM32L4R5ZIT6	120	Cortex-M4	2048	640	0	LQFP144	115	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	0	0
STM32L4R5ZGY6	120	Cortex-M4	1024	640	0	WLCSP144	115	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	0	0
STM32L4R5ZIY6	120	Cortex-M4	2048	640	0	WLCSP144	115	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	0	0
STM32L4R5AGI6	120	Cortex-M4	1024	640	0	UFBGA169	140	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	0	0
STM32L4R5AII6	120	Cortex-M4	2048	640	0	UFBGA169	140	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	0	0

STM32 L4+系列 – Arm® Cortex®-M4超低功耗高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L4S5VIT6	120	Cortex-M4	2048	640	0	LQFP100	83	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4S5QII6	120	Cortex-M4	2048	640	0	UFBGA132	110	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4S5ZIT6	120	Cortex-M4	2048	640	0	LQFP144	115	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4S5ZIY6	120	Cortex-M4	2048	640	0	WLCSP144	115	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4S5AII6	120	Cortex-M4	2048	640	0	UFBGA169	140	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4R7/4S7系列：带USB OTG与TFT接口 - 120 MHz																																																					
STM32L4R7VIT6	120	Cortex-M4	2048	640	0	LQFP100	83	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	0	0
STM32L4R7ZIT6	120	Cortex-M4	2048	640	0	LQFP144	115	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	0	0
STM32L4R7AII6	120	Cortex-M4	2048	640	0	UFBGA169	140	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	0	0
STM32L4S7VIT6	120	Cortex-M4	2048	640	0	LQFP100	83	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4S7ZIT6	120	Cortex-M4	2048	640	0	LQFP144	115	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4S7AII6	120	Cortex-M4	2048	640	0	UFBGA169	140	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1
STM32L4R9/4S9系列：带USB OTG与TFT接口 - 120 MHz																																																					
STM32L4R9VGT6	120	Cortex-M4	1024	640	0	LQFP100	77	1.71-3.6	9	2	2	2	0	1	14	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	0	0
STM32L4R9VIT6	120	Cortex-M4	2048	640	0	LQFP100	77	1.71-3.6	9	2	2	2	0	1	14	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	0	0
STM32L4R9ZGT6	120	Cortex-M4	1024	640	0	LQFP144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	0	0
STM32L4R9ZIT6	120	Cortex-M4	2048	640	0	LQFP144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	0	0
STM32L4R9ZGJ6	120	Cortex-M4	1024	640	0	UFBGA144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	0	0

STM32 L4+系列 – Arm® Cortex®-M4超低功耗高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L4R9ZIJ6	120	Cortex-M4	2048	640	0	UFBGA144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	0	0
STM32L4R9ZGY6	120	Cortex-M4	1024	640	0	WLCSP144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	0	0
STM32L4R9ZIY6	120	Cortex-M4	2048	640	0	WLCSP144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	0	0
STM32L4R9AGI6	120	Cortex-M4	1024	640	0	UFBGA169	131	1.71-3.6	9	2	2	2	0	1	14	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	0	0
STM32L4R9AII6	120	Cortex-M4	2048	640	0	UFBGA169	131	1.71-3.6	9	2	2	2	0	1	14	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	0	0
STM32L4S9VIT6	120	Cortex-M4	2048	640	0	LQFP100	77	1.71-3.6	9	2	2	2	0	1	14	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	1	1
STM32L4S9ZIT6	120	Cortex-M4	2048	640	0	LQFP144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	1	1
STM32L4S9ZIJ6	120	Cortex-M4	2048	640	0	UFBGA144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	1	1
STM32L4S9ZIY6	120	Cortex-M4	2048	640	0	WLCSP144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	1	1
STM32L4S9AII6	120	Cortex-M4	2048	640	0	UFBGA169	131	1.71-3.6	9	2	2	2	0	1	14	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	1	0	0	1	2	0	1	1	0	0	1	0	0	1	1

STM32 L5系列 – Arm® Cortex®-M33超低功耗高性能安全MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L5x2系列 - 110 MHz																																																						
STM32L552CCT6	110	Cortex-M33	256	256	0	LQFP48	38	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552CCU6	110	Cortex-M33	256	256	0	UFQFN48	38	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552CET6	110	Cortex-M33	512	256	0	LQFP48	38	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552CEU6	110	Cortex-M33	512	256	0	UFQFN48	38	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552CET6P	110	Cortex-M33	512	256	0	LQFP48	36	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	2+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552CEU6P	110	Cortex-M33	512	256	0	UFQFN48	36	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	2+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552RCT6	110	Cortex-M33	256	256	0	LQFP64	52	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552RET6	110	Cortex-M33	512	256	0	LQFP64	52	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552RET6P	110	Cortex-M33	512	256	0	LQFP64	50	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552RET6Q	110	Cortex-M33	512	256	0	LQFP64	47	1.71-3.6	9	2	2	3	0	2	15	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552MEY6P	110	Cortex-M33	512	256	0	WLCSP81	54	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552MEY6Q	110	Cortex-M33	512	256	0	WLCSP81	51	1.71-3.6	9	2	2	3	0	2	15	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552VET6	110	Cortex-M33	512	256	0	LQFP100	83	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552VCT6Q	110	Cortex-M33	512	256	0	LQFP100	79	1.71-3.6	9	2	2	3	0	2	14	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552VET6Q	110	Cortex-M33	512	256	0	LQFP100	79	1.71-3.6	9	2	2	3	0	2	14	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552QEI6P	110	Cortex-M33	512	256	0	UFBGA132	108	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552QCI6Q	110	Cortex-M33	256	256	0	UFBGA132	105	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1

STM32 L5系列 – Arm® Cortex®-M33超低功耗高性能安全MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L552QEI6Q	110	Cortex-M33	512	256	0	UFBGA132	105	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552ZET6	110	Cortex-M33	512	256	0	LQFP144	115	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552ZET6Q	110	Cortex-M33	512	256	0	LQFP144	111	1.71-3.6	9	2	2	3	0	2	14	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552ZCT6Q	110	Cortex-M33	256	256	0	LQFP144	111	1.71-3.6	9	2	2	3	0	2	14	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L562CET6	110	Cortex-M33	512	256	0	LQFP48	38	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562CEU6	110	Cortex-M33	512	256	0	UFQFN48	38	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562CET6P	110	Cortex-M33	512	256	0	LQFP48	36	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	2+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562CEU6P	110	Cortex-M33	512	256	0	UFQFN48	36	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	2+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562RET6	110	Cortex-M33	512	256	0	LQFP64	52	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562RET6P	110	Cortex-M33	512	256	0	LQFP64	50	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562RET6Q	110	Cortex-M33	512	256	0	LQFP64	47	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562MEY6P	110	Cortex-M33	512	256	0	WLCSP81	54	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562MEY6Q	110	Cortex-M33	512	256	0	WLCSP81	51	1.71-3.6	9	2	2	3	0	2	15	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562VET6	110	Cortex-M33	512	256	0	LQFP100	83	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562VET6Q	110	Cortex-M33	512	256	0	LQFP100	79	1.71-3.6	9	2	2	3	0	2	14	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562QEI6	110	Cortex-M33	512	256	0	UFBGA132	110	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562QEI6P	110	Cortex-M33	512	256	0	UFBGA132	108	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562QEI6Q	110	Cortex-M33	512	256	0	UFBGA132	105	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1

STM32 L5系列 – Arm® Cortex®-M33超低功耗高性能安全MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L562ZET6	110	Cortex-M33	512	256	0	LQFP144	115	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1
STM32L562ZET6Q	110	Cortex-M33	512	256	0	LQFP144	111	1.71-3.6	9	2	2	3	0	2	14	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	1	1

STM32 WB系列 – Arm® Cortex®-M4和Cortex®-M0+双核无线MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32WBx5 系列 - 64 MHz																																																					
STM32WB50CGU5	64	M4 & M0+	1024	128	0	UFQFPN48	30	2-3.6	3	1	1	2	0	0	0	1	10	0	0	0	0	0	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	0
STM32WB55CCU6	64	M4 & M0+	256	128	0	UFQFPN48	30	1.71-3.6	3	1	1	2	0	0	0	1	13	0	2	0	0	0	1	1 [QSPI]	0	2	1	1	0	0	0	0	1	0	0	0	0	4x13	0	0	0	0	1	0	0	0	0	0	1	0	1	1	0
STM32WB55CEU6	64	M4 & M0+	512	256	0	UFQFPN48	30	1.71-3.6	3	1	1	2	0	0	0	1	13	0	2	0	0	0	1	1 [QSPI]	0	2	1	1	0	0	0	0	1	0	0	0	0	4x13	0	0	0	0	1	0	0	0	0	0	1	0	1	1	0
STM32WB55CGU6	64	M4 & M0+	1024	256	0	UFQFPN48	30	1.71-3.6	3	1	1	2	0	0	0	1	13	0	2	0	0	0	1	1 [QSPI]	0	2	1	1	0	0	0	0	1	0	0	0	0	4x13	0	0	0	0	1	0	0	0	0	0	1	0	1	1	0
STM32WB55RCV6	64	M4 & M0+	256	128	0	VFQFPN68	49	1.71-3.6	3	1	1	2	0	0	0	1	19	0	2	0	0	0	2	1 [QSPI]	0	2	1	1	0	0	0	0	1	0	0	0	0	7x23 or 4x26	0	0	0	0	1	0	0	0	0	0	1	0	1	1	0
STM32WB55REV6	64	M4 & M0+	512	256	0	VFQFPN68	49	1.71-3.6	3	1	1	2	0	0	0	1	19	0	2	0	0	0	2	1 [QSPI]	0	2	1	1	0	0	0	0	1	0	0	0	0	7x23 or 4x26	0	0	0	0	1	0	0	0	0	0	1	0	1	1	0
STM32WB55RGV6	64	M4 & M0+	1024	256	0	VFQFPN68	49	1.71-3.6	3	1	1	2	0	0	0	1	19	0	2	0	0	0	2	1 [QSPI]	0	2	1	1	0	0	0	0	1	0	0	0	0	7x23 or 4x26	0	0	0	0	1	0	0	0	0	0	1	0	1	1	0
STM32WB55VCY6	64	M4 & M0+	256	128	0	WLCSP100	72	1.71-3.6	3	1	1	2	0	0	0	1	19	0	2	0	0	0	2	1 [QSPI]	0	2	1	1	0	0	0	0	1	0	0	0	0	8x40 or 4x44	0	0	0	0	1	0	0	0	0	0	1	0	1	1	0
STM32WB55VEY6	64	M4 & M0+	512	256	0	WLCSP100	72	1.71-3.6	3	1	1	2	0	0	0	1	19	0	2	0	0	0	2	1 [QSPI]	0	2	1	1	0	0	0	0	1	0	0	0	0	8x40 or 4x44	0	0	0	0	1	0	0	0	0	0	1	0	1	1	0
STM32WB55VGY6	64	M4 & M0+	1024	256	0	WLCSP100	72	1.71-3.6	3	1	1	2	0	0	0	1	19	0	2	0	0	0	2	1 [QSPI]	0	2	1	1	0	0	0	0	1	0	0	0	0	8x40 or 4x44	0	0	0	0	1	0	0	0	0	0	1	0	1	1	0

STM32 WL系列 - Arm® Cortex®-M4 / Arm® Cortex®-M4+M0+长距离无SoC

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32WLE5系列 - Cortex-M4 48 MHz支持LoRa®、(G)FSK、(G)MSK和BPSK																																																						
STM32WLE5J8I6	48	Cortex M4	64	20	0	UFBGA73	43	1.8-3.6	3	1	1	3	0	1	16	0	0	1	2	0	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	1	0
STM32WLE5JBI6	48	Cortex M4	128	48	0	UFBGA73	43	1.8-3.6	3	1	1	3	0	1	16	0	0	1	2	0	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	1	0
STM32WLE5JCI6	48	Cortex M4	256	64	0	UFBGA73	43	1.8-3.6	3	1	1	3	0	1	16	0	0	1	2	0	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	1	0

STM32 MP1系列 – 双核Arm® Cortex®-A7和Arm® Cortex®-M4高超性能MPU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32MP151系列- Arm Cortex-A7 + Cortex-M4																																																						
STM32MP151AAD1	650 + 209	Cortex A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2[HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP151AAB1	650 + 209	Cortex A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2[HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP151AAC1	650 + 209	Cortex A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP151AAA1	650 + 209	Cortex A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP151CAD1	650 + 209	Cortex A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2[HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP151CAB1	650 + 209	Cortex A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2[HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP151CAC1	650 + 209	Cortex A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP151CAA1	650 + 209	Cortex A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP151DAD1	800 + 209	Cortex A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP151DAB1	800 + 209	Cortex A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP151DAC1	800 + 209	Cortex A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP151DAA1	800 + 209	Cortex A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP151FAD1	800 + 209	Cortex A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1

STM32 MP1系列 – 双核Arm® Cortex®-A7和Arm® Cortex®-M4高超性能MPU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32MP151FAB1	800 + 209	Cortex A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP151FAC1	800 + 209	Cortex A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP151FAA1	800 + 209	Cortex A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP153系列- 双核Arm Cortex-A7 + Cortex-M4																																																						
STM32MP153AAD1	650 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP153AAB1	650 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP153AAC1	650 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP153AAA1	650 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP153CAD1	650 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP153CAB1	650 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP153CAC1	650 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP153CAA1	650 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP153DAD1	800 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP153DAB1	800 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1

STM32 MP1系列 – 双核Arm® Cortex®-A7和Arm® Cortex®-M4高超性能MPU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32MP153DAC1	800 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP153DAA1	800 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	0	1
STM32MP153FAD1	800 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP153FAB1	800 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP153FAC1	800 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP153FAA1	800 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	4	8	1	0	0	1	0	0	1	1
STM32MP157系列- 双核Arm Cortex-A7 + Cortex-M4 + 3D GPU + DSI + CAN FD																																																					
STM32MP157AAD3	650 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	0	1
STM32MP157AAB3	650 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	0	1
STM32MP157AAC3	650 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	0	1
STM32MP157AAA3	650 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	0	1
STM32MP157CAD3	650 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	1	1
STM32MP157CAB3	650 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	1	1
STM32MP157CAC3	650 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	1	1

STM32 MP1系列 – 双核Arm® Cortex®-A7和Arm® Cortex®-M4高超性能MPU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32MP157CAA3	650 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	1	1
STM32MP157DAD1	800 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	0	1
STM32MP157DAB1	800 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	0	1
STM32MP157DAC1	800 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	0	1
STM32MP157DAA1	800 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	0	1
STM32MP157FAD1	800 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2[Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	1	1
STM32MP157FAB1	800 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	1	1
STM32MP157FAC1	800 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	1	1
STM32MP157FAA1	800 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	1	1	4	4	8	1	0	0	1	0	0	1	1

缩写和封装

缩写

ADC : Analog-to-digital converter
ART : Auto-reload timer
ATAPI : AT attachment packet interface
AWU : Auto wake-up from halt
BLPD : Byte level protocol decoder
BOD : Brown-out detector
CAN : Controller area network
CAPCOM : Capture compare
CSS : Clock security system
DALI : Digital addressable lighting interface
DDC : Data display channel
DiSEqC : Digital satellite equipment control
DMA : Direct memory access
DSC : Dual supply control
DTC : Data transfer coprocessor
ETM : Embedded trace macrocell
EMI : External memory interface
HDLC : High-level data link control
IAP : In-application programming
IC/OC : Input capture/output compare
ICP : programming
IR : Infrared
IrDA : Infrared data association
ISP : In-situ programming
I^2C : Inter-integrated circuit
I^2S : Inter-IC sound
LCD : Liquid crystal display
LIN : Local interconnect network
LVD : Low voltage detection
MAC : Multiply accumulator
MC : Motor control
MFT : Multifunction timer
MMC : MultiMediaCard
NMI : Non-maskable interrupt
OSG : Oscillator safeguard
PCA : Programmable counter array
PDR : Power-down reset
PHW : Programmable halt wake-up
PEC : Peripheral event controller
PLD : Programmable logic device
PLL : Phase locked loop
POR : Power-on reset
PVD : Programmable voltage detector
PVR : Programmable voltage regulator
PWM : Pulse width modulation
ROP : Readout protection
RTC : Real-time clock timer
SAI : Serial Audio Interface
SC : Smartcard
SCI : Serial communication interface
SCR : Smartcard reader
SDIO : Secure digital input output
SDMMC : Secure Digital / Multi Media Card
SMI : Serial memory interface
SPI : Serial peripheral interface
SSC : Single-cycle switching support
SSP : Synchronous serial port
TBU : Time base unit
TLI : Top level interrupt
UART : Universal asynchronous receiver transmitter
USART : Universal sync/async receiver transmitter
USB : Universal Serial Bus
WDG : Watchdog timer
WWDG : Window watchdog timer

封装

DIP : Dual in-line package
LCC : Leaded chip carrier
PDIP Shrink : Shrink Plastic Dual In-line Package
PQFP : Plastic quad flat package
SO : Small outline
LQFP : Low-profile quad flat package
PBGA : Plastic ball grid array
DFN : Dual flat no-lead
QFN : Quad flat no-lead
WLCSP : Wafer-Level Chip-Scale Package

STM32 & STM8产品型号(仅适用于MCU)

STM32	F	051	R	8	T	6	x	xx

家族

STM32 32位MCU / MPU
STM8 8位MCU

产品类别

A 汽车级
F 基础型
L 超低功耗
S 标准型
WB 蓝牙及802.15.4
WL 长距离无限产品
H 高性能
G 主流型

特定功能(3位数字)

(依据产品系列非详细列表)

STM32x …
051 入门级
103 STM32基础型
303 103升级版，带DSP和模拟外设
407 高性能，带DSP和FPU
152 超低功耗

STM8x …/STM8Ax…
103 主流入门级
F52 汽车级CAN
L31 低端汽车级

引脚数(适用于STM8和STM32)

D	14引脚	C	48 & 49引脚	A	169引脚
Y	20引脚(STM8)	U	63引脚	I	176 & 201 (176+25)引脚
F	20引脚(STM32)	R	64 & 66引脚	B	208引脚
E	24 & 25引脚	J	72引脚	N	216引脚
G	28引脚	M	80引脚	X	256引脚
K	32引脚	O	90引脚	汽车级	
T	36引脚	V	100引脚	8	48
H	40引脚	Q	132引脚	9	64
S	44引脚	Z	144引脚	A	80

闪存容量(Kbytes)

0	1
1	2
2	4
3	8
4	16
5	24
6	32
7	48
8	64
9	72
A	96 or 128*
B	128
Z	192
C	256
D	384
E	512
F	768
G	1024
H	1536
I	2048

Note:
* 仅针对STM8A

封装

B Plastic DIP*
D Ceramic DIP*
G Ceramic QFP
H LFBGA /TFBGA
I UFBGA Pitch 0.5**
J UFBGA Picth 0.8**
K UFBGA Picth 0.65**
M Plastic SO
P TSSOP
Q Plastic QFP
T QFP
U UFQFPN
V VFQFPN
Y WLCSP

* Dual-in-Line封装
** 仅针对全新产品系列
现有产品系列请使用H

温度范围(°C)

6和A - 40到+ 85
7和B - 40到+ 105
3和C - 40到+ 125
D - 40到+ 150

固件版税

U Universal 不用于生产(样品和工具)
V MP3解码器
W MP3编解码器
D IS2T JAVA

选项

xxx Fastrom code
or
xTR Tape and Real
Dxx No RTC (STM8L)
Dxx BOR OFF with Special bonding + Boot standard
Dxx BOR OFF with Boot I2CS (Special)
Sxx BOR OFF
Ixx BOR ON
No Letter BOR ON + Boot standard
or
Yxx Die rev (Y)

STM32MPU产品型号

STM32	MP	157	A	AA	3	T

家族

基于ARM内核的32位处理器

产品类别

MP　MPU微处理器

特定功能 (3位数字)

151　Arm Cortex-A7 + Cortex-M4
153　Dual Arm Cortex-A7 + Cortex-M4 + CAN FD
157　Dual Arm Cortex-A7 + Cortex-M4 +3D GPU + DSI + CAN FD

类型

A　基本安全型　650MHz
D　基本安全型　800MHz
C　增强安全型　650MHz
F　增强安全型　800MHz

选项

AD　TFBGA257 10x10, 257 balls pitch 0.5 mm

AB　LFBGA354 16x16, 354 balls pitch 0.8 mm
AC　TFBGA361 12x12, 361 balls pitch 0.5 mm
AA　LFBGA448 18x18, 448 balls pitch 0.8 mm

温度范围(°C)

1　-20°C < 结温< +105°C
3　-40°C < 结温< +125°C

封装

T　卷带包装
无字符　管装或托盘包装

ST MCU Finder

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