Team 7			Legend:	On trac	ck	Low ris	sk	Med risk		High ris	sk	Unassi	gned																					
Project start date: 8	8/29/2024			Septemi	ber							Oct	ober																					
Scrolling increment:	14			12 13 14	15 16	17 18 19	20 21 2	2 23 24	25 26 2	27 28 29	30 1	2 3	4 5 6	5 7 8	9 10	11 12	13 14	15 16 1	17 18 1	9 20 21	22 23	24 25	26 27 2	8 29 30	31 1	2 3 4	5 6	7 8	9 10	11 12 1	3 14 15	16 17 1	8 19 2	20 21 22 2
Milestone description	Category Pr	rogress Start	Days	T F S	S M	т w т	F S S	s м т	w T	F S S	мт	w T	F S S	S M T	w T	F S	S M	T W	T F S	s s m	T W	T F	s s N	1 T W	T F	s s m	T W	T F	s s	мтм	/ T F	S S P	и т и	W T F S
Project Proposal		9/12/2024	18																															
Preliminary Research	Low Risk	9/25/2024	9																															
Learning PCB Design		10/3/2024	14																															
Conceptual Design Drafting	On Track	10/10/2024	8																															
Fall Break	On Track	10/17/2024	2																															
Conceptual Design Finalize	Low Risk	10/18/2024	10																															
Subsystem Interfacing	Med Risk	10/21/2024	14																															
PCB Design		10/21/2024	30																															
Circuit Design	High Risk	10/21/2024	30																															
Conceptual Design Presentation		10/21/2024	9																															
Detailed Design	On Track	11/14/2024	16																															
Final Presentation	Low Risk	11/19/2024	11																															
Winter Break	On Track	12/12/2024	35																															
Ordering	On Track	1/16/2025	14																															
PCB Assembly	Low Risk	1/26/2025	24																															
Programming Microcontrollers	On Track	1/26/2025	30																															
Experimentation & analysis	Low Risk	2/1/2025	28																															
Iteration and debug	On Track	3/1/2025	60																															
Final report and presentation	Low Risk	4/1/2025	30																															
To add more data, Insert new rows ABOVE this one																																		

Team 7			Legen	d: On track	Low risk	Med risk	High risk	Unassi	gned													
Project start date:	8/29/2024			November			December							January								
Scrolling increment:	86			23 24 25 26	27 28 29 30 1	2 3 4 5 6	7 8 9 10 11	12 13 14	15 16 17 1	8 19 20 21 23	23 24 25 2	6 27 28 29 30	31 1 2 3	3 4 5 6	7 8 9 10	11 12 13 14 1	15 16 17 18	19 20 21	22 23 24	25 26 27 2	3 29 30	31 1 2 3
Milestone description	Category	Progress 5	Start Days	S S M T	W T F S S	M T W T F	S S M T W	T F S	S M T V	V T F S S	M T W	T F S S M	T W T	s s m	T W T F	S S M T	W T F S	S M T	W T F	S S M	w T	F S S N
Project Proposal		9/1	2/2024 18																			
Preliminary Research	Low Risk	9/2	25/2024 9																			
Learning PCB Design		10/	3/2024 14																			
Conceptual Design Drafting	On Track	10/	10/2024 8																			
Fall Break	On Track	10/	17/2024 2																			
Conceptual Design Finalize	Low Risk	10/	18/2024 10																			
Subsystem Interfacing	Med Risk	10/	21/2024 14																			
PCB Design		10/	21/2024 30																			
Circuit Design	High Risk	10/	21/2024 30																			
Conceptual Design Presentation		10/	21/2024 9																			
Detailed Design	On Track	11/	14/2024 16																			
Final Presentation	Low Risk	11/	19/2024 11																			
Winter Break	On Track	12/	12/2024 35																			
Ordering	On Track	1/1	.6/2025 14																			
PCB Assembly	Low Risk	1/2	26/2025 24																			
Programming Microcontrollers	On Track	1/2	16/2025 30																			
Experimentation & analysis	Low Risk	2/	1/2025 28																			
Iteration and debug	On Track	3/	1/2025 60																			
Final report and presentation	Low Risk	4/:	1/2025 30																			
To add more data, Insert new																						

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Med risk Low risk High risk Unassigned Team 7 Project start date: 8/29/2024 February March 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 1 2 3 4 5 6 7 8 9 10 11 12 13 14 Scrolling increment: 156 Milestone description Days S S M T W T F S S M T W T T F S S M T W T T F S S M T W T T F S S M T W Start Category 9/12/2024 18 Project Proposal Preliminary Research Low Risk 9/25/2024 Learning PCB Design 10/3/2024 On Track 10/10/2024 Conceptual Design Drafting Fall Break On Track 10/17/2024 2 Conceptual Design Finalize 10/18/2024 10 Med Risk 10/21/2024 14 Subsystem Interfacing PCB Design 10/21/2024 30 Circuit Design High Risk 10/21/2024 30 Conceptual Design 10/21/2024 Presentation **Detailed Design** On Track 11/14/2024 16 Final Presentation Low Risk 11/19/2024 11 Winter Break On Track 12/12/2024 35 On Track 1/16/2025 14 Ordering PCB Assembly Low Risk 1/26/2025 24 Programming On Track 1/26/2025 30 Microcontrollers Experimentation & analysis 2/1/2025 28 Iteration and debug 3/1/2025 Final report and presentation Low Risk 4/1/2025 30 To add more data, Insert new

To add more data, Insert no rows ABOVE this one

Team 7			L	Legend:	On	track		Low	risk	М	led risk	k	Hig	h risk		Unas	ssigned	i																									
Project start date:	8/29/2024				Marc	h			April															May	,																		
Scrolling increment:	210				27 28	29 30	31 1	1 2	3 4	5 6	7 8	9 10) 11 12	2 13 1	14 15	16 17	7 18 1	9 20	21 22	2 23 24	25 2	6 27 :	28 29 3	0 1 2	2 3	4 5	6 7	8 9	10 11	12 13 1	14 15 1	16 17	18 19 2	0 21 2	22 23 2	4 25 26	27 28	8 29	30 31	1 2	3 4	l 5 é	5 7
Milestone description	Category	Progress	Start D	Days	T F	s s	мт	r w	T F	s s	мт	w T	F S	s n	мт	w T	F S	s s	мт	w T	F S	s s	M T V	V T F	s s	s M	T W	T F	s s	M T V	w T	F S	S M	r w	T F S	s s N	T W	/ т	F S	s M	T W	v T I	F S
Project Proposal		9,	/12/2024	18																																							Т
Preliminary Research	Low Risk	9,	/25/2024	9																																							
Learning PCB Design		10	0/3/2024	14																																							
Conceptual Design Drafting	On Track	10	0/10/2024	8																																							
Fall Break	On Track	10	0/17/2024	2																																							
Conceptual Design Finalize	Low Risk	10	0/18/2024	10																																							
Subsystem Interfacing	Med Risk	10	0/21/2024	14																																							
PCB Design		10	0/21/2024	30																																							
Circuit Design	High Risk	10	0/21/2024	30																																							
Conceptual Design Presentation		10	0/21/2024	9																																							
Detailed Design	On Track	11	1/14/2024	16																																							
Final Presentation	Low Risk	11	1/19/2024	11																																							
Winter Break	On Track	12	2/12/2024	35																																							
Ordering	On Track	1,	/16/2025	14																																							
PCB Assembly	Low Risk	1,	/26/2025	24																																							
Programming Microcontrollers	On Track	1,	/26/2025	30																																							
Experimentation & analysis	Low Risk	2	2/1/2025	28																																							
Iteration and debug	On Track	3	3/1/2025	60																																							
Final report and presentation	Low Risk	4	4/1/2025	30																																							
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