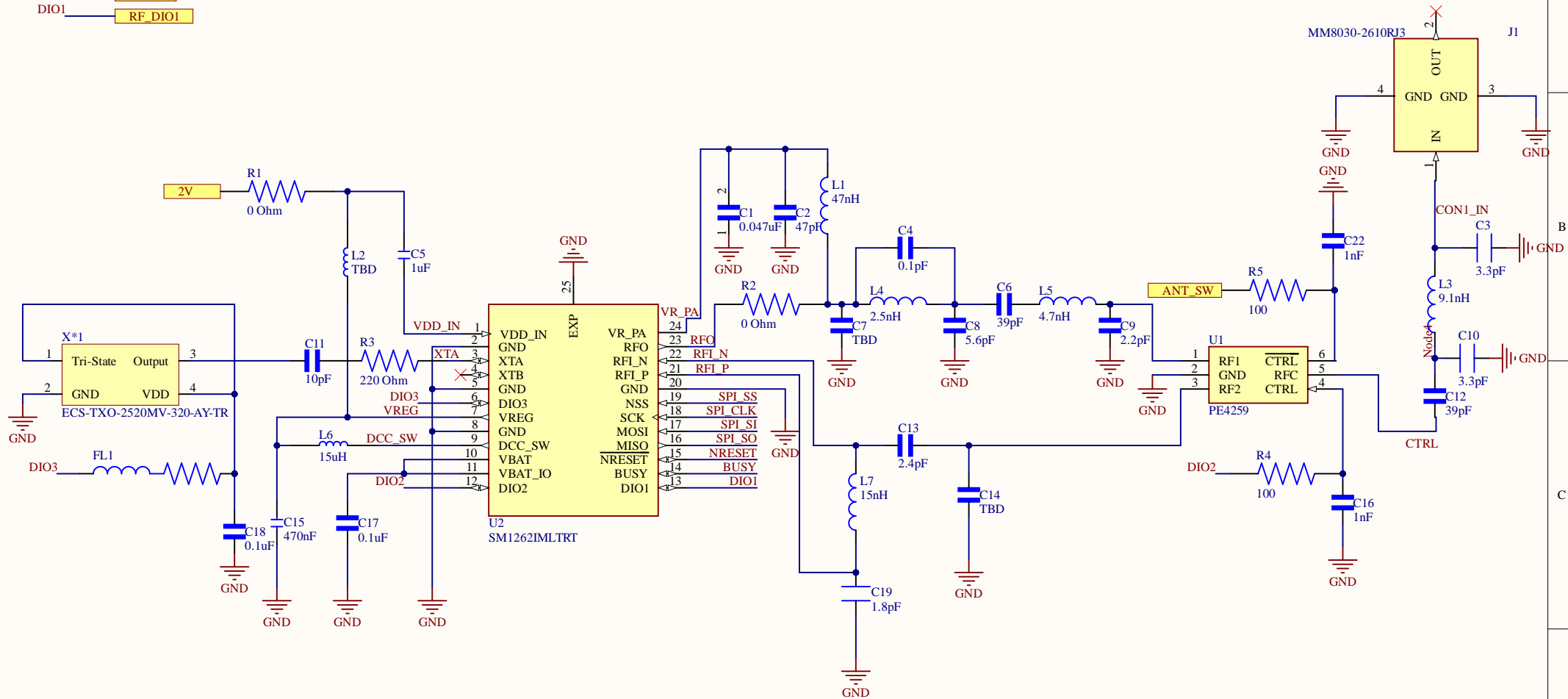


SPI\_SS — SPI\_SS  
 SPI\_CLK — SPI\_CLK  
 SPI\_SI — SPI\_MO  
 SPI\_SO — SPI\_MI  
 NRESET — RF\_NRESET  
 BUSY — BUSY  
 DIO1 — RF\_DIO1



Title		
LoRa Addon Board for Kallisto		
Size	Number	Revision
A4	1	0.1
Date:	7/08/2022	Sheet of Fraunhofer Portugal
File:	\\..\\LoRA_Schematic_v2.SchDoc	Drawn By: Tiago Carvalho