

CprE 3810: Computer Organization and Assembly Level Programming

Team Contract – Project Part 2

Project Teams Group #: _____

Team Members: James Gaul

Andy Eslick

Discuss the following aspects of teamwork with your team – make sure to get input from each member. Write down your team's consensus for each of the bolded headings.

Italicized text contains instructions and examples and should be deleted once you've read it. Please see the example contract for rough length expectations.

Course Goals:

- Get an A (or at least as high a score as possible) on this project.
- Have a processor that works as reliably as possible, to ensure a solid foundation for future project portions.

Team Expectations:

- Conduct: Work reliably, remain polite and civil
- Communication: Text Messages, we will respond within 24 hours, barring emergency situations.
- Group conventions:
 - s_ in front of signals, i_ for input, o_ for output.
 - compilation and simulation will be placed in separate .do files
 - Do files to ensure readable names and values
 - We will use GitHub for version control
- Meetings: Given the significant portion of the course that the lab covers, it is expected that your team will spend more time working on the labs than in your scheduled lab sections. How will your group expect to handle this? **Please include at least two additional times outside of lab that your team can meet (preferably in-person).**
 - We can meet on Mondays, Fridays, Weekends, and some evenings.
 - In the evenings, online works better for James

- Peer Evaluation Criteria: Now that you have experience working on a 381 lab with a team, please create a brief criteria for how effort and contribution are defined. Note that teams with vastly divergent scores may require a meeting with course instructor and result in different grades for different group members. Teams with reasonably equitable scores will receive the same grade.
 - Design – design is easy to follow, structural for everything beyond basic components, avoids process statements unless absolutely necessary
 - Tests – Tests are easy to read and understand, check components as comprehensively as possible, provide easily

Role Responsibilities: Complete the following planning table. Each lab part should be the responsibility of one team member. Also make sure that no one team member is the lead on both the design and test aspects of a single lab part. These guidelines aid in all students having a complete view of the lab. Note that the non-lead is encouraged to participate and support the lead wherever possible, increasing both the quality of the lab part and each team member's knowledge.

Lab Part		Estimated Time	Design		Test	
			Lead	Deadline	Lead	Deadline
Software-Scheduled Pipeline	Control Signals	0.5 hr	Andy		James	
	Datapath	3 hr	James		Andy	
	Testing	3 hr	Andy		James	
	Synthesis (human effort)	0.5 hr	Andy		James	
Hardware-Scheduled Pipeline	Pipeline Register Update	1 hr	James		Andy	
	Data Hazard Avoidance	4 hr	Andy		James	
	Control Hazard Avoidance	2-6 hr based on group size	James/ Andy		Both	
	Integration (Hardware-Schedule Pipeline)	3 hr	Andy		James	
	Testing	3 hr	James		Andy	
	Synthesis	0.5 hr	Andy		James	

Estimated Time is given as a very rough guide for even distribution of tasks assuming you've already read through the lab document and have the prerequisite knowledge. Please note that to be done properly, the test programs will require significant time investment, but will result in a much stronger final design.

Integrity of Work: Do not delete the following. We agree that the work we provide to other team members and ultimately submit for a grade is a direct result of our own work as described in the course syllabus. Specifically, we will generate all VHDL code ourselves and not copy VHDL code from online sources, other groups, book companion material, or past student projects to which anyone outside of my team has contributed.

Student Signature _____ Date _____

Student Signature: James Gaul Date 11/7/25

Student Signature: Andrew Eslick Date 11/7/25