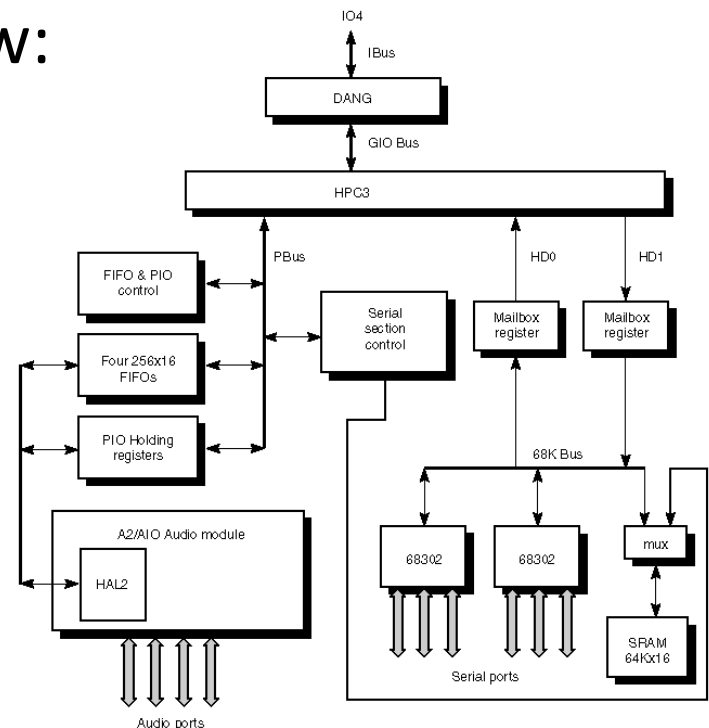# Edge Detection

Student name

Team #

Architecture and key ideas.

- A figure that shows the overall structure of your design. An example from www:

- Key ideas on how the circuit operates:

  - For example snapshots of register content

NOTE: Focus on the big picture.

You are not able to explain details during the give time frame.

FSMD

The major part of your design is one or more FSMDs

- Present your FSMD(s) (as illustrated in chap 11.)
- **The FSMD(s) should be complete** but you are not able to explain every single operation during the give time frame.
- Focus on the main operations.

Performance and Cost

- Number of multipliers, register, and adders
- Expected throughput:
  - Pictures per second
  - Number of clock cycles per picture
  - … or smilar.

Project Plan (Time Line)

- We expect a list where we can see that you did some planning.

- Make sure that you will meet all the deadlines.