



Licensed under CC-BY-SA 4.0
Copyright Franklin Harding 2020

Sheet:
File: tigard.kicad_pcb

Title: Tigard

Size: A4 Date: 2020-10-27

KiCad E.D.A. 8.0.5

Rev: v1.1

Id: 1/1

DCD DSR DTR CTS RTS RX TX GND VTGT

UART ↙

4x M3 ↗

Set MODE switch:

	JTAG	SPI	SWD	I2C
POS	LEFT		RIGHT	

Set TARGET (VREF)
switch to target logic
level, or VTGT (for a
self-powered target)

securinghw.com

↗ HACK

JTAG ↙ Bridge HACK to bypass
SWDIO hack resistor

\$ flashrom
-p ft2232_spi:type=2232H,port=B,divisor=4

SRST TRST TMS TDO TDI TCK GND VTGT

LA	Bitmagic	FT2232	JTAG	SPI	SWD	I2C	UART
1	xPB0	BD0	TCK	SCK	SWCLK	SCL	
2	xPB1	BD1	TDI	COPI	SWDIO	SDA	
3	xPB2	BD2	TDO	CIP0	SWDIO	SDA	
4	xPB3	BD3	TMS	CS			
5	xPB4	BD4	TRST				
6	xPB5	BD5	SRST				
7	xPB6	AD0					TX
8	xPB7	AD1					RX

ISO ↗

Cut ISO to
isolate VREF
from VTGT

↘ NC NC NC GND

<https://github.com/tigard-tools/tigard>

© Franklin Harding 2020
CC-BY-SA 4.0

I2C

VTGT NC SCL SDA

Licensed under CC-BY-SA 4.0
Copyright Franklin Harding 2020

Sheet:
File: tigard.kicad_pcb

Title: Tigard

Size: A4 Date: 2020-10-27

KiCad E.D.A. 8.0.5

Rev: v1.1

Id: 1/1