

THE CHINESE UNIVERSITY OF HONG KONG, SHENZHEN

# CSC3050

Computer Architecture

# Assignment 2 Report

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# Contents

1	Envi	ronment	2
<b>2</b>	Over	view	2
3	Design		
	3.1	Architecture	2
	3.2	Memory & Registers	3
	3.3	Instruction Execution	3
4	Imple	${f ementation}$	3
	4.1	Instruction Execution Functions	3
	4.2	System Calls	3
	4.3	Checkpoints	4
5	Prog	ram Output	5

#### 1 Environment

cat /etc/\*-release output:

```
1 DISTRIB_ID=Ubuntu
2 DISTRIB_RELEASE = 22.04
3 DISTRIB_CODENAME = jammy
4 DISTRIB_DESCRIPTION="Ubuntu 22.04.3 LTS"
5 PRETTY_NAME="Ubuntu 22.04.3 LTS"
6 NAME = "Ubuntu"
7 VERSION_ID="22.04"
8 VERSION="22.04.3 LTS (Jammy Jellyfish)"
9 VERSION_CODENAME = jammy
10 ID=ubuntu
11 ID_LIKE=debian
12 HOME_URL="https://www.ubuntu.com/"
13 SUPPORT_URL="https://help.ubuntu.com/"
14 BUG_REPORT_URL="https://bugs.launchpad.net/ubuntu/"
15 PRIVACY_POLICY_URL="https://www.ubuntu.com/legal/terms-and-policies
     /privacy-policy"
16 UBUNTU_CODENAME = jammy
  python3 --version output:
```

```
1 Python 3.10.12
```

#### 2 Overview

The MIPS Simulator is a program designed to emulate the behavior of a MIPS processor. It is capable of executing MIPS assembly instructions and provides a platform for debugging and analyzing MIPS programs. The simulator is implemented in Python and supports a subset of MIPS instructions. The main part of the MIPS Simulator is implemented in simulator.py with a function libaray named lib.py.

## HOW TO RUN:

Navigate to the source code directory and run the following command:

```
python3 simulator.py test.asm test.txt test_checkpoints.txt test.in
```

Or you can run the test shell file in the test\_bash directory:

```
1 bash test.sh
```

#### 3 Design

#### 3.1Architecture

The simulator.py follows a simple architecture comprising a MIPS-like memory, registers, and an instruction execution unit. It utilizes a bytearray to represent the memory and an array for registers. The program counter keeps track of the current instruction being executed.

When the simulator start running, it will first initialize the memory, register and the output file information by analyzing all the static data. Then it will start to execute the instructions one by one and it will update the memory and register according to the corresponding instructions. When the simulator reaches the end of the program, it will print the final memory and register information to the output file. In addition, there has a flag array in order to record the return value and exit information. The simulator will stop if it receive the exit signal and generate the output file finally.

## 3.2 Memory & Registers

The memory is represented as a bytearray in Python, providing a contiguous block of addressable memory.

```
1 mem = bytearray(MEMORY_SIZE)
2 reg = [0] * (32 + 3)
```

This bytearray is divided into two segments: the text segment and the data segment. The text segment stores machine code instructions, while the data segment holds static data.

The register file is implemented as an array, simulating the registers of a MIPS processor. Each register is represented by an element in the array. The array allows for easy access and manipulation of register values during instruction execution. These two array will output as a .bin file when the program encounters to the checkpoints.

### 3.3 Instruction Execution

Instructions are executed through a set of functions corresponding to each instruction type, which stores in the lib.py. The simulator uses a switch-case-like structure to determine the operation to be performed based on the opcode. Control flow instructions, arithmetic operations, and memory access instructions are implemented.

## 4 Implementation

#### 4.1 Instruction Execution Functions

Each MIPS instruction has a corresponding function implemented in Python. These functions emulate the behavior of the MIPS instructions, updating registers and memory accordingly. Debugging statements have been included to print the details of each instruction execution.

## 4.2 System Calls

The simulator comprehensively supports MIPS system calls, enabling interaction with the simulated environment. The implemented system calls cover a range of functionalities, such as reading and writing integers, strings, and characters. Additionally, the simulator supports file operations, including opening and closing files, as well as program termination.

### File Operations

For the file opening syscall <code>\_open</code>, the filename is obtained from register <code>\_a0</code>, and the Python <code>os.open</code> function is employed to acquire a file descriptor, which is stored in the register <code>\_a0</code>. To read from a file <code>\_read</code>, the file descriptor, buffer address, and length are fetched from specific registers. Subsequently, the program uses <code>os.read</code> to retrieve data from the file and stores it in the designated memory buffer. For writing to a file <code>\_write</code>, the file descriptor, buffer address, and length are similarly extracted from registers. The program then reads data from memory and utilizes <code>os.write</code> to write it to the file. Closing a file <code>\_close</code> involves retrieving the file descriptor and using <code>os.close</code> to close the file. In summary, these operations make use of Python's file handling functions, facilitating interaction with the simulated environment through system calls.

#### Print

For printing integers: \_print\_int, the value is obtained from register \_a0 and directly printed. String printing: \_print\_string extracts the address of the string from register \_a0, and the characters are retrieved from memory and printed until a null terminator is encountered. Character printing: \_print\_char obtains the character from register \_a0 and prints it.

## 4.3 Checkpoints

During the process of executing the instructions, the simulator will check the the current line to the checkpoints. If the current line is the checkpoint, the simulator will output the memory and register information to the .bin file.

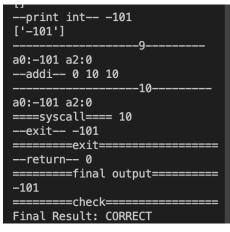
In order to debugging the program and make the registers information more readable, I write reg.py to convert the .bin file to the a .txt file, which contains all the registers' value and it makes me easier to check the current registers' value with the correct value.

Here is one of the test shell files I wrote to test the program:

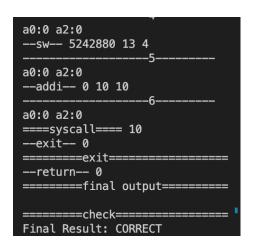
```
1 cd "/home/parallels/toby_dev/csc3050_toby/CSC3050_P2"
2 clear
3 python3 simulator.py Example_test_cases/many/many.asm
     Example_test_cases/many/many.txt Example_test_cases/many/
     many_checkpts.txt Example_test_cases/many/many.in many.out
4
5 values = (0 23 97)
6
7 for a in "${values[@]}"
8 do
      cmp register_$a.bin Example_test_cases/many/correct_dump/
     register_$a.bin
      if [ $? -eq 1 ]
10
11
      then
          hexdump register_$a.bin > register_$a.txt
12
          hexdump Example_test_cases/many/correct_dump/register_$a.
13
     bin > correct_register_$a.txt
          python3 reg.py Example_test_cases/many/correct_dump/
     register_$a.bin register_$a.bin $a
      cmp true_reg_$a.txt my_$a.txt
15
```

```
fi
16
17
     cmp memory_$a.bin Example_test_cases/many/correct_dump/
     memory_$a.bin
     if [ $? -eq 1 ]
19
     then
          hexdump memory_$a.bin > memory_$a.txt
21
          hexdump Example_test_cases/many/correct_dump/memory_$a.bin
     > correct_memory_$a.txt
23
24 done
26 cmp many.out Example_test_cases/many/many_correct.out
27 if [ $? -eq 1 ]
28 then
     echo "Final Result: FAIL"
29
30 else
  echo "Final Result: CORRECT"
32 fi
```

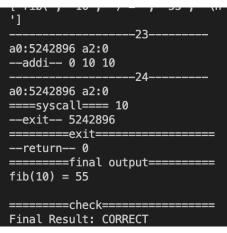
# 5 Program Output



(a) a-plus-b



(c) lw\_sw



(b) fib

(d) many

(e) memcpy

Figure 1: Program Output