

Ceres

Digilent Inc.

Digilent Ceres Board

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Engineer: NEA

TITLE: Ceres

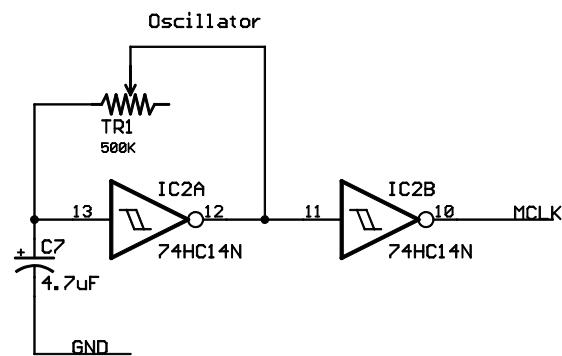
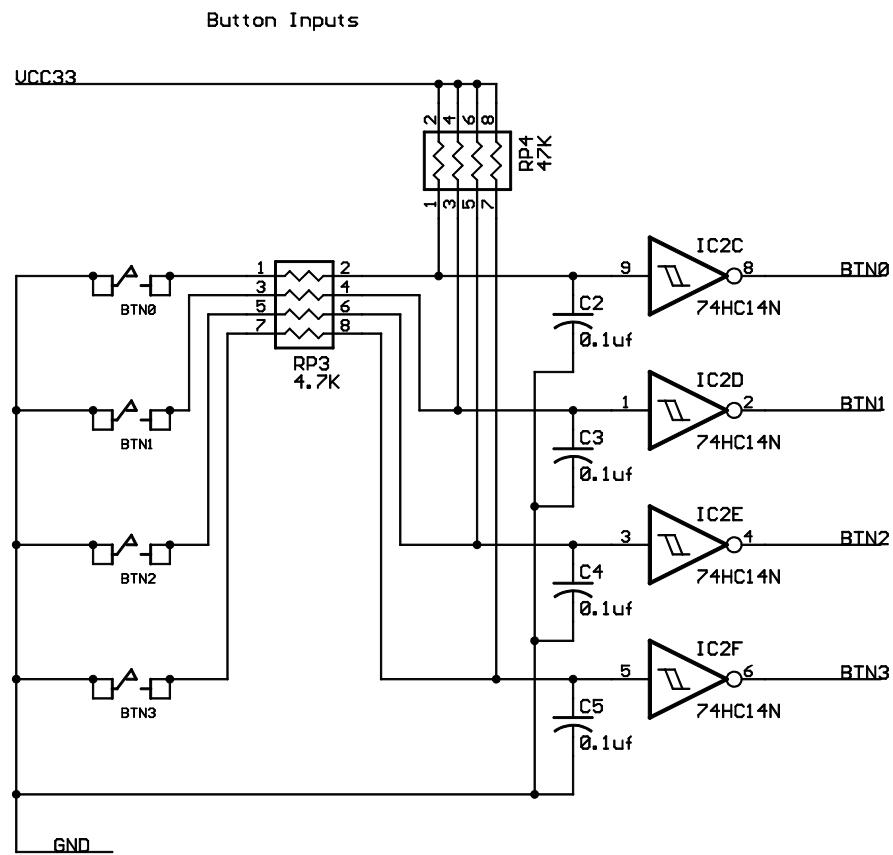
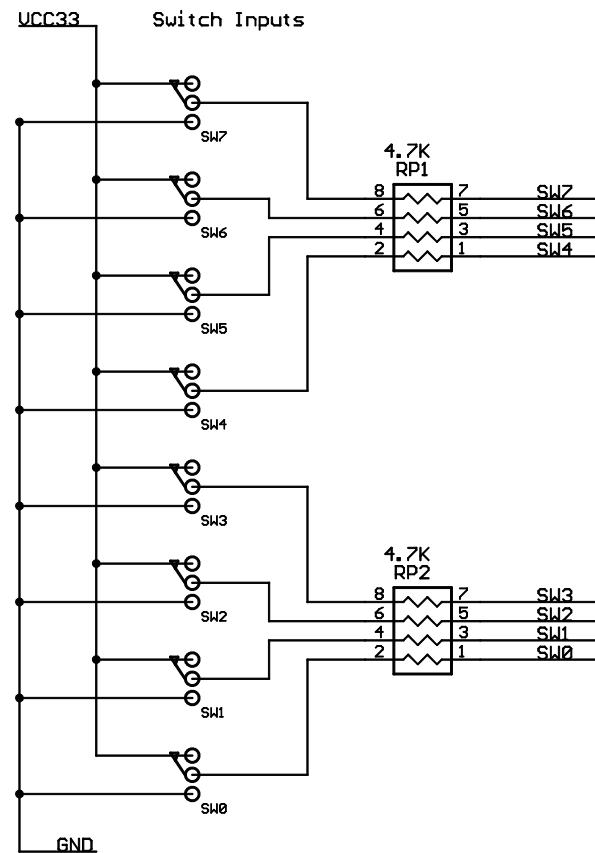
Author: GMA

Document Number: 500-048

Rev: C

Release Date: 3/31/2004

Sheet: 1/3



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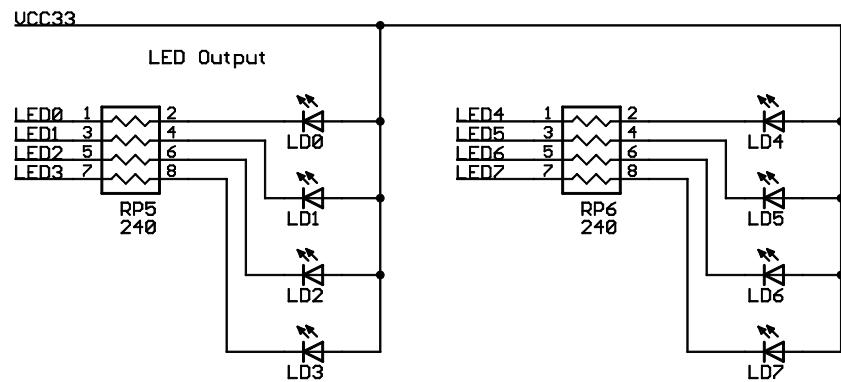
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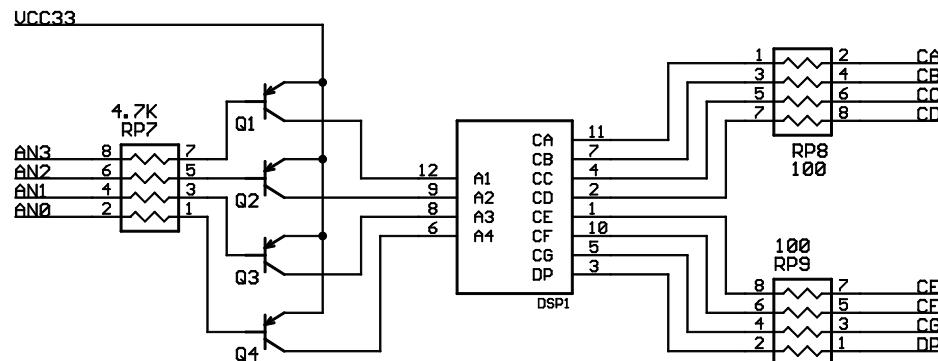


Interface Connector

1	GND
2	UII
3	UCC33
4	
5	SW7
6	
7	SW6
8	
9	SW5
10	BTN3
11	SW4
12	BTN2
13	SW3
14	BTN1
15	SW2
16	BTN0
17	SW1
18	DP
19	SW0
20	AN0
21	
22	
23	
24	
25	AN1
26	
27	AN2
28	
29	LED7
30	
31	AN3
32	
33	LED6
34	
35	CG
36	
37	LED5
38	
39	CF
40	

J1

Seven Segment Displays



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