

Elec Project Preliminary Work

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- I assigned user inputs as 3-bit vector:
 $100 \rightarrow \text{rock}$, $010 \rightarrow \text{paper}$, $001 \rightarrow \text{scissors}$
- FPGA board's input as integers up to 2:
 $0 \rightarrow \text{scissors}$, $1 \rightarrow \text{rock}$, $2 \rightarrow \text{paper}$
- The conditions are also assigned as 3-bit vector:
 $010 \rightarrow \text{draw}$, $100 \rightarrow \text{user win}$, $001 \rightarrow \text{FPGA win}$

Truth Table

User	FPGA	Result
100	0	100
100	1	010
100	2	001
010	0	001
010	1	100
010	2	010
001	0	010
001	1	001
001	2	100

