```
0B3C  BC  PHI   RC ;RC.1=0C  (page address work area)
  3D  F8  LDI
  3E  24
  3F  AC  PLO   RC ;RC=21 byte work area @ 0C24

0B40  F8  LDI
  41  05
  42  AD  PLO   RD ;RD.0=05 loop count
  43  4A  LDA   RA ;D=M(R(A))  (get card)
  44  FA  ANI      ;Strip suit by "AND"ing with 0F byte
  45  0F
  46  F9  ORI
  47  30           ;Combine with 30 for ASCII 3N via "OR" function
  48  5C  STR   RC ;Put in work area
  49  1C  INC   RC ;Advance pointer in work area
  4A  F8  LDI
  4B  20
  4C  5C  STR   RC ;Store an ASCII space (20 hex)
  4D  1C  INC   RC ;Advance pointer in work area
  4E  2D  DEC   RD ;Loop - 01
  4F  8D  GLO   RD

0B50  3A  BNZ
  51  43           ;Loop till all 5 card types done to 0B43
  52  5C  STR   RC ;M(R(C))=00 (null byte needed by messager)
  53  1C  INC   RC ;Advance pointer in work area

                        (SUITS)

0B54  F8  LDI
  55  05
  56  AD  PLO   RD ;RD=05  Loop count
  57  8F  GLO   RF
  58  AA  PLO   RA ;RA.0=RF.0 to reset to top card in hand
  59  4A  LDA   RA ;D=M(R(A))  (get card)
  5A  F6  SHR      ;Shift right x 4 for suit only  (four MSB's)
  5B  F6  SHR      ;   "      "     "      "
  5C  F6  SHR      ;   "      "     "      "
  5D  F6  SHR      ;   "      "     "      "
  5E  5C  STR   RC ;Store in work area (ASCII conversion="ON" byte)
  5F  1C  INC   RC ;Advance pointer in work area

0B60  F8  LDI
  61  20
  62  5C  STR   RC ;Store an ASCII space (20 hex)
  63  1C  INC   RC ;Advance pointer in work area
  64  2D  DEC   RD ;Loop count - 01
  65  8D  GLO   RD
  66  3A  BNZ      ;If loop count ≠ 00 yet, branch to 0B59
  67  59               (decode 5 card suits)
  68  5C  STR   RC ;M(R(C))=00 (null stop byte for messager)
  69  D4  SEP   R4 ;Return control to Chip-8 Interpreter
```