

Chip-8 instruction of the addressing type.

- c) For any and all arguments, there must be one and only one identical label.
- d) Arguments may be repeated as many times as needed to allow multiple calls to subroutines, etc.

## I C. RMKK, LINKING

### RMKK

Most assemblers contain pseudo instructions whose only purpose in life is to instruct the assembler to perform a particular operation.

Chip-8 Assembler-3 has one pseudo instruction, the RMKK. It is usually preceded by a label and is never followed by an argument as it is not of the addressing type. (In fact it is not really an instruction at all, and never becomes a part of the final Chip-8 program in assembled form.)

Typing RMKK -- beginning in position #7 of the line as for normal data and instructions -- will cause the assembler to reserve KK bytes of memory at that position in the program. (Hence the name RM = Reserve Memory) The KK is replaced by the number of bytes needed (in