ERASE CHARACTER SET - SELECTED KEY IS C (E A)

```
0547
      F8
           LDI
                        ;Load RD (Utility
  48
      07
                        ;Register) with
  49
           PHI
      BD
                  RD
                        ;Last address of
  4A
      F8
           LDI
                        ;Character Set
  4B
      FF
                                      **
                              11
  4C
      AD
           PLO
                  RD
  4D
      ED
           SEX
                  RD
                        ;X=RD
  4E
      F8
           LDI
                        ;Load D with
  4F
      00
                        ;00 - for erasing
0550
      73
           STXD
                        ;Store via X (RD) and decrement
  51
                  RD
                        Get high byte of RD
      9D
           GHI
  52
      FB
           XRI
                        ;Exclusive OR it with
      05
                        ;05 to test if done
                        ;Branch if RD \neq 05 (page above first ;character set )
       3A
           BNZ
                        ;Branch to 054E - loop until done
      4E
                  R4
      D4
           SEP
                        ;Return to CHIP-8 program
```

DEPOSIT SAMPLE CHARACTER IN MEMORY

```
0557
      E2
           SEX
                       ;X=R2 (Stack pointer)
                 R2
  58
      22
                 R2
           DEC
                       ; Point to free location
      F8
           LDI
                       ;Load Utility Register RF
  5Á
      04
                       ;With loop count of 04
  5B
      ΑF
           PLO
                 RF
                       ;And also put in RD (RD.1)
  5C
5D
      BD
           PHI
                 RD
                       ;Which will point to the storage
      F8
           LDI
                       ;Area holding ASCII reference
  5E
      E6
                       ;Bit pattern address
  5F
      AD
           PLO
                 RD
                                               - RD=04E6
0560
      4D
           LDA
                 RD
                       Get first part of address and advance pointer
  61
      BC
           PHI
                 RC
                       ;Store in RC.1
  62
      OD
           LDN
                 RD
                       ;Get second half of address
  63
      AC
           PLO
                       ;Store in RC.O (RC is indexed
                 RC
                       ; to correct address)
  64
      9B
           GHI
                 RB
                       ;Display page
  65
      BD
           PHI
                 RD
                       Store in RD.1 (High order sample
                       ;character address)
  66
      F8
           LDI
                       ;Load Low order Sample
  67
68
      93
                       ;Character address
      AD
           PLO
                 RD
                       ;Into RD.0
                                    (RD.0=0Y93)
  69
      0D
           LDN
                 RD
                       Get a row of the sample
  6А
      FE
           SHL
                       ;Shift left (for packing)
  6в
      FE
           SHL
  6C
      FE
           SHL
  6D
      FE
           SHL
  6E
      52,
           STR
                 R2
                       ; Push stack
      8D
  6F
           GLO
                 RD
                       Get RD.0 and
0570
      FC
           ADI
                       ;Add 08 for next
  71
      80
  72
           PLO
      AD
                 RD
                       ; Put adjusted value back in RD.0
```