```
0318   A7   PLO  R7    ;Restore R7.0
 19    F0   LDX        ;Pop R7.1
 1A    B7   PHI  R7    ;Restore R7.1
 1B    30   BN         ;Branch to exit
 1C    10

                       CLEAR MEMORY

031D   F8   LDI
 1E    0F
 1F    BF   PHI  RF    ;Set RF = 0FFF  Last byte to clear
0320   F8   LDI
 21    FF
 22    AF   PLO  RF
 23    EF   SEX  F     ;X = F
 24    F8   LDI
 25    00
 26    73   STXD       ;Store 00 via X (RF) & decrement
 27    9F   GHI  RF
 28    FB   XRI
 29    09
 2A    3A   BNZ        ;Loop until 00's stored to 0A00
 2B    24
 2C    D5   SEP  R5    ;Return

                       ADDRESS ENTRY

032D   F8   LDI
 2E    04
 2F    AA   PLO  RA    ;RA.0 = Loop Counter of 04
0330   9B   GHI  RB
 31    B6   PHI  R6
 32    F8   LDI
 33    00
 34    A6   PLO  R6    ;R6 = First display byte
 35    DC   SEP  RC    ;Call Key Scan - Get one entry-branches here
 36    AE   PLO  RE    ;Put in RE.0 for display
 37    F8   LDI
 38    04
 39    AF   PLO  RF    ;RF.0 = Loop Count of 04
 3A    89   GLO  R9
 3B    FE   SHL        ;Shift R9 left x 4
 3C    A9   PLO  R9
 3D    99   GHI  R9    ;For double precision
 3E    7E   SHLL
 3F    B9   PHI  R9    ;Prepare for next
0340   2F   DEC  RF
 41    8F   GLO  RF    ;Nibble of 4
 42    3A   BNZ
```