```
08DC          5245
  DE          4553

08E0          0053        -- ASCII -- "STRT" (straight)
  E2          5452
  E4          5400
  E6          464C        -- ASCII -- "FLUSH"
  E8          5553
  EA          4800
  EC          4655        -- ASCII -- "FULHSE" (full house)
  EE          4C48

08F0          5345
  F2          0046        -- ASCII -- "FOURS"
  F4          4F55
  F6          5253
  F8          0053        -- ASCII -- "STRTFL" (straight flush)
  FA          5452
  FC          5446
  FE          4C00
```

MLS - HIGH SUB (V1 V2 V3 V4 = 00's OR HIGHEST VALUE)

```
0900  22  DEC   R2 ;Stack free
  01  F8  LDI
  02  F1
  03  A7  PLO   R7 ;R7.0=F1 (points to Chip-8 V1 value)
  04  87  GLO   R7          (loop here on M(R(7))>M(R(6))
  05  A6  PLO   R6 ;R6.0=R7.0
  06  06  LDN   R6 ;D=M(R(6))
  07  52  STR   R2 ;Push for comparing
  08  17  INC   R7 ;R7=R7+1  (loop here on M(R(7))≤M(R(6))
  09  87  GLO   R7
  0A  FB  XRI      ;Test if R7 past V4 value
  0B  F5
  0C  32  BZ       ;If so, exit the loop
  0D  14
  0E  07  LDN   R7 ;D=M(R(7))
  0F  F5  SD       ;Subtract M(R(x))-D ( M(R(6)) - M(R(7)) )

0910  3B  BM       ;If neg, (i.e. M(R(7))>M(R(6)) ) branch to
  11  04                    reset R(6)
  12  30  BR       ;Else branch to continue the search
  13  08
  14  F8  LDI      ;Begin set all others = 00
  15  F1           ;D=F1 (Chip-8's V1 value)
```