INITIALIZATION

```
0000   91   GHI  R1    ;R1 points to last RAM page
  01   FF   SMI        ;Subtract 3 to point to
  02   03              ;Last four pages
  03   BB   PHI  RB    ;RB is display page pointer
  04   F8   LDI
  05   03
  06   B1   PHI  R1    ;R1.1 = 03
  07   B4   PHI  R4    ;R4.1 = 03
  08   B5   PHI  R5    ;R5.1 = 03
  09   F8   LDI
  0A   FF
  0B   A2   PLO  R2    ;R2 = 00  -Stack (see ML 0016)
  0C   F8   LDI
  0D   02
  0E   A1   PLO  R1    ;R1 = 0302  -Interrupt Routine
  0F   F8   LDI
0010   13
  11   A4   PLO  R4    ;R4 = 0313  -Call Routine
  12   F8   LDI
  13   23
  14   A5   PLO  R5    ;R5 = 0323  -Return Routine
  15   90   GHI  R0    ;(00) R0 still PC
  16   B2   PHI  R2    ;R2 = 00FF  -Stack Pointer
  17   A9   PLO  R9    ;R9.0 = 00
  18   B3   PHI  R3    ;R3.1 = 00  -prepare for use as PC
  19   F8   LDI
  1A   04
  1B   B9   PHI  R9    ;R9 = 0400  -Data Page Pointer
  1C   F8   LDI        ;Load address into
  1D   20              ;R3 to prepare for use as PC
  1E   A3   PLO  R3
  1F   D3   SEP  R3    ;R3 is PC/R0 free for DMA
0020   E2   SEX  2     ;X = 2
  21   69   INP        ;Video on
  22   D4   SEP  R4
  23   03              ;Call Display Memory Page
  24   2F
  25   D4   SEP  R4
  26   03              ;Call Home Cursor
  27   53
```

MAIN LOOP - CHARACTER ENTRY

```
0028   F8   LDI        ;Begin Main Loop
  29   81
  2A   BC   PHI  RC
  2B   F8   LDI
```