```
0573
      0D
           LDN
                 RD
                       ;Get a row of the sample
                       ;"OR" it with top of stack (packs two
      F1
           OR
      5C
           STR
                 RC
                       ;sample rows into one byte. Store in RC)
                 RC
      1C
           INC
                       ;RC+1 - next storage position
  77
78
      8D
           GLO
                 RD
                       ;Add 8
      FC
           ADI
                       ;To RD for
  79
      80
                       ;Another row
  7A
      AD
           PLO
                 RD
                       ; Of sample character.
                       ;Decrement loop count (-1)
  7B
      2F
           DEC
                 RF
  7C
      8F
                 RF
           GLO
                       ;Get RF.O to
  7D
      3A
           BNZ
                       ;Test if done.
                                         Loop until RF=00
      69
  7E
                       ;Branch to 0569
  7F
      12
                       ;Reset Stack Pointer - prepare to return
                 R2
           INC
0580
      D4
           SEP
                 R4
                       ;Return to CHIP-8 control
                   DISPLAY CHARACTER SET - KEY C (D)
0581
      E2
           SEX
                 R2
                       ;X=R2 (stack pointer)
  82
      22
           DEC
                 R2
                       ;Point to free location
  83
      F8
           LDI
                       ;Load address of
  84
      06
                       Character set into
  85
86
      BC
           PHI
                 RC
                       ;RC
                             11
                                     "
      F8
           LDI
  87
      00
  88
                                       (RC=0600 - base address)
      AC
           PLO
                 RC
  89
      AD
           PLO
                 RD
                       ;RD=Display page cursor
  8A
      9B
           GHI
                 RB
                         **
                                             **
  8B
      BD
                 RD
                                                 (RD=0Y00)
           PHI
  8C
      93
                 R3
                                                   Load high address
           GHI
                       ;R3 is program counter.
  8D
      BE
                 RE
           PHI
                       ;Of minor subroutines used by this subroutine
                       ; With RE as program counter.
  8E
      F8
           LDI
                        Load loop...part one begins....left character
  8F
      04
                       ;Count of four
0590
      ΑF
           PLO
                 RF
                       ;Into RF.0
  91
      OC.
           LDN
                 RC
                       Get bit pattern in memory
                        ;"AND" it with FO
  92
      FΑ
           ANI
  93
94
      FO
                       ;For MSB's
      5D
           STR
                 RD
                       ;Store @ Display page cursor
  95
96
      F8
           LDI
                       ;Load address of
      E9
                       ;Next Line Subroutine
  97
98
      ΑE
           PL0
                 RE
                       ;Put it in RE.O
           SEP
                 RE
      DE
                       ;And call the subroutine
  99
      4C
           LDA
                 RC
                       Get the same bit pattern (packed)
  9Å
      FE
           SHL
                       ;Shift Left for LSB's
  9В
      FE
           SHL
                       ;(for next row)
  9C
      FE
           SHL
                        ;To appear under
  9D
      FE
           SHL
                        ;The previous row
  9E
      5D
           STR
                 RD
                        ;Store @ cursor
  9F
      DE
           SEP
                 RE
                        ;Call Next-line Subroutine
05A0
      2F
           DEC
                 RF
                       ;Decrement loop counter (-01)
      8F
           GLO
                  RF
                        Get low RF to test if done
  Α1
  A2
       3A
           BNZ
                        ;Loop to 0591
       91
  A3
                        ;RF=00
```