

0270	FE	SHL		;Shift left for LSB's
0271	FE	SHL		; " " " "
0272	FE	SHL		; " " " "
0273	5A	STR	RA	;
0274	2A	DEC	RA	;
0275	0C	LDN	RC	;
0276	FA	ANI		;And with F0
0277	F0			;For MSB's
0278	5A	STR	RA	;
0279	2C	DEC	RC	;Next bits
027A	2F	DEC	RF	;Loop count-1
027B	8F	GLO	RF	;
027C	3A	BNZ		;
027D	6D			;Loop until done - bit pattern unpacked @ M(R(A))
027E	B3	PHI	R3	;(00-R3.1)
027F	F8	LDI		;
0280	70			;Address DXYN routine
0281	A3	PLO	R3	;R3 is PC for Display routine
0282	D3	SEP	R3	;Do display routine
0283	E2	SEX	R2	;Reset stack pointer to 2 on return
0284	25	DEC	R5	;Point back to second half of DXYN Instruction
0285	72	LDXA		;Pop the saved RA.0
0286	AA	PLO	RA	;
0287	F0	LDX		;Pop the saved RA.1
0288	BA	PHI	RA	;Restore RA value
0289	1A	INC	RA	;RA points to next ASCII code
028A	F8	LDI		;
028B	F5			;
028C	A6	PLO	R6	;R6 points to VX again
028D	A7	PLO	R7	;
028E	17	INC	R7	;R7 points to VY again (or R6 + 1)
028F	06	LDN	R6	;Get VX value
0290	FC	ADI		;Add 04
0291	04			;
0292	56	STR	R6	;And store via R6
0293	30	BN		;Loop until done - all
0294	53			;Characters displayed
0295	15	INC	R5	;Point to next CHIP-8 instruction
0296	9F	GHI	RF	;Saved value of VX
0297	56	STR	R6	;Replace in CHIP-8 variable storage area
0298	12	INC	R2	;To counter the last STXD instruction
0299	F8	LDI		;Load Address
029A	01			;
029B	B3	PHI	R3	;Of return sub @ 01F2
029C	F8	LDI		;
029D	F2			;Into R3
029E	A3	PLO	R3	;And call to begin
029F	D3	SEP	R3	;Return