

Operation,Platform,Estimated Cycles,Estimated Time (μ s),Estimated RAM Use (Bytes),Notes

Encapsulation,Cortex-M0+,150000,9375,2000,Includes 28 transitions, SHA3

Encapsulation,Cortex-M4F,30000,375,2000,Includes 28 transitions, SHA3

Encapsulation,RISC-V RV64,20000,167,2000,Includes 28 transitions, SHA3

Decapsulation,Cortex-M0+,130000,8125,1800,Includes 28 transitions, SHA3

Decapsulation,Cortex-M4F,26000,325,1800,Includes 28 transitions, SHA3

Decapsulation,RISC-V RV64,18000,150,1800,Includes 28 transitions, SHA3

SHA3-256 (30-byte input),Cortex-M0+,40000,2500,500,Approximate cost

SHA3-256 (30-byte input),Cortex-M4F,8000,100,500,Approximate cost

SHA3-256 (30-byte input),RISC-V RV64,5000,42,500,Approximate cost

Transition (1 symbol δ),Cortex-M0+,1000,63,8,Single table lookup

Transition (1 symbol δ),Cortex-M4F,200,3,8,Single table lookup

Transition (1 symbol δ),RISC-V RV64,150,1,8,Single table lookup

Mutation (single row in transition table),Cortex-M0+,20000,1250,256,Full row write (approx)

Mutation (single row in transition table),Cortex-M4F,4000,50,256,Full row write (approx)

Mutation (single row in transition table),RISC-V RV64,3000,25,256,Full row write (approx)