{EchoPulse: A Lightweight Symbolic Key Encapsulation Mechanism for Post-Quantum Environments}
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\EchoPulse

The looming threat of quantum computers necessitates the development of post-quantum secure cryptographic primitives. While lattice-based Key Encapsulation Mechanisms (KEMs) like Kyber and Dilithium are leading candidates, their inherent algebraic structures and often substantial RAM requirements pose challenges for deployment in resource-constrained Internet of Things (IoT) devices. This paper introduces EchoPulse, a novel lightweight KEM based on a symbolic transition system. EchoPulse leverages deterministic traversals of a dynamically mutating state transition graph, driven by secret and public symbolic paths, to establish a shared secret. The encapsulation process involves a random symbolic payload guiding the traversal to a final state, which is then hashed with the payload to derive the shared key. We present a preliminary architecture, discuss security considerations focusing on resistance against pattern inference and replay attacks through graph mutation, and provide benchmark estimates indicating its potential for low RAM usage and competitive performance on embedded platforms. Our results suggest that symbolic KEMs offer a promising alternative for post-quantum security in resource-limited environments.

\end{abstract}

\section{Introduction}

The anticipated advent of quantum computers poses a significant threat to currently deployed public-key cryptosystems, most of which rely on the computational hardness of number-theoretic problems that are believed to be efficiently solvable by quantum algorithms. This has spurred extensive research into post-quantum cryptography (PQC), aiming to develop cryptographic primitives resistant to these emerging threats. The National Institute of Standards and Technology (NIST) has been conducting a standardization process for PQC algorithms, with lattice-based KEMs like Kyber and Dilithium emerging as frontrunners.

However, while offering strong security guarantees, these lattice-based schemes often exhibit substantial memory footprints and complex algebraic operations, which can be limiting factors for

their adoption in resource-constrained environments such as the vast and diverse ecosystem of Internet of Things (IoT) devices. These devices typically have limited RAM, processing power, and energy budgets, making the integration of heavy cryptographic libraries challenging.

This paper explores an alternative approach to post-quantum secure key exchange by introducing EchoPulse, a lightweight KEM based on a symbolic transition system. Instead of relying on algebraic structures over large rings or modules, EchoPulse utilizes deterministic traversals of a dynamically mutating state transition graph. The secret and public keys are represented as symbolic paths within this graph. The key exchange process involves the sender using a random symbolic payload to guide a traversal based on the receiver's public key, leading to a final state that, when combined with the payload and hashed, yields the shared secret.

Our research question is: can a KEM based on symbolic structures and deterministic graph transitions offer a viable and lightweight alternative for post-quantum secure key exchange, particularly in resource-constrained environments where algebraic KEMs might face limitations? This paper presents the foundational architecture of EchoPulse, discusses its potential security properties, and provides preliminary benchmark estimates to evaluate its resource efficiency.

\section{Related Work}

The NIST Post-Quantum Cryptography Standardization Process has highlighted several promising candidates for post-quantum KEMs. Among the finalists, lattice-based schemes like Kyber \ cite{kyber} and Dilithium \cite{dilithium} have shown strong security properties and performance characteristics. Kyber, based on the Module-LWE problem, offers relatively compact key and ciphertext sizes and efficient operations. Dilithium, a signature scheme also based on lattices (Module-LWR), has been adapted for key exchange. Another notable lattice-based KEM is FrodoKEM \cite{frodokem}, based on the Learning With Errors (LWE) problem, which prioritizes provable security.

Other PQC KEM candidates include code-based schemes like BIKE \cite{bike}, which relies on the hardness of decoding random binary codes. While offering different security assumptions, these schemes, particularly the lattice-based ones, often involve operations over large algebraic structures (e.g., polynomial rings) and can exhibit significant RAM usage, especially during key generation and encapsulation/decapsulation processes. The storage requirements for keys and ciphertexts can also be substantial for certain applications.

EchoPulse departs from these algebraic foundations by employing a purely symbolic structure. The security of EchoPulse relies on the complexity of traversing and predicting paths within a dynamically changing graph, rather than the hardness of mathematical problems in abstract algebra. This symbolic approach aims to offer a more lightweight alternative, particularly in terms of RAM usage, which is a critical constraint for many embedded systems. While lacking a direct reduction to a well-established number-theoretic hard problem, EchoPulse leverages deterministic graph logic and dynamic mutation to achieve security against various attack vectors.

\section{Architecture}

EchoPulse is a Key Encapsulation Mechanism built upon a symbolic transition system. The core components of this system are defined as follows:

- \* \*\*Symbol Set ( $\S$ Sigma $\S$ ):\*\* A finite set of symbols, in this instantiation,  $\S$ Sigma =  $\S$ 0x00, 0x01, ..., 0xFF $\S$  (8-bit bytes).
- \* \*\*State Transition Graph (\$G(V, E)\$):\*\* A directed graph where \$V\$ is a finite set of states (represented as 16-bit unsigned integers) and \$E \subseteq V \times \Sigma \times V\$ is the set of labeled edges representing state transitions. The transition function \$\delta: V \times \Sigma \ rightarrow V\$ defines the next state given a current state and an input symbol. For implementation efficiency, the transition function can be structured as a table `transitions: [[State; 16]; 256]`, where the input symbol is reduced modulo 16 to select a column within the row corresponding to the current state.
- \* \*\*Secret Key (SK):\*\* A `SymbolPath`, represented as a sequence of \$L\$ symbols from \$\Sigma\$.
- \* \*\*Public Key (PK):\*\* Also a `SymbolPath` of length \$L\$, derived from the secret key through a series of graph traversals.
- \* \*\*Random Payload (r):\*\* A randomly generated `SymbolPath` of a fixed length (e.g., 28 symbols) used during the encapsulation process.
- \*\*Kev Generation:\*\*
- 1. A secret key <span class="math-inline">SK</span> is generated as a random sequence of <span class="math-inline">L</span> symbols.
- 2. The secret key <span class="math-inline">SK</span> is used to traverse the graph <span class="math-inline">G</span> starting from a fixed initial state <span class="math-inline">v\\_0</span>, resulting in a private terminal state <span class="math-inline">v\\_\[\{\priv\}</span>.

3. From <span class="math-inline">v\\_\{priv\}</span>, a public key <span class="math-inline">PK</span> is generated as a new forward-resolvable symbolic path of length <span class="math-inline">L</span> within <span class="math-inline">G</span>.

## \*\*Encapsulation:\*\*

- 1. The sender obtains the receiver's public key <span class="math-inline">PK</span>.
- 2. The sender resolves the public state <span class="math-inline">v\\_\{pub\}</span> by applying the <span class="math-inline">PK</span> symbol sequence to the graph <span class="math-inline">G</span> starting from <span class="math-inline">v\\_0</span>.
- 3. The sender generates a random symbol sequence <span class="math-inline">r</span> of a fixed length.
- 5. The shared secret <span class="math-inline">K</span> is derived by hashing the little-endian encoding of <span class="math-inline">v\\_\{enc\}</span> concatenated with the random payload <span class="math-inline">r</span>: <span class="math-inline">K \= H\(\\text\{encode\}\(v\\_\ \enc\}\)\|\| r\)</span>, where <span class="math-inline">H</span> is a cryptographic hash function (e.g., SHA3-256).
- 6. The ciphertext is the random payload <span class="math-inline">r</span>.

## \*\*Decapsulation:\*\*

- 1. The receiver receives the ciphertext <span class="math-inline">r</span>.
- 2. The receiver resolves the private state <span class="math-inline">v\\_\{priv\}</span> by applying their secret key <span class="math-inline">SK</span> to the graph <span class="math-inline">G</span> starting from <span class="math-inline">v\ 0</span>.
- 3. Starting from <span class="math-inline">v\\_\{priv\}</span>, the receiver applies each symbol of <span class="math-inline">r</span> sequentially to traverse the graph <span class="math-inline">r</span>, reaching a final decapsulation state <span class="math-inline">v\\_\{dec\}</span>.
- 4. The receiver computes the shared secret <span class="math-inline">K'</span> by hashing the little-endian encoding of <span class="math-inline">v\\_\{dec\}</span> concatenated with the received random payload <span class="math-inline">r</span>: <span class="math-inline">K' \= H\(\\ text\{encode\}\(v\\_\{dec\}\)\|\| r\)</span>.
- 5. If <span class="math-inline">K' \= K</span>, the decapsulation is successful.

\*\*Graph Mutation (<span class="math-inline">\\mu\(V, t\)</span>):\*\*

The state transition graph <span class="math-inline">G\(V, E\)</span> is not static but undergoes periodic mutation based on a function <span class="math-inline">\\mu</span> that takes the current graph <span class="math-inline">V</span> and a temporal parameter <span class="math-inline">t</span> (e.g., a session counter) as input. The mutation can involve overwriting rows of the transition table or, less frequently, regenerating the entire table based on a seed and the session counter. The mutation process aims to introduce dynamism and prevent long-term pattern analysis by adversaries.

\section{Security Considerations}

The security of EchoPulse relies on several factors, including the size and connectivity of the state transition graph, the length of the symbolic paths (SK, PK, r), the properties of the hash function, and the dynamic mutation of the graph.

\*\*IND-CCA Security Model:\*\* While a formal security proof is part of future work, EchoPulse aims to achieve indistinguishability under chosen-ciphertext attacks (IND-CCA). An adversary attempting to distinguish between ciphertexts would need to predict the final state <span class="math-inline">v\\_\{enc\}</span> reached by the random payload <span class="math-inline">r</span> after traversing the graph based on the public key <span class="math-inline">PK</span>. The complexity of this task is intended to be high due to the non-linearity introduced by the graph transitions and the randomness of <span class="math-inline">r</span>. Chosen-ciphertext security would be addressed through mechanisms that prevent the adversary from gaining useful information by querying a decapsulation oracle.

\*\*Resistance to Pattern Inference and AI Path Recovery:\*\* The symbolic nature of the keys and the random payload <span class="math-inline">r</span> aims to resist attacks based on statistical analysis of key bit patterns, which might be applicable to algebraic KEMs. Furthermore, the dynamic mutation of the graph is designed to thwart attempts by advanced adversaries, including those employing artificial intelligence techniques, to learn the underlying graph structure or predict future transition paths based on observed key exchanges.

\*\*Mutation as Replay and Hardening Function:\*\* The graph mutation mechanism serves a dual purpose. Firstly, by changing the graph state over time, it invalidates previously observed ciphertext-key pairs, mitigating the risk of replay attacks. An attacker replaying a ciphertext <span class="math-inline">r</span> in a future session with a mutated graph will likely lead to a different final state and

thus a different shared secret. Secondly, mutation acts as a hardening function, continuously evolving the cryptographic landscape and increasing the difficulty for an adversary to build a consistent model of the system.

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\section{Benchmark Results}
```

Preliminary benchmark estimates, based on the analysis in Module C, suggest the following performance characteristics for core EchoPulse operations on different embedded platforms:

```
\begin{table}[h!]
  \centering
  \begin{tabular}{||||r|r|}
    \hline
                                & Estimated Time (µs) & Estimated RAM Use (Bytes) \\
    Operation
                   & Platform
    \hline
    Encapsulation
                    & Cortex-M0+ & <span class="math-inline">\\sim 9375</span>
                                                                                      & <span
class="math-inline">\\sim 2000</span>
                                            //
    Encapsulation
                    & Cortex-M4F & <span class="math-inline">\\sim 375</span>
                                                                                     & <span
class="math-inline">\\sim 2000</span>
                                            //
                                 & <span class="math-inline">\\sim 167</span>
    Encapsulation
                    & RV64
                                                                                  & <span
class="math-inline">\\sim 2000</span>
                                            II
                     & Cortex-M0+ & <span class="math-inline">\\sim 8125</span>
    Decapsulation
                                                                                      & <span
class="math-inline">\\sim 1800</span>
                                            II
    Decapsulation
                     & Cortex-M4F & <span class="math-inline">\\sim 325</span>
                                                                                     & <span
class="math-inline">\\sim 1800</span>
                                            //
                                 & <span class="math-inline">\\sim 150</span>
    Decapsulation
                     & RV64
                                                                                  & <span
class="math-inline">\\sim 1800</span>
                                            //
    SHA3-256 (30 B) & Cortex-M0+ & <span class="math-inline">\\sim 2500</span>
                                                                                       &
<span class="math-inline">\\sim 500</span>
                                                  //
    SHA3-256 (30 B) & Cortex-M4F & <span class="math-inline">\\sim 100</span>
                                                                                      & <span
class="math-inline">\\sim 500</span>
                                            II
```

```
SHA3-256 (30 B) & RV64
                                  & <span class="math-inline">\\sim 42</span>
                                                                                   & <span
class="math-inline">\\sim 500</span>
                                            \parallel
    Mutation (Row)
                      & Cortex-M0+ & <span class="math-inline">\\sim 4</span>
                                                                                      & <span
class="math-inline">\\sim 32</span>
                                            //
    Mutation (Row)
                      & Cortex-M4F & <span class="math-inline">\\sim 0\.5</span>
                                                                                        &
<span class="math-inline">\\sim 32</span>
                                                  //
                                  & <span class="math-inline">\\sim 1</span>
    Mutation (Row)
                      & RV64
                                                                                   & <span
class="math-inline">\\sim 32</span>
                                            //
    \hline
    Total Estimated RAM & All Platforms &
                                                    & <span class="math-inline">\\sim
8420</span>
                    //
    \hline
  \end{tabular}
  \caption{Estimated Performance and RAM Usage of Core EchoPulse Operations}
  \label{tab:benchmarks}
\end{table}
These estimates suggest that EchoPulse can achieve reasonable performance on embedded
platforms while maintaining a relatively low RAM footprint of under 9 KB. The row-based mutation
operation exhibits minimal overhead.
\section{Comparison}
The following table provides a preliminary comparison of EchoPulse with leading lattice-based KEM
candidates:
\begin{table}[h!]
  \centering
  \begin{tabular}{|||c|c|c|}
    \hline
    KEM
             & Estimated RAM Usage & Payload Size (Bytes) & Encapsulation Time (µs, M4F) \\
```

```
\hline
    EchoPulse & <span class="math-inline">\\sim 8420</span>
                                                                 & 28
                                                                               & <span
class="math-inline">\\sim 375</span>
    Kyber512 & <span class="math-inline">\\sim 12</span> KB +
                                                                                 & <span
class="math-inline">\\sim 1700</span> - <span class="math-inline">\\sim 2500</span>
                                                                                    //
    FrodoKEM-640 & <span class="math-inline">\\sim 16</span> KB +
                                                                     & 976
                                                                                    & <span
class="math-inline">\\sim 8000</span> - <span class="math-inline">\\sim 13000</span> \\
    \hline
  \end{tabular}
  \caption{Preliminary Comparison of EchoPulse with Lattice-Based KEMs (Estimates)}
  \label{tab:comparison}
```

\*\*Note:\*\* The RAM usage for Kyber and FrodoKEM is a rough estimate and can vary based on implementation. The encapsulation times are based on reported figures for optimized implementations on similar Cortex-M4F platforms.

This comparison positions EchoPulse as a symbolic-first KEM that potentially offers significant advantages in terms of RAM usage and competitive performance, particularly in resource-constrained environments where the larger memory footprints and more complex algebraic operations of lattice-based schemes might be prohibitive. The significantly smaller payload size of EchoPulse is also noteworthy for bandwidth-limited applications.

\section{Discussion}

\end{table}

EchoPulse introduces a novel class of KEMs based on symbolic transition systems rather than traditional number-theoretic hard problems. This approach offers the potential for lightweight implementations, particularly in terms of RAM usage, making it attractive for embedded and IoT devices where resource constraints are paramount. The dynamic mutation of the state transition graph provides a unique security mechanism against long-term pattern analysis and replay attacks.

However, the security of EchoPulse currently lacks a formal reduction to a well-established cryptographic hardness assumption. Instead, its security relies on the deterministic and complex

nature of the graph transitions and the difficulty for an adversary to predict the outcomes of traversals, especially in the presence of ongoing mutation. Further research is needed to formally analyze the security properties of symbolic KEMs against various attack vectors.

Future work will focus on hardware optimization of EchoPulse for specific embedded platforms and exploring the potential for hybrid KEMs that combine the lightweight nature of symbolic systems with the formal security guarantees of algebraic approaches.

\section{Conclusion}

This paper has presented EchoPulse, a lightweight Key Encapsulation Mechanism based on a symbolic transition system. By leveraging deterministic graph traversals and dynamic mutation, EchoPulse offers a potentially RAM-efficient alternative for post-quantum secure key exchange, particularly suited for resource-constrained environments. Preliminary benchmark estimates suggest competitive performance on embedded platforms compared to leading lattice-based KEM candidates. While further formal security analysis is required, the symbolic approach introduces a new paradigm for post-quantum cryptography. We call upon the research community to further evaluate the potential of symbolic KEMs as a viable and lightweight solution for securing future communication in a post-quantum world.

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