OCT. 1987

DISSIPS PS-2 SERVICE NOTES

First Edition

PS-2

SPECIFICATIONS

: 9V DC (Battery 6F22 × 1, AC Adapter PSA series or RPW-7) **Power Source**

Current Draw : 60mA @ 9V DC Pitch Shift : -1 to +1 octave: 30ms to 2s **Delay Time**

: [Direct] 10Hz to 100kHz (±3 dB) Frequency Response

[Effect] 80Hz to 6KHz (±3 dB) : Less than -90 dBm (IHF-A)

Residual Noise Input Impedance

: $\mathsf{1M}\Omega$

Output Load Impedance More than $10k\Omega$

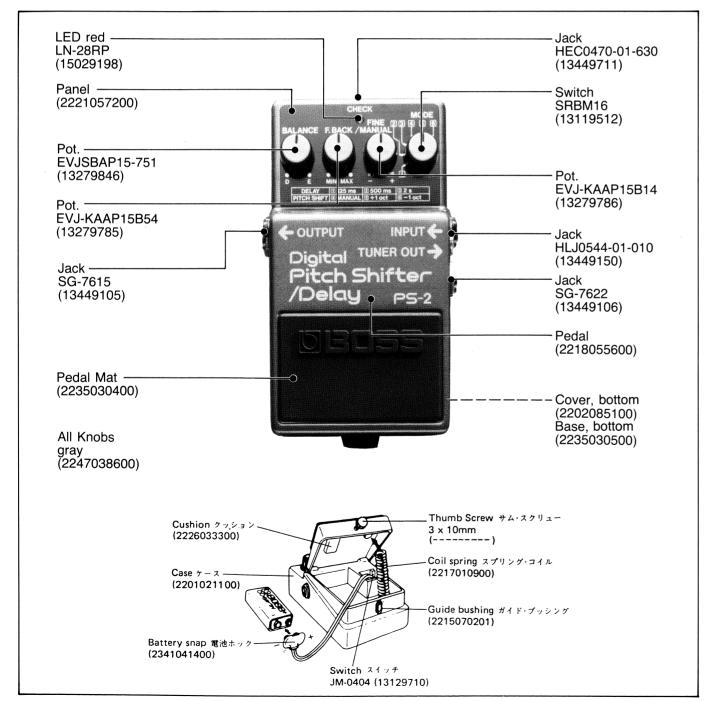
Dimensions

: 70 (W) \times 55 (H) 125 (D) mm

2-3/4 (W) \times 2-3/16 (H) \times 4-15/16 (D) in.

Weight

: 450g/1lb





Printed in Japan BD-2 (AX)

ADJUSTMENT

1 CLOCK FREQUENCY (VR5, VR4)

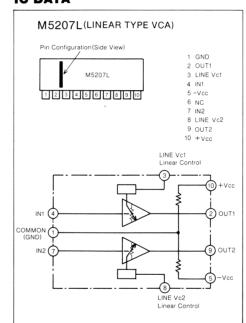
- 1-1 Connect a tuner (TU-12, TU-100 etc) or a frequency counter to TUNER OUT jack.
- 1-2 Turn MODE to 4.
- 1-3 Turn FINE/MANUAL full clockwise and adjast VR5 for C# (277.18Hz).
- 1-4 Turn FINE/MANUAL full counterclockwise and adjast VR4 for B (61.735Hz).

調整仕様

1. クロック調整(VR5、VR4)

- 1-1 TUNER OUTジャックにチューナー(TU-12、TU-100など)か周波数カウンターを接続する。
- 1-2 MODEを4にする。
- 1-3 FINE/MANUALを右(→)に回しきってチューナーか周波数カウンターの表示がC#(277.18Hz)になる様に
- 1-4 FINE/MANUALを左(✓)に回しきってチューナーか周波数カウンターの表示がB(61.735Hz)になる様にVR4 を調整する。

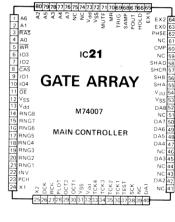
IC DATA



GATE ARRAY M74007

Pin Configuration

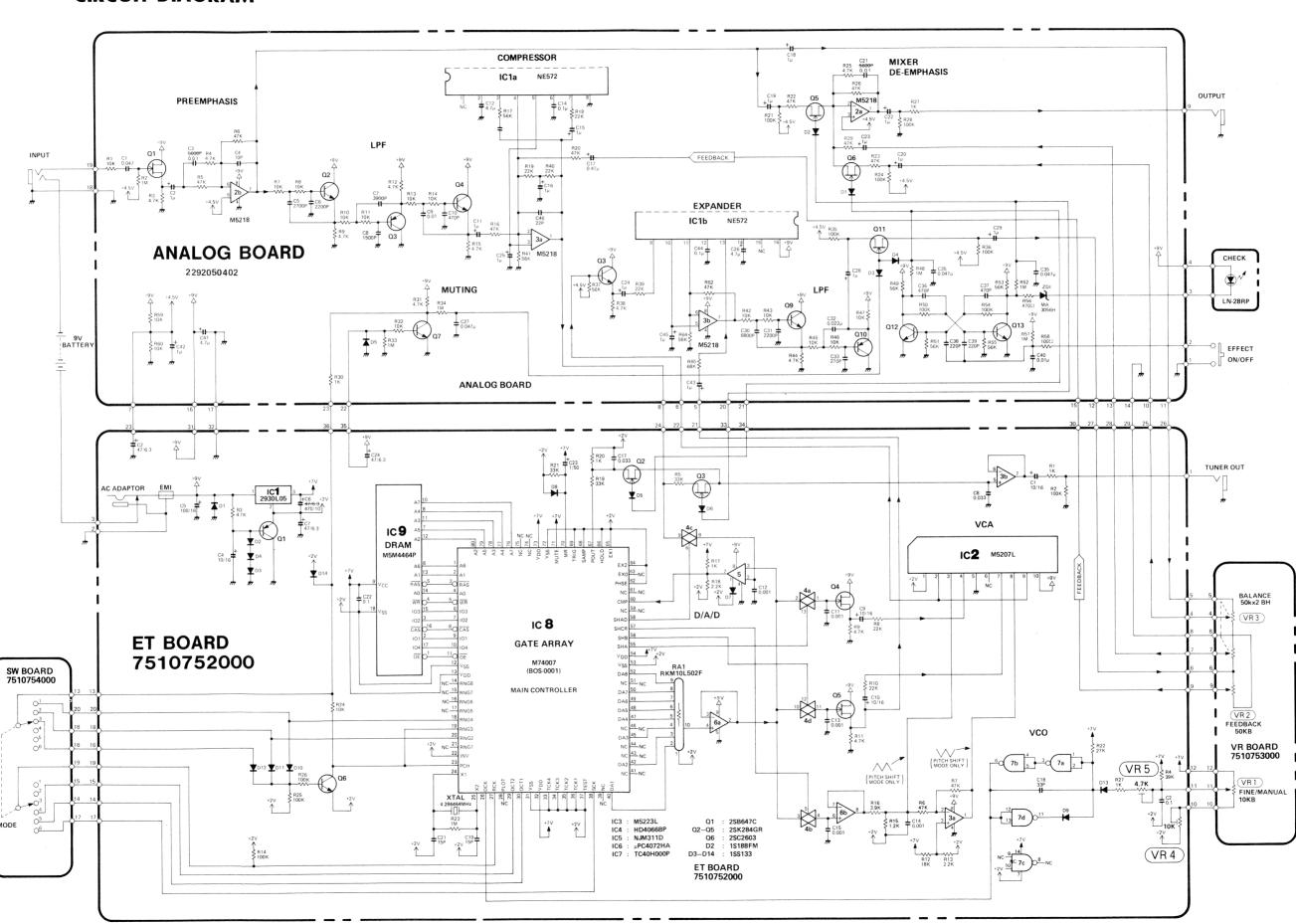
(Top View)



DESIGNATION	PIN No.	DESCRIPTION			
Vss	12,31,53,72	input, +2V power supply			
Vdd	13,32,54,73	input, +7V power supply			
A0 - 7	4,2,80,78,77,79,1,76	output, address AO-A7			
RAS	3	output, row address strobe (active low)			
WR	5	output, write enable (active low) Connect			
CAS	8	output, column address strobe (active low) to D-RAM			
101 - 4	9,7,6,10	I/O data bus D1 - D4			
OE	11	output, output enable (active low)			
RNG1 - 8	14 - 21	input, range 1 - 8 (unused=RNG5 - 8)			
INV	22	input, H=INVERT mode, L=NORMAL mode			
PCH	23	input, H=PITCH SHIFT mode, L=DELAY mode			
X1, X2	24,25	input, XTAL, master clock in PITCH SHIFT mode			
DCK	26	input, master clock input in DELAY mode			
RCK	27	input, read address counter clock input in PITCH SHIFT mode			
PLOT	28	output, clock to PLL (f=1/64RCK)			
OCT2	29	octave 2 unused, open			
OCT1	30	octave l unused, open			
TCK4,3,2,1,TEST	33 - 37	test unused, pulled down +2V			
SCK	38	clock unused, open (f=1/32DCK)			
DA1 - 8	40 - 52	output, data to DA convertor			
SHA, SHB, SHCR, SHAD	55 - 58	output, sample & hold (D/A/D timing control)			
CMP	60	input, comparator input			
PHSE	62	input, phase comparator (H=phase invert mode)			
EXO - 2	63 - 65	output, exclusive OR gate unused			
HOLD	66	input, H=hold, L=normal			
POUT	67	output, clock to TUNER OUT (f=1/512RCK)			
SAMP	68	input unused, H=SAMPLER mode, L=NORMAL mode			
TRIG	69	input unused, trigger input in SAMPLER mode			
MR	70	input, master reset			
MUTE	71	input, mute signal			
NC	39,41,43,44,46	unused, open			
	51,59,61,74,75				

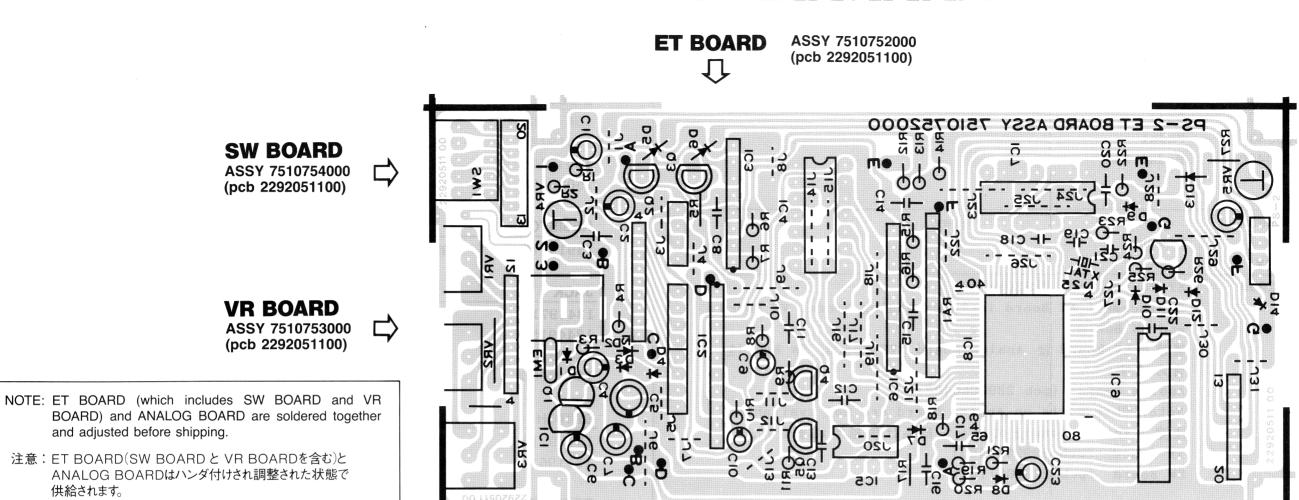
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37

CIRCUIT DIAGRAM



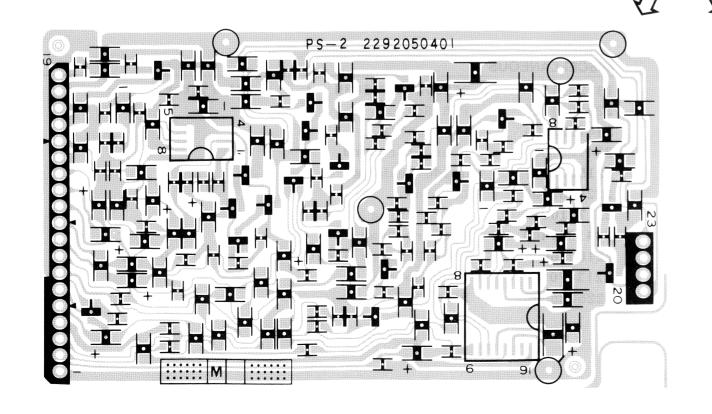
供給されます。

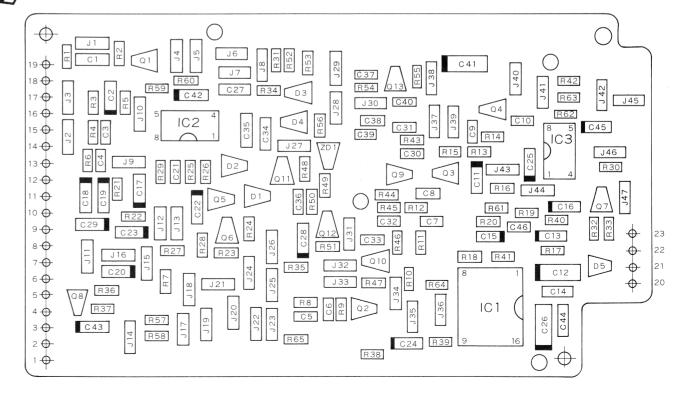
5 6 63 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38



ANALOG BOARD ASSY 7510756000

(pcb 2292050402)





PARTS LIST

2201021100	Case				
2221057200	Panel				
2202085100	Cover				
2235030500	Base				
2218055600	Pedal				
2235030400	Pedal Mat				
KNOB					
2247038600	gray				
PCB ASSY					
7510752000	ET Board (pcb 2292051100)				
	Replacement ET Board includes Analog Board, VR Board				
	and SW Board.				
	補修用ET Board は Analog Board, VR Board, SW Boardを				
	含みます。				
	Analog Board (pcb 2292050402) Note: Chip parts on Analog board are not available. 注意:Analog Board上のチップ部品は供給されません。				
					注意: Analog Board
	任息: Analog Board VR Board (pcb 2292				
		(051100)			
*****	VR Board (pcb 2292	2051100) 2051100)			
	VR Board (pcb 2292 SW Board (pcb 2292	2051100) 2051100)			
********* IC 15229843	VR Board (pcb 2292 SW Board (pcb 2292	2051100) 2051100)			
IC	VR Board (pcb 2292 SW Board (pcb 2292 LED Board(PS-2) (p	2051100) 2051100) ocb 2292053300)			
IC 15229843	VR Board (pcb 2292 SW Board (pcb 2292 LED Board(PS-2) (p	(051100) (051100) (0cb 2292053300) (Gate Array			
IC 15229843 15179359	VR Board (pcb 2292 SW Board (pcb 2292 LED Board(PS-2) (p M74007 BOS-0001 M5M4464P	(051100) (051100) (0cb 2292053300) (Gate Array			
IC 15229843 15179359 15159503	VR Board (pcb 2292 SW Board (pcb 2292 LED Board(PS-2) (p M74007 BOS-0001 M5M4464P TC40H000	(051100) (051100) (0cb 2292053300) (Gate Array			
IC 15229843 15179359 15159503 15159115H0	VR Board (pcb 2292 SW Board (pcb 2292 LED Board(PS-2) (p M74007 BOS-0001 M5M4464P TC40H000 HD4066BP	(051100) (051100) (ocb 2292053300) Gate Array DRAM			
IC 15229843 15179359 15159503 15159115H0 15219181	VR Board (pcb 2292 SW Board (pcb 2292 LED Board(PS-2) (p M74007 BOS-0001 M5M4464P TC40H000 HD4066BP M5207L01	051100) 051100) 0cb 2292053300) Gate Array DRAM			
15229843 15179359 15159503 15159115H0 15219181 15189185	VR Board (pcb 2292 SW Board (pcb 2292 LED Board(PS-2) (p M74007 BOS-0001 M5M4464P TC40H000 HD4066BP M5207L01 M5223L	051100) 051100) 0cb 2292053300) Gate Array DRAM			
15229843 15179359 15159503 15159115H0 15219181 15189185 15189203	VR Board (pcb 2292 SW Board (pcb 2292 LED Board(PS-2) (p M74007 BOS-0001 M5M4464P TC40H000 HD4066BP M5207L01 M5223L μ PC4072HA	051100) 051100) 0cb 2292053300) Gate Array DRAM			
IC 15229843 15179359 15159503 15159115H0 15219181 15189185 15189203 15189111JI	VR Board (pcb 2292 SW Board (pcb 2292 LED Board(PS-2) (p M74007 BOS-0001 M5M4464P TC40H000 HD4066BP M5207L01 M5223L μ PC4072HA NJM311	051100) 051100) 0cb 2292053300) Gate Array DRAM			
15229843 15179359 15159503 15159115H0 15219181 15189185 15189203 15189111JI 15199146	VR Board (pcb 2292 SW Board (pcb 2292 LED Board(PS-2) (p M74007 BOS-0001 M5M4464P TC40H000 HD4066BP M5207L01 M5223L μ PC4072HA NJM311	051100) 051100) 0cb 2292053300) Gate Array DRAM			
IC 15229843 15179359 15159503 15159115H0 15219181 15189185 15189203 15189111JI 15199146 TRANSISTOR	VR Board (pcb 2292 SW Board (pcb 2292 LED Board(PS-2) (p M74007 BOS-0001 M5M4464P TC40H000 HD4066BP M5207L01 M5223L μ PC4072HA NJM311 NJM2930L05	051100) 051100) 0cb 2292053300) Gate Array DRAM			
IC 15229843 15179359 15159503 15159115H0 15219181 15189185 15189203 15189111JI 15199146 TRANSISTOR 15119105	VR Board (pcb 2292 SW Board (pcb 2292 LED Board(PS-2) (p M74007 BOS-0001 M5M4464P TC40H000 HD4066BP M5207L01 M5223L μ PC4072HA NJM311 NJM2930L05	051100) 051100) 0cb 2292053300) Gate Array DRAM			

CRYSTAL				
12389757	4.286464MHz			
DIODE				
15019209T0	S5500G			
15019124	1s-188FM1			
15019125	1SS-133			
15029198	LN-28RP		LED	
RESISTOR				
13919172	RKM10L502		Ladder Network	
JACK				
13449711	HEC0470-01-630		AC Adaptor	
13449150	HLJ0544-01-010			INPUT
13449105	SG-7615			OUTPUT
13449106	SG-7622			TUNER OUT
SWITCH				
13129710	JM-0404		Key Switch	
13119512	SRBM16			MODE
POTENTIOMETER				
13279786	EVJ-KAAP15B14	10kB		FINE/MANUAL
13279785	EVJ-KAAP15B54	50kB		FEEDBACK
13279846	EVJSBAP15-751	50k x	2BH	BALANCE
13299153	H0651	4.7kB		trimmer
13299140	Н0651	10kB		trimmer
MISCELLANEOUS	-			
13529145	DSS306-55FZ103N		EMI Filter	
2220011800	PCB Holder			
2347023100	Flat Cable			
2341057900	Connector Cord			
2215070201	Guide Bushing			
2226033300	Cushion			
2217010900	Coil Spring			
2341041400	Battery Snap			
2216056000	NS-2 Plastic Sh	eet	clear large type	е
	_			

PS-2

2213070900

2225025500

Jack Spacer PS-2 Shield OCT. 1987

CHANGE INFORMATION

変更案内

IMPORTANT

The change (A) below must be done with the units numbered prior to 845800.

重要

下記のノイズ対策 (A)は製番 845800 までの製品に対して必ず実施して 下さい。

- O NOISE (high-frequency)
- (A) EFF.SN845800 859200 Capacitor
 - C3 , C21 (Analog Board) 5600pF to 5600pF

with 0.0047μ F (surface mounting) (Refer to Fig.A)

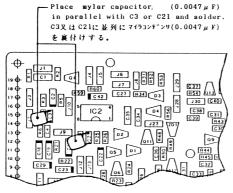
C6 (ET Board) $47\,\mu\;\mathrm{F/6.3V}\;\;\mathrm{to}\;\;470\,\mu\;\mathrm{F/10V}$ (Refer to Fig.B)

REASON:

PREEMPHASIS and DE-EMPHASIS circuits cannot reduce high-frequency noize.

Fig.A

Analog Board



- (B) EFF.SN859300 UP Capacitor
 - C3 , C21 (Analog Board) $5600 \rm pF \ to \ 0.01 \, \mu \ F$ C6 (ET Board) $47 \, \mu \ F/6.3 V \ to \ 470 \, \mu \ F/10 V$
 - * (A) and (B) above mentioned are same change.

〇ノイズ (高域)

(A) 実施製番845800 から 859200 コンデンサ定数変更

C3 , C21 (Analog Board) 5600pF → 5600pFに 0.0047μF を裏付けする

(Fig.A 参照)

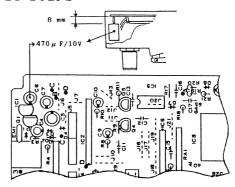
C6 (ET Board) 47 μ F/6.3V → 470 μ F/10V (Fig.B 参照)

理由:

PREEMPHASIS, DE-EMPHASIS回路の 高域特性が十分でなかったので、 高域ノイズを除去できなかった為

Fig.B

ET Board



- (B) 実施製番859300 以上 コンデンサ定数変更 C3 , C21 (Analog Board) 5600pF → 0.01μF へ C6 (ET Board) 47μF/6.3V → 470μF/10V
 - * 上記(A)と(B)は同じ変更です。

Roland® 17059411

UPC

1705941



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