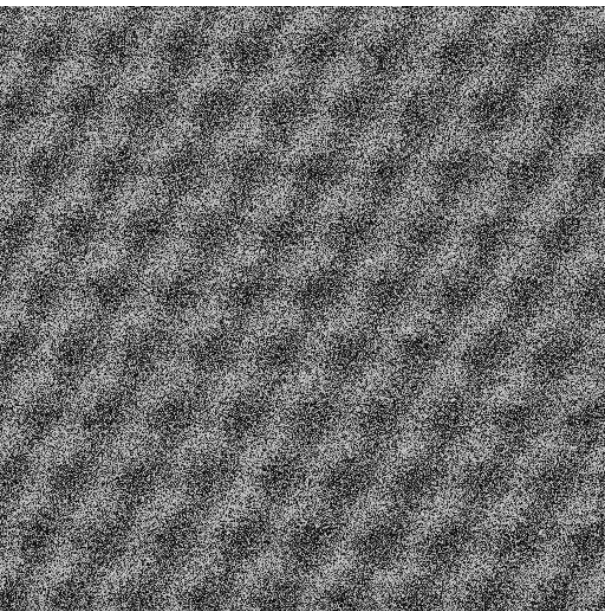
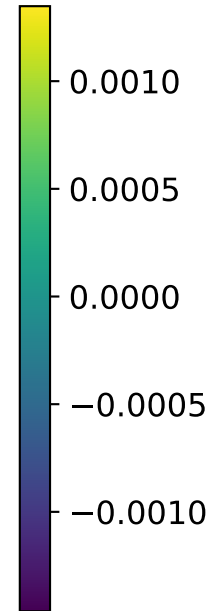
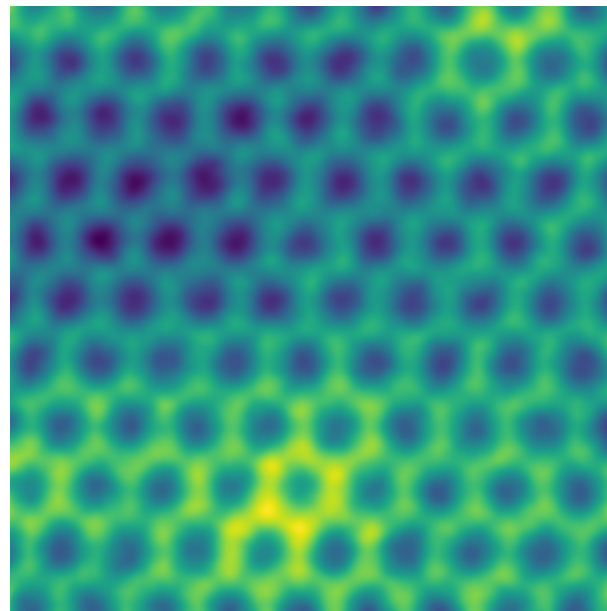


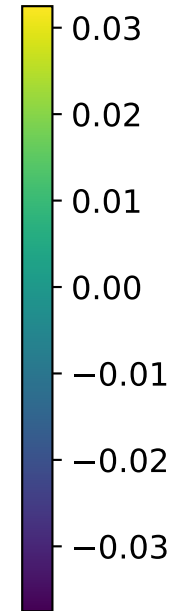
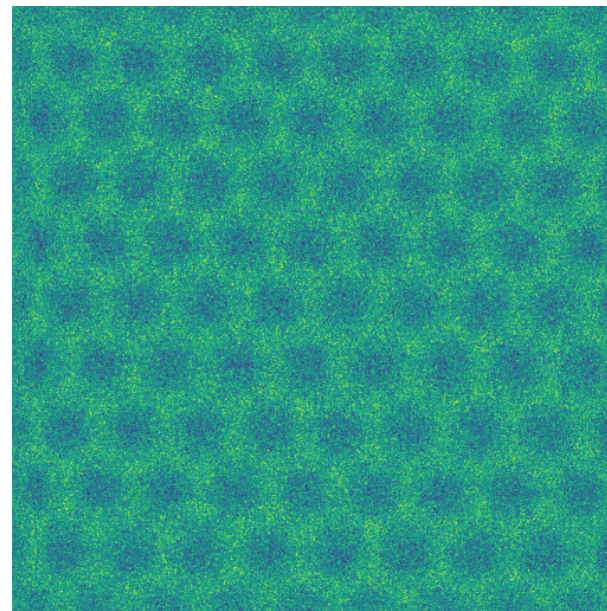
a) Virtual ADF (py4DSTEM)



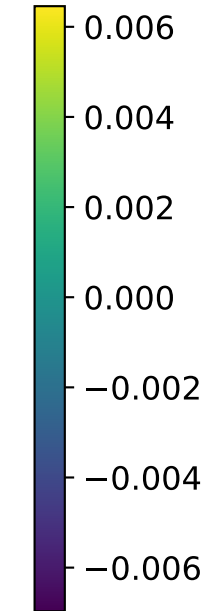
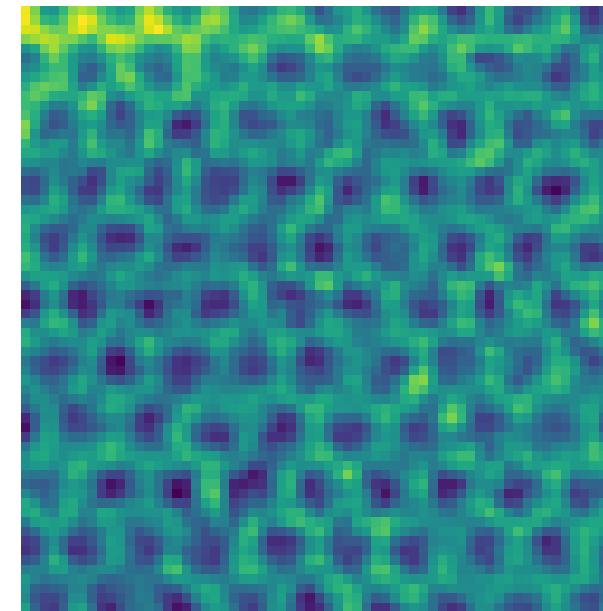
b) DPC phase (py4DSTEM)



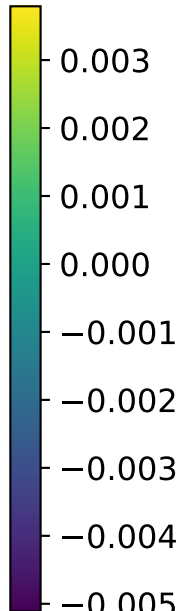
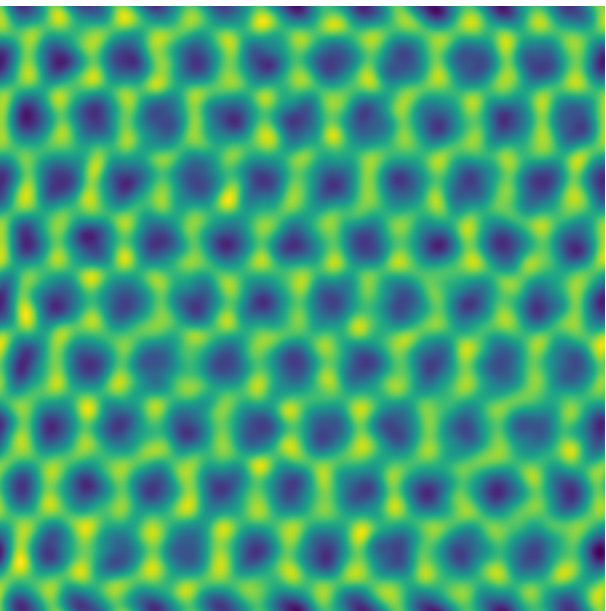
c) Parallax (py4DSTEM)



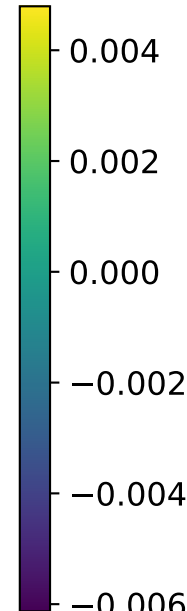
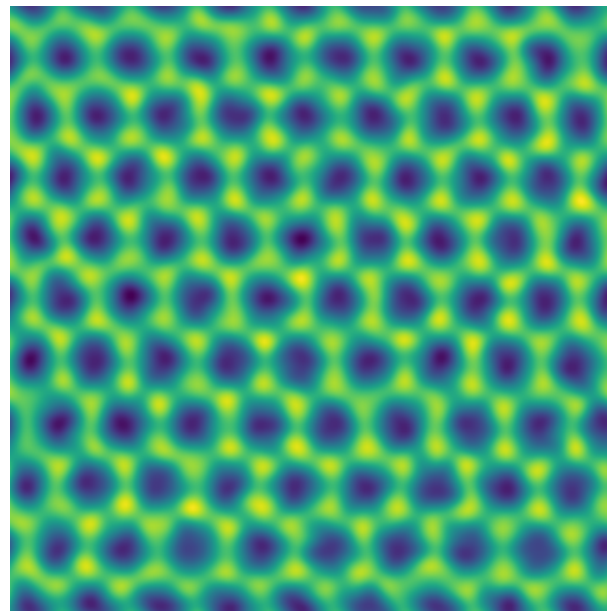
d) Iterative GD (py4DSTEM)



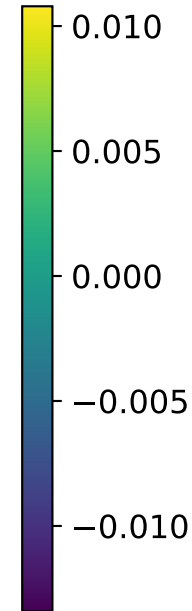
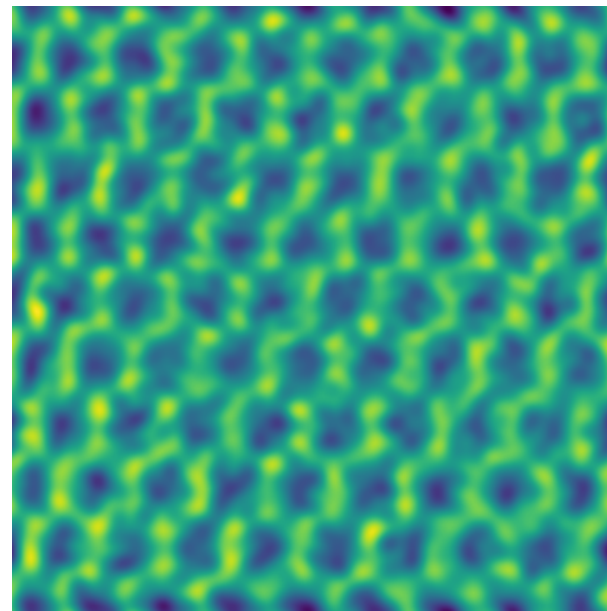
e) SSB phase (PyPtychoSTEM)



f) SSB phase AC (PyPtychoSTEM)



g) WDD phase (PyPtychoSTEM)



h) WDD phase AC (PyPtychoSTEM)

