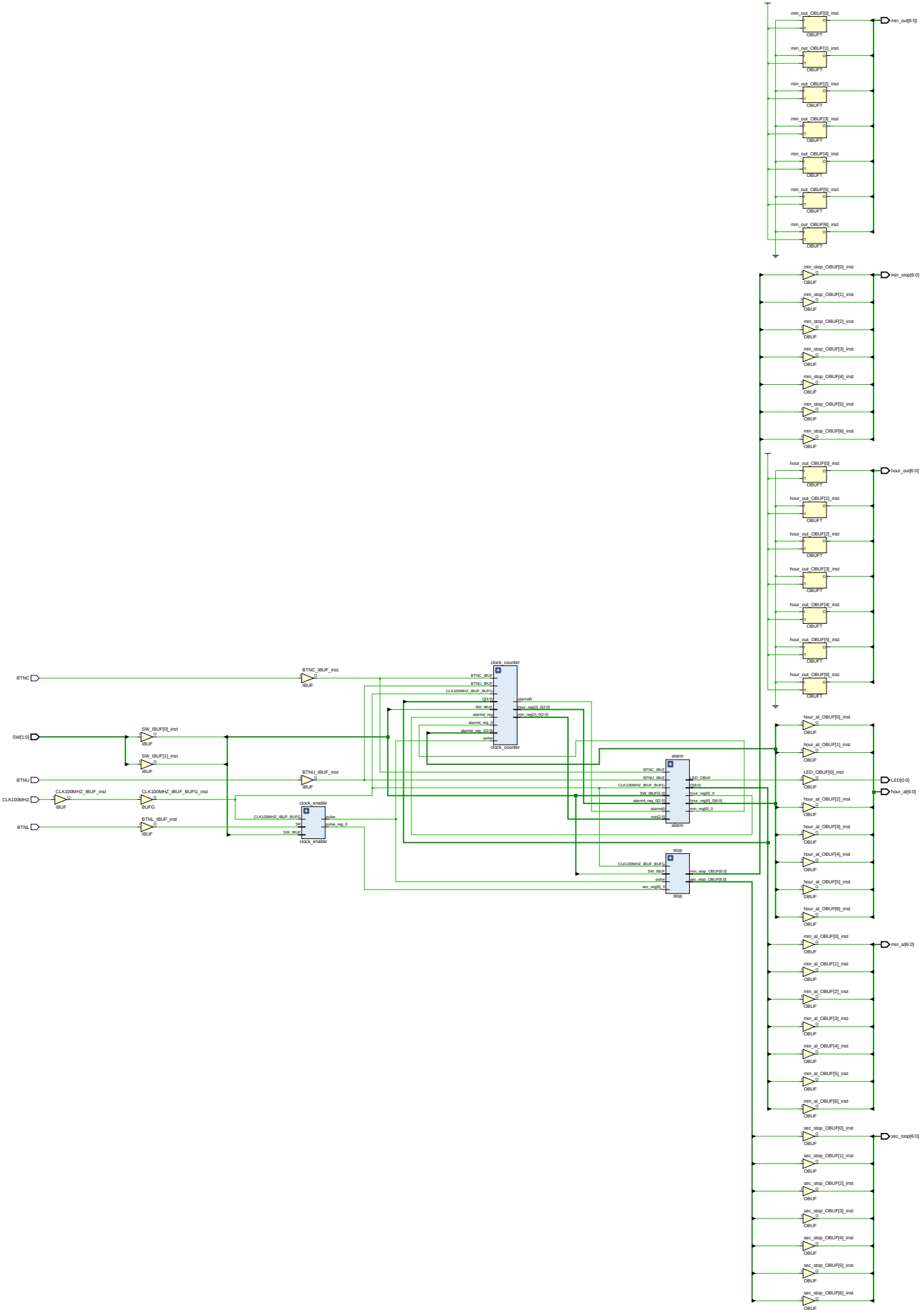
BTNC
SW[1:0]
BTNU
CLK100MHZ
BTNL
BTNC_IBUF_inst
SW_IBUF[0]_inst
SW_IBUF[1]_inst
BTNU_IBUF_inst
CLK100MHZ_IBUF_inst
CLK100MHZ_IBUF_BUFG_inst
BTNL_IBUF_inst
IBUF
BUFG
clock_counter
clock_enable
alarm
stop
min_out_OBUF[0]_inst
min_out_OBUF[1]_inst
min_out_OBUF[2]_inst
min_out_OBUF[3]_inst
min_out_OBUF[4]_inst
min_out_OBUF[5]_inst
min_out_OBUF[6]_inst
OBUFT
min_out[6:0]
min_stop_OBUF[0]_inst
min_stop_OBUF[1]_inst
min_stop_OBUF[2]_inst
min_stop_OBUF[3]_inst
min_stop_OBUF[4]_inst
min_stop_OBUF[5]_inst
min_stop_OBUF[6]_inst
OBUF
min_stop[6:0]
hour_out_OBUF[0]_inst
hour_out_OBUF[1]_inst
hour_out_OBUF[2]_inst
hour_out_OBUF[3]_inst
hour_out_OBUF[4]_inst
hour_out_OBUF[5]_inst
hour_out_OBUF[6]_inst
hour_out[6:0]
hour_al_OBUF[0]_inst
hour_al_OBUF[1]_inst
LED_OBUF[0]_inst
hour_al_OBUF[2]_inst
hour_al_OBUF[3]_inst
hour_al_OBUF[4]_inst
hour_al_OBUF[5]_inst
hour_al_OBUF[6]_inst
LED[0:0]
hour_al[6:0]
min_al_OBUF[0]_inst
min_al_OBUF[1]_inst
min_al_OBUF[2]_inst
min_al_OBUF[3]_inst
min_al_OBUF[4]_inst
min_al_OBUF[5]_inst
min_al_OBUF[6]_inst
min_al[6:0]
sec_stop_OBUF[0]_inst
sec_stop_OBUF[1]_inst
sec_stop_OBUF[2]_inst
sec_stop_OBUF[3]_inst
sec_stop_OBUF[4]_inst
sec_stop_OBUF[5]_inst
sec_stop_OBUF[6]_inst
sec_stop[6:0]