User's Guide

Document Number: MCSDKRN

Rev. 10, 06/2019

Multicore SDK (MCSDK) Release Notes



Contents

Chapter 1 Overview	3
Chapter 2 What is new	4
Chapter 3 Development tools	5
Chapter 4 Release contents	6
Chapter 5 Multicore SDK release overview	7
Chapter 6 Demo applications	8
Chapter 7 Revision history	9

Chapter 1 Overview

These are the release notes for the NXP Multicore Software Development Kit (MCSDK) version 2.6.0. This software package contains components for efficient work with multicore devices as well as for the multiprocessor communication.

Chapter 2 What is new

- eRPC: Improved support of const types.
- eRPC: Fixed Mac build.
- eRPC: Fixed serializing python list.
- eRPC: Documentation update.
- eRPC: Add missing doxygen comments for transports.
- RPMsg-Lite: Added configuration macro RL_DEBUG_CHECK_BUFFERS.
- RPMsg-Lite: Several MISRA violations fixed.
- RPMsg-Lite: Added environment layers for QNX and Zephyr.
- RPMsg-Lite: Allow environment context required for some environments (controlled by the RL_USE_ENVIRONMENT_CONTEXT configuration macro).
- RPMsg-Lite: Data types consolidation.
- MCMgr: Documentation updated to describe handshaking in a graphic form.
- MCMgr: Minor code adjustments based on static analysis tool findings.
- Supported evaluation boards (multicore examples):
 - LPCXpresso54114
 - LPCXpresso54102
 - LPCXpresso55S69
- Supported evaluation boards (multiprocessor examples):
 - FRDM-K22F
 - FRDM-K28F
 - FRDM-K28FA
 - FRDM-K64F
 - FRDM-K66F
 - FRDM-K82F
 - FRDM-KL25Z
 - FRDM-KL27Z
 - FRDM-KL43Z

Chapter 3 Development tools

The Multicore SDK (MCSDK) version 2.6.0 was compiled and tested with these development tools:

- IAR Embedded Workbench for Arm® version 8.32.3
- MDK-ARM Microcontroller Development Kit (Keil)[®] version 5.27
- Makefiles support with GCC revision 8-2018-q4-major from Arm Embedded
- MCUXpresso IDE v11.0.0

Chapter 4 Release contents

This table describes the release contents. Not all MCUXpresso SDK packages contain the whole set of these components.

Table 1. Release contents

Deliverable	Location
Multicore SDK location within the MCUXpresso SDK folder structure	<mcuxpressosdk_install_dir>/middleware/multicore/</mcuxpressosdk_install_dir>
Documentation	<mcuxpressosdk_install_dir>/docs/multicore/</mcuxpressosdk_install_dir>
Embedded Remote Procedure Call component	<mcsdk_dir>/erpc/</mcsdk_dir>
Multicore Manager component	<mcsdk_dir>/mcmgr/</mcsdk_dir>
RPMsg-Lite	<mcsdk_dir>/rpmsg_lite/</mcsdk_dir>
Multicore demo applications	<mcuxpressosdk_install_dir>/boards/<board_name>/ multicore_examples/</board_name></mcuxpressosdk_install_dir>
Multiprocessor demo applications	<pre><mcuxpressosdk_install_dir>/boards/<board_name>/ multiprocessor_examples/</board_name></mcuxpressosdk_install_dir></pre>

Chapter 5 Multicore SDK release overview

Together, the Multicore SDK (MCSDK) and the MCUXpresso SDK (SDK) form a framework for the development of software for NXP multicore devices. The MCSDK release consists of the following elementary software components for multicore:

- Embedded Remote Procedure Call (eRPC)
- Multicore Manager (MCMGR) included just in SDK for multicore devices
- Remote Processor Messaging Lite (RPMsg-Lite) included just in SDK for multicore devices

The MCSDK is also accompanied with documentation and several multicore and multiprocessor demo applications.

Chapter 6 Demo applications

The multicore demo applications demonstrate the usage of the MCSDK software components on supported multicore development boards. The following multicore demo applications are located together with other MCUXpresso SDK examples in the <MCUXpressoSDK_install_dir>/boards/

//board_name>/multicore_examples... subdirectories.

- · erpc_matrix_multiply_rpmsg
- · erpc_matrix_multiply_rpmsg_rtos
- erpc_two_way_rpc_rpmsg_rtos
- hello_world
- low_power
- multicore_manager
- rpmsg_lite_pingpong
- rpmsg_lite_pingpong_rtos

The eRPC multicore component can be leveraged for inter-processor communication and remote procedure calls between SoCs / development boards. The following multiprocessor demo applications are located together with other MCUXpresso SDK examples in the <a href="https://document.org/lines.org/line

- erpc_client_matrix_multiply_spi
- erpc_server_matrix_multiply_spi
- · erpc_client_matrix_multiply_uart
- · erpc_server_matrix_multiply_uart
- erpc_server_dac_adc
- erpc_remote_control

Chapter 7 Revision history

This table summarizes revisions to this document.

Table 2. Revision history

Revision number	Date	Substantive changes
0	09/2015	Initial release
1	03/2016	Updated for the KSDK 2.0.0 and the MCSDK 1.1.0
2	08/2016	Updated for the MCSDK 2.0.0 and the LPCXpresso54114 support
3	09/2016	Updated for the MCSDK 2.1.0
4	03/2017	Updated for the MCSDK 2.2.0
5	06/2017	Updated for the MCSDK 2.2.1 and the LPCXpresso54102 support
7	11/2017	Updated for the MCSDK 2.3.0 and MCUXpresso SDK 2.3.0 release
8	05/2018	Updated for the MCSDK 2.4.0 and MCUXpresso SDK 2.4.0 release
9	12/2018	Updated for the MCSDK 2.5.0 and MCUXpresso SDK 2.5.0 release
10	06/2019	Updated for the MCSDK 2.6.0 and MCUXpresso SDK 2.6.0 release

How To Reach Us

Home Page:

www.nxp.com

Web Support:

www.nxp.com/support

Information in this document is provided solely to enable system and software implementers to use NXP products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits based on the information in this document. NXP reserves the right to make changes without further notice to any products herein.

NXP makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does NXP assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in NXP data sheets and/or specifications can and do vary in different applications, and actual performance may vary over time. All operating parameters, including "typicals," must be validated for each customer application by customer's technical experts. NXP does not convey any license under its patent rights nor the rights of others. NXP sells products pursuant to standard terms and conditions of sale, which can be found at the following address: www.nxp.com/
SalesTermsandConditions.

While NXP has implemented advanced security features, all products may be subject to unidentified vulnerabilities. Customers are responsible for the design and operation of their applications and products to reduce the effect of these vulnerabilities on customer's applications and products, and NXP accepts no liability for any vulnerability that is discovered. Customers should implement appropriate design and operating safeguards to minimize the risks associated with their applications and products.NXP, the NXP logo, NXP SECURE CONNECTIONS FOR A SMARTER WORLD, COOLFLUX, EMBRACE, GREENCHIP, HITAG, I2C BUS, ICODE, JCOP, LIFE VIBES, MIFARE, MIFARE CLASSIC, MIFARE DESFIRE, MIFARE PLUS, MIFARE FLEX, MANTIS, MIFARE ULTRALIGHT, MIFARE4MOBILE, MIGLO, NTAG, ROADLINK, SMARTLX, SMARTMX, STARPLUG, TOPFET, TRENCHMOS, UCODE, Freescale, the Freescale logo, AltiVec, C - 5, CodeTEST, CodeWarrior, ColdFire, ColdFire+, C - Ware, the Energy Efficient Solutions logo, Kinetis, Layerscape, MagniV, mobileGT, PEG, PowerQUICC, Processor Expert, QorlQ, QorlQ Qonverge, Ready Play, SafeAssure, the SafeAssure logo, StarCore, Symphony, VortiQa, Vybrid, Airfast, BeeKit, BeeStack, CoreNet, Flexis, MXC, Platform in a Package, QUICC Engine, SMARTMOS, Tower, TurboLink, and UMEMS are trademarks of NXP B.V. All other product or service names are the property of their respective owners. AMBA, Arm, Arm7, Arm7TDMI, Arm9, Arm11, Artisan, big.LITTLE, Cordio, CoreLink, CoreSight, Cortex, DesignStart, DynamlQ, Jazelle, Keil, Mali, Mbed, Mbed Enabled, NEON, POP, RealView, SecurCore, Socrates, Thumb, TrustZone, ULINK, ULINK2, ULINK-ME, ULINK-PLUS, ULINKpro, μVision, Versatile are trademarks or registered trademarks of Arm Limited (or its subsidiaries) in the US and/or elsewhere. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. Oracle and Java are registered trademarks of Oracle and/or its affiliates. The Power Architecture and Power.org word marks and the Power and Power.org logos and related marks are trademarks and service marks licensed by Power.org.

© NXP B.V. 2019.

All rights reserved.

For more information, please visit: http://www.nxp.com
For sales office addresses, please send an email to: salesaddresses@nxp.com

Date of release: 06/2019

Document identifier: MCSDKRN

