

Core/Src/stm32f4xx_it.c

```
graph TD; A[Core/Src/stm32f4xx_it.c] --> B[main.h]; A --> C[stm32f4xx_it.h]; B --> D[stm32f4xx_hal.h];
```

main.h

stm32f4xx_it.h

stm32f4xx_hal.h