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| SYNC\_GEN\_RESOLUTION | SYNC\_GEN\_RESOLUTION | 0x203000 | TD0-TD31 | 0x04000500 | RW |
| SYNC\_GEN\_SYNC\_WIDTH | SYNC\_GEN\_SYNC\_WIDTH | 0x203004 | TD0-TD31 | 0x00030016 | RW |
| SYNC\_GEN\_HPORCH | SYNC\_GEN\_HPORCH | 0x203008 | TD0-TD31 | 0x00200015 | RO |
| SYNC\_GEN\_VPORCH | SYNC\_GEN\_VPORCH | 0x20300C | TD0-TD31 | 0x00260001 | RO |
| EXTERNAL\_SYNC\_SELECT | EXTERNAL\_SYNC\_SELECT | 0x203010 | TD0-TD31 | 0x00000000 | RW |
| EXT\_SYNC\_EXIST | EXT\_SYNC\_EXIST | 0x203014 | TD0-TD31 | 0x00000000 | RO |
| EXT\_SYNC\_ERROR | EXT\_SYNC\_ERROR | 0x203018 | TD0-TD31 | 0x00000000 | RW |
| Time\_Tag\_960Hz | Time\_Tag\_960Hz\_LSB | 0x2030C0 | TD0-TD31 | 0x00000000 | RO |
| Time\_Tag\_960Hz | Time\_Tag\_960Hz\_MSB | 0x2030C4 | TD0-TD31 | 1 | RO |

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| VCH\_Convert\_Mode | VCH\_Convert\_Mode | 0x300010 | TD0-TD31 | 0x7 | RW |
| VCH\_False\_Coloring | VCH\_False\_Coloring | 0x30003C | TD0-TD31 | 0x0000 | RW |

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| VP\_version | VP\_version | 0x700100 | TD0-TD31 |  | RO |

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| FPGA\_version | FPGA\_version\_minor | 0x940000 | TD0-TD7 |  | RO |
| FPGA\_version | FPGA\_version\_major | 0x940000 | TD8-TD15 |  | RO |
| MUXBUS\_XX\_MULTI\_RT | MUXBUS\_RT\_DISABLE\_BIST | 0x94003C | TD0 | 0x00 | RW |
| MUXBUS\_XX\_MULTI\_RT | MUXBUS\_BM\_DISABLE\_BIST | 0x94003C | TD1 | 0x0 | RW |

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| TEST\_REG | TEST\_REG | 0xFA200000 | TD0 | 0x00 | RO |