

# Po-Chi, Hsu

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## EDUCATION

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- **National Yang Ming Chiao Tung University** Hsinchu, TW  
*Master of Science in Computer Science; GPA: 4.18* *Sep 2022 – Jun 2025*
- **National Yunlin University of Science and Technology** Yunlin, TW  
*Bachelor of Science in Computer Science; GPA: 3.65* *Sep 2017 – Jun 2022*

## EXPERIENCE

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- **Mediatek** Chupei, TW  
*Software Engineer Intern (Windows Wi-Fi Driver)* *July 2023 – Aug 2023*
  - **Driver Development - Efuse:** Implemented driver operations to read and write efuse, configuring and managing internal circuit parameters. Patched event handling mechanisms to ensure process stability and accuracy.
  - **Driver Development - Sniffer Mode:** Extracted information from RX descriptors. Incorporated Radiotap and PCAP headers for advanced packet analysis. Established robust packet receiving and transmission workflows in sniffer mode to aid in network monitoring and debugging.

## PROJECTS

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- **Physical Design Automation:** Circuit Partitioning using the Fiduccia–Mattheyses heuristic to balance module sizes and minimize interconnections. Chip Floorplanning with simulated annealing and B\*-trees for optimized area and wirelength. Standard Cell Layout through Euler path methods to maximize diffusion sharing and reduce total HPWL. Channel Routing employing a dogleg routing algorithm to efficiently place tracks and vias.
- **Parallel Programming:** dddd
- **Software Testing:** dddd
- **Cloud Native Development:** ddd
- **Device Driver Programming:** ddd

## SKILLS

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- **Languages:** C, C++, Python, CUDA, Assembly
- **Tools:** GDB, Wireshark, Git, Docker, WinDbg
- **Frameworks:** PyTorch
- : Linux, Unix, Socket, Shell, Makefile, Boost, Arm