ASSEMBLY LANGUAGE

13B - Wednesday 25th January 2023

WHY ARE WE EVEN LEARNING THIS?

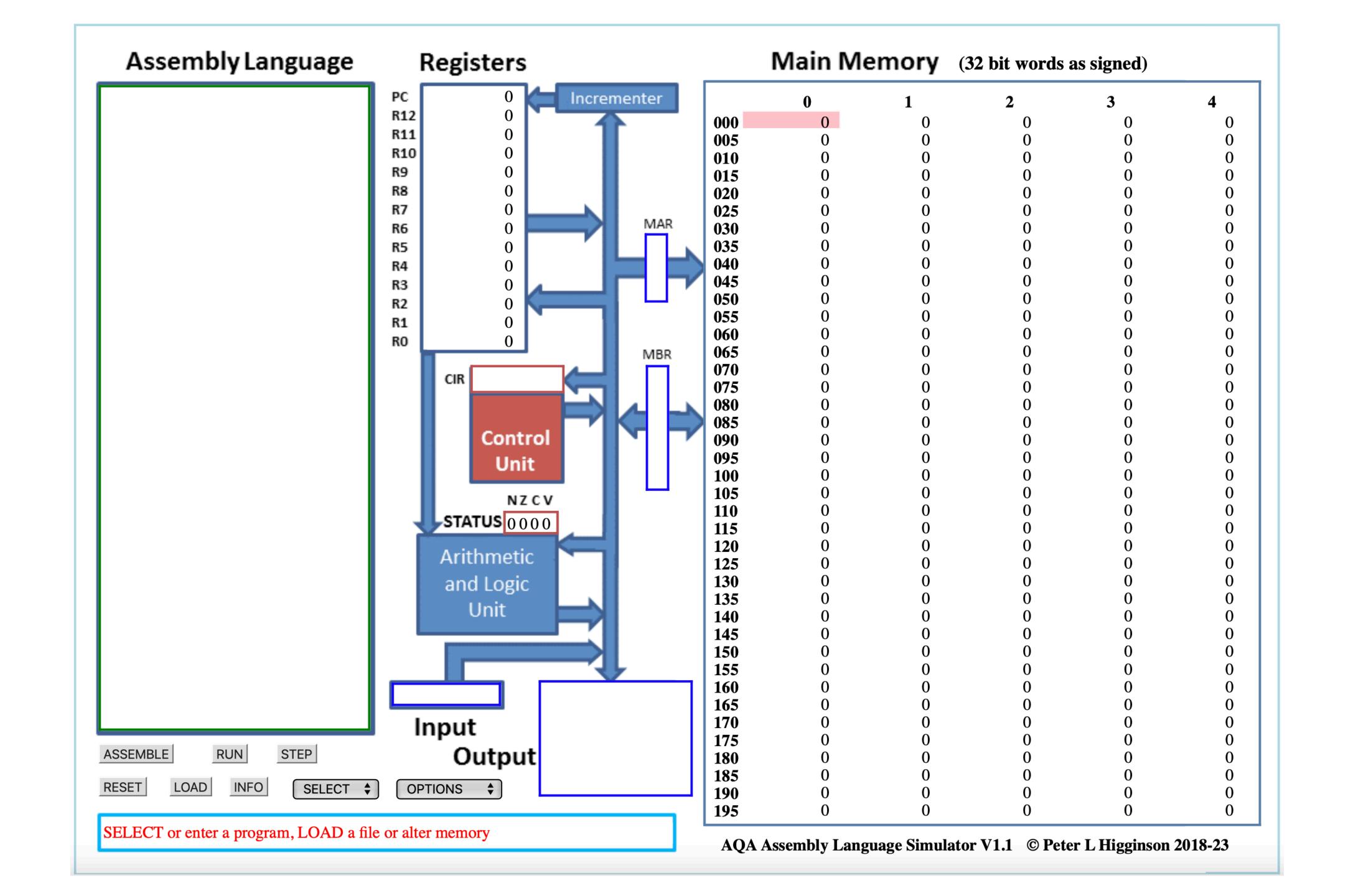
WHY CAN'T WE JUST USE PYTHON?

(OR WHATEVER YOUR FAVOURITE LANGUAGE IS)

- In reality we don't write programs in it very often*
- But we need to know it:
 - Finding Bugs
 - Incredible Performance
 - System Software
 - Malware

SIN YOUR EXAM

AND IT WILL COME UP



RECAP OF INSTRUCTIONS

T.D.D. D. 1	
LDR Rd, <memory ref=""></memory>	Load the value stored in the memory location specified by <memory ref=""> into register d.</memory>
STR Rd, <memory ref=""></memory>	Store the value that is in register d into the
Sin na, amemory rer	
	memory location specified by <memory ref="">.</memory>
ADD Rd, Rn, <operand2></operand2>	Add the value specified in <pre><pre>coperand2> to the</pre></pre>
	value in register n and store the result in register
	d.
SUB Rd, Rn, <operand2></operand2>	Subtract the value specified by <pre><pre>operand2></pre></pre>
	from the value in register n and store the result
	in register d.
MOV Rd, <pre>operand2></pre>	Copy the value specified by <pre><pre>coperand2> into</pre></pre>
	register d.
CMP Rn, <operand2></operand2>	Compare the value stored in register n with the
	value specified by <pre>operand2>.</pre>
B <label></label>	Always branch to the instruction at position
	<pre><label> in the program.</label></pre>
B <condition> <label></label></condition>	Branch to the instruction at position <label> if</label>
	the last comparison met the criterion specified
	by <condition>. Possible values for</condition>
	<pre><condition> and their meanings are:</condition></pre>
	EQ: equal to NE: not equal to
	GT: greater than LT: less than
AND Rd, Rn, <pre>operand2></pre>	Perform a bitwise logical AND operation
_	between the value in register n and the value
	specified by <pre><pre>specified by <pre><pre>coperand2> and store the result in</pre></pre></pre></pre>
	register d.
ORR Rd, Rn, <operand2></operand2>	Perform a bitwise logical OR operation between
	the value in register n and the value specified by
	<pre><operand2> and store the result in register d.</operand2></pre>
	<u> </u>

EOR Rd, Rn, <operand2></operand2>	Perform a bitwise logical XOR (exclusive or) operation between the value in register n and the value specified by <pre>operand2></pre> and store the result in register d.
MVN Rd, <operand2></operand2>	Perform a bitwise logical NOT operation on the value specified by <pre><pre>value specified by <pre>coperand2> and store the result in register d.</pre></pre></pre>
LSL Rd, Rn, <operand2></operand2>	Logically shift left the value stored in register n by the number of bits specified by <pre><pre>operand2></pre> and store the result in register d.</pre>
LSR Rd, Rn, <operand2></operand2>	Logically shift right the value stored in register n by the number of bits specified by <pre><pre>operand2></pre> and store the result in register d.</pre>
HALT	Stops the execution of the program.

IF STATEMENTS

```
if ...:
   #INNER CODE
  # After Code
 CMP ...
 B<NOT Condition> label
 # Inner Code
label:
 # After
```

IF ELSE

```
if <Condition>:
    # TRUE Code
else:
    # FALSE Code
# After Code
```

YOUR TURN

```
x = 0
if y == 7:
x = 100
else:
x = 10
```

Use R0 for x
Use R1 for y

```
MOV R0, #0
 CMP R1, #7
 BEQ is seven
MOV RO, #10
B after
is seven:
MOV RO, #100
after:
 HALT
```

WHILE LOOPS

```
while ...:
      # INNER CODE
loopStart:
 CMP ...
 B<NOT Condition> loopDone
 # Inner Code
 B loopStart
loopDone:
 # After
```

```
while ...:
    # INNER CODE
 B test
loopTop:
 # Inner Code
test:
 CMP ...
 B<Condition> loopTop
 # After Code
```

YOUR TURN

```
sum = 0
while x > 1:
  sum = sum + x
  x = x - 1
```

Store the result of sum in location 42.

Assume R0 contains the value x You may use R1 for other steps

```
MOV R1, #0
B test
loop top:
ADD R1, R1, R0
 SUB R1, R1, #1
test:
 CMP R1, #1
 BLT loopTop
 HALT
```

YOUR EXAM

Machine-code/assembly language operations

Content

Understand and apply the basic machine-code operations of:

- load
- add
- subtract
- store
- branching (conditional and unconditional)
- compare
- logical bitwise operators (AND, OR, NOT, XOR)
- logical
 - shift right
 - shift left
- halt.

Use the basic machine-code operations above when machine-code instructions are expressed in mnemonic formassembly language, using immediate and direct addressing.

Types of Question:

- Values in Registers + Tracing.
- Write small assembly programs.
- Operand vs Opcode.

TIPS FOR SOLVING OUESTIONS

- Treat each variable as a register.
- Treat each register as a variable.
- Draw arrows on Assembly Code.
- Memorise the common If and While structure.
- Think about your code in a language you're used to.
- Practice, Practice, Practice.

BUBBLESORT

```
data = [2, 47, 4, 23, 9, 11]
n = 6
while True:
  current_index = 0
  num_swaps = 0
  while n >= (current_index + 2)
     next_index = current_index + 1
     current = data[current_index]
     next = data[next_index]
     if current > next: # So we should swap
        data[current_index] = next
        data[next_index] = current
        num_swaps = num_swaps + 1
     current_index = next_index
  if num_swaps == 0:
     break # Otherwise do the loop again
```