ASSEMBLY LANGUAGE

13B - Wednesday 25th January 2023

WHY ARE WE EVEN LEARNING THIS?

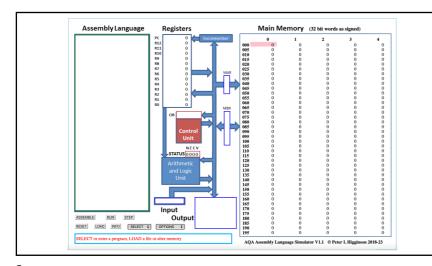
WHY CAN'T WE JUST USE PYTHON?
(OR WHATEVER YOUR FAVORITE LANGUAGE IS)

- In reality we don't write programs in it very often*
- But we need to know it:
- Finding Bugs
- Incredible Performance
- System Software
- Malware

*IT'S IN YOUR EXAM

AND IT WILL COME UP

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RECAP OF INSTRUCTIONS Load the value stored in the memory location EOR Rd, Rn, <operand2> Perform a bitwise logical XOR (exclusive or) operation between the value in register n and STR Rd, <memory ref> the value specified by coperand2> and store memory location specified by <memory ref>.

ADD Rd, Rn, <operand2> Add the value specified in <operand2> to the value in register n and store the result in register the result in register d Perform a bitwise logical NOT operation on the value specified by operand2> and store the SUB Rd, Rn, <operand2> Subtract the value specified by <operand2> from the value in register n and store the result result in register d. LSL Rd, Rn, coperand2>
Logically shift left the value stored in register n
by the number of bits specified by coperand2> in register d.

Copy the value specified by <operand2> into MOV Rd. coperand2> and store the result in register d. register d.

Compare the value stored in register n with the LSR Rd, Rn, <operand2> Logically shift right the value stored in register n by the number of bits specified by <operand2> value specified by operand2>.
Always branch to the instruction at position and store the result in register d.

Stops the execution of the program. Always branch to the instruction at position <_label > In the program.

Branch to the instruction at position <_label > In the last comparison met the criterion specified the last comparison met the criterion specified with the last comparison met the criterion specified vector and their meanings are:

**Cecndut Lisan's and their meanings are:

**E2 (equal to NE: not equal to TI: less than their programs belowed begind AND operations and their programs are the value in register and the value between the value in register and the value. register d.

ORR Rd, Rn, <operand2> Perform a bitwise logical OR operation between the value in register n and the value specified by <operand2> and store the result in register d.

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1



```
YOUR TURN

x = 0
if y == 7:
x = 100
else:
x = 10

Use R0 for x
Use R1 for y
```

WHILE LOOPS while ...: while ...: # INNER CODE # INNER CODE loopStart: 👡 B test CMP ... loopTop: ___ B<NOT Condition> loopDone # Inner Code # Inner Code test: B loopStart CMP ... loopDone: B<Condition> loopTop # After Code # After

YOUR TURN

sum = 0
while x > 1:
sum = sum + x
x = x - 1
Store the result of sum in
location 42.
Assume R0 contains the value x
You may use R1 for other steps

8

7

2

YOUR EXAM

Machine-code/assembly language operations

Understand and apply the basic machine-code operations of:

- subtract store
- branching (conditional and unconditional)
- logical bitwise operators (AND, OR, NOT, XOR)
- logical
- shift left

• halt.

Use the basic machine-code operations above when machine-code instructions are expressed in mnemonic formassembly language, using immediate and direct addressing.

Types of Question:

- Values in Registers + Tracing.
- Write small assembly programs.
- Operand vs Opcode.

TIPS FOR SOLVING QUESTIONS

- Treat each variable as a register.
- Treat each register as a variable.
- Draw arrows on Assembly Code.
- Memorise the common If and While structure.
- Think about your code in a language you're used to.
- Practice, Practice, Practice.

BUBBLE SORT

```
data = [2, 47, 4, 23, 9, 11]
n = 6
white True:
  current_index = 0
  num_swaps = 0
   while n >= (current_index + 2)
    next_index = current_index + 1
     current = data[current_index]
     next = data[next_index]
    if current > next: # So we should swap
       data[current_index] = next
       data[next_index] = current
       num_swaps = num_swaps + 1
    current_index = next_index
  if num_swaps == 0:
    break # Otherwise do the loop again
```

9 10