Salvator-X schematics Revision History

Date	SALVATOR-X_REVxxx.DSN	•					formation			
of of		Page	Description	Before the	ne changes		or Addition		Delete	Notes
Issue				REF#	Part Number	REF#	Part Number	REF#	Part Number	
board	updated to revision 1.00. Type numb	per changed to RTP0RC7795SIPB0011S								
11/1	DOE: D. Carl IS. NEW, DOWA	Connection about and	The power voltage supplied to VDDQVA_SD1 and VDDQVA_SD2 on the SiP changed from VLDO SD1 and VLDO SD2 to D1.8V because the power	1						
9	P05: R-CarH3_NEW_POW1	Connection changed	supplied to the eMMC is D1.8V.	ſ	-	-	-	-	-	
			The resistors added so that D1.8V and VLDO SD1/VLDO SD 2 can be							
			switched in consideration of cases where SD1 and SD2 on the expansion	f ⁻	-	R790,R791	MCR01MZPJ000	-	-	
	DAG BU ABOB	0	VREF handling and RSET constant changed according to ADV7123最新デー			D44	AD4500ADT7			
	P16: DU_ARGB	Components replaced	ターシートRev.D	<u>[</u>	-	D44	AD1580ARTZ	-	-	
			VREF handling: AD1580, 1kΩ added,	<u> -</u>	-	R789	MCR01MZPF1001	-	-	
			Constant changed: 0.1uF -> 1uF	C372	GCM155R71A104KA55	C372	GCM155C71A105KE38	-	-	
			RSET constant changed from 560Ω to 536Ω (sinice the nominal resistance has	R238	MCR03EZPJ561	R238	MCR03MZPF5360	-	-	
	-	Mounting specification of components	no 530Ω, the frequency response was taken)							
	P19: P19: MIPI CSI-2_VIN	changed components changed	R632 changed to "not mounted"	f ⁻	-	-	-	-	-	
	DOS. DOWED		For R-CarM3: A trimmer resistor added so that DDR0 1.1V and DDR1 1.1V			VP0 VP4	OT 0T4000			
	P25: POWER	Components replaced	will be variable	<u> </u>	=	VR3,VR4	ST-2TA203	=	-	
			R450 and R454 changed from 15k Ω ro 10k Ω	R450,R454	MCR01MZPF1502	R450,R454	MCR01MZPJ103	-	-	
			R451 and R455 changed from 39kΩ ro 15kΩ	R451,R455	MCR01MZPF3902	R451,R455	MCR01MZPF1502	-	=	
			For the 2nd-cut PMIC: Locations of the output capacitors for the VDD_DVFS	i .						
		Connection changed	and VDD power supplies changed. Locations of the current detection amplifier	í-	-	-	-	-	-	
	1		resistors (R417 and R432) for DVFS0.8V and VDD0.8V changed so that more output capacitors are allocated on the SoC.	İ						
4011	D07: D 0-:110 1 2222 2011	E				L68,L69,L70,L71,L72,L108,L1				
12/1	P07: R-CarH3_LPDDR_POW	Error corrected	An error in the LQG15HH2N0S02 rated current corrected; 300mA -> 900mA	<u>i⁻</u>	<u> </u>	09	-		-	
	P25: POWER	Connection changed	The input to VD25_IN (pin 24 of U72) changed from D3.3V to VSYS so that the							
		ů	power sequence of VLDO 2.5V can be changed as required.	- 	-			ļ-		
12/1	P05: R-CarH3_NEW_POW1	Components replaced	Inductors changed to ferrite beads to improve analog power supply pattern	L3,L4,L5,L6,L8	MLF1608A1R0JTD25	L3,L4,L5,L6,L8	BLM15AX601SZ1D	-	-	
	1		A 1-uF capacitor added to each power supply of the EtherAVB (VDDQ25_ETH)	i -	-	C952,C953,C954,C955,C956	GCM155C71A105KE38	-	-	
12/2	 		and SDIH (VDDQVA SDx) Capacitors added to improve the DDR1.8V power suppy to support both of the		1		1			
5	P07: R-CarH3_LPDDR_POW	Components added	R-Car H3 and M3	i ⁻	-	C957,C958	GRM21BR61E226ME44	-	-	
ľ			it dan it dana mo	[-	-	C959,C960,C961,C962	GCM32ER70J476KE19	-	-	
	P06: R-CarH3_NEW_POW2	Components added	A capacitor added to improve the DVFS0.8V power supply	-	-	C963	GCM21BC71C106KE36	-	-	
1/14				<u>-</u>	-	C964,C965,C966	GCM32ER70J476KE19	-	-	
	P25: POWER	Components replaced	Resistance of the current sense part and capacitance changed to improve the	R425	MCR01MZPF6200	R425	MCR01MZPF4700	_	_	
	25.1 5112.1	Componente replacea	DVFS0.8V power supply							
			$620\Omega -> 470\Omega$, $1M\Omega -> 10k\Omega$ Locations of the capacitors for the SDHI (VDDQVA_SDx) changed;	R426	MCR01MZPF1004	R426	MCR01MZPF1002	-	-	
1/13	P05: R-CarH3 NEW POW1	Components replaced	0.1uF(C126, C127, C128, C129) moved to the back of U1 and 1uF(C953,	L3,L4,L5,L6,L8	MLF1608A1R0JTD25	L3,L4,L5,L6,L8	BLM15AX601SZ1D	_		
1,10	1 00. 11 00.110_112.V_1 0VV1	Componente replaced	C954, C955, C956) moved to out of U1.	1	WEI 1000/THOUTBEO	20,24,20,20,20	BEW 107 0100 102 1B			
			A capacitor for EtherAVB (VDDQ25 ETH) deleted	1-	-	-	-	C97	GCM155R71A104KA55	
			The capacitance of a capacitor for EtherAVB (VDDQ25_ETH) changed	C949	GCM155R71E103KA37	C949	GCM155R71A104KA55	-	-	
1/14	P25: POWER	Components deleted	The resistors for initial verification deleted.	<u>-</u>	-	-	-	R781,R782,R783,R784	MCR03EZPJ000	
			The patterns for R422, R427, and R441 are left unchanged so that they can be	1-	-	-	-	R422,R427,R441	MCR01MZPJ000	
		_	modified. The capacitance of capacitors changed to improve the DDR1.1V power supply	 	+		-	+		
1/15	P07: R-CarH3_LPDDR_POW	Components replaced	to support support both of the R-Car H3 and M3 (based on the simulation	C612-C615, C667-C670	GCM155R71A104KA55	C612-C615, C667-C670	GCM155C71A105KE38	-	-	
			to support support both of the fit out his this time (subsect of the simulation	C624-C631, C679-C686	GCM155R71A104KA55	C624-C631, C679-C686	GCM155R71E103KA37	-	-	
				C632-C637, C687-C692	GCM155R71A104KA55	C632-C637, C687-C692	GCM155R71H472KA37	-	-	
				10032-0037, 0007-0092	GCIVITION TO TRADO	0002-0007, 0007-0002				
				C638-C666, C693-C733	GCM155R71A104KA55	C638-C666, C693-C733	GCM155R71H102KA37	-	-	
								-	-	
1/18	P12: USB2.0	Mounting specification of components		C638-C666, C693-C733	GCM155R71A104KA55	C638-C666, C693-C733	GCM155R71H102KA37	-	-	
		changed components changed	"mounted".	C638-C666, C693-C733	GCM155R71A104KA55	C638-C666, C693-C733 C269,C273	GCM155R71H102KA37	-	-	
	P22: DEBUG_SCIF/LED/TactSW	changed components changed Connection changed	"mounted". ExPAD(pin29) added to the component library of CP2102 and connected to	C638-C666, C693-C733 C269,C273	GCM155R71A104KA55 GRM155R61A475MEAA -	C638-C666, C693-C733 C269,C273 - U42,U44	GCM155R71H102KA37 GCM155C71A105KE38 -	-	-	
		changed components changed	"mounted".	C638-C666, C693-C733	GCM155R71A104KA55	C638-C666, C693-C733 C269,C273	GCM155R71H102KA37	-	-	
1/20	P22: DEBUG_SCIF/LED/TactSW	changed components changed Connection changed	"mounted". EXPAD(pin29) added to the component library of CP2102 and connected to Type number changed: 5P49V5923A -> 5P49V5923B	C638-C666, C693-C733 C269,C273	GCM155R71A104KA55 GRM155R61A475MEAA -	C638-C666, C693-C733 C269,C273 - U42,U44	GCM155R71H102KA37 GCM155C71A105KE38 -	-	-	
1/20	P22: DEBUG_SCIF/LED/TactSW P03: R-CarH3_DU/LBSC P03: R-CarH3_DU/LBSC	changed components changed Connection changed Component replaced Component added	"mounted". EXPAD(pin29) added to the component library of CP2102 and connected to Type number changed: 5P49V5923A -> 5P49V5923B A resistor added so that Versaclock5(U61) OUT2 can be input to DU_DOTCLKIN2 of the H3SiP(U1). This is to make analog RGB output software-compatible between R-CarH3 and M3.	C638-C666, C693-C733 C269,C273 - - U61	GCM155R71A104KA55 GRM155R61A475MEAA - - 5P49V5923A	C638-C666, C693-C733 C269,C273 - U42,U44 U61 R792	GCM155R71H102KA37 GCM155C71A105KE38 - - 5P49V5923B MCR01MZPJ330	-	-	
1/20 1/22 2/4	P22: DEBUG SCIF/LED/TactSW P03: R-CarH3 DU/LBSC P03: R-CarH3_DU/LBSC P03: R-CarH3_DU/LBSC	changed components changed Connection changed Component replaced Component added Components replaced	"mounted". EXPAD(pin29) added to the component library of CP2102 and connected to Type number changed: 5P49V5923A -> 5P49V5923B A resistor added so that Versaclock5(U61) OUT2 can be input to DU_DOTCLKIN2 of the H3SiP(U1). This is to make analog RGB output software-compatible between R-CarH3 and M3. The capacitance of capacitors C744 and C745 changed from 27pF to 24pF.	C638-C666, C693-C733 C269,C273 - - U61 - C744,C745	GCM155R71A104KA55 GRM155R61A475MEAA - - - 5P49V5923A - GCM1555C1H270JA16	C638-C666, C693-C733 C269,C273 - U42,U44 U61 R792 C745,C745	GCM155R71H102KA37 GCM155C71A105KE38 - - - 5P49V5923B MCR01MZPJ330 GCM1555C1H240JA16	-	-	
1/20 1/22 2/4	P22: DEBUG SCIF/LED/TactSW P03: R-CarH3_DU/LBSC P03: R-CarH3_DU/LBSC P03: R-CarH3_DU/LBSC P11: Serial ATA	changed components changed Connection changed Component replaced Component added Components replaced Components replaced Components replaced	"mounted". EXPAD(pin29) added to the component library of CP2102 and connected to Type number changed: 5P49V5923A -> 5P49V5923B A resistor added so that Versaclock5(U61) OUT2 can be input to DU_DOTCLKIN2 of the H3SiP(U1). This is to make analog RGB output software-compatible between R-CarH3 and M3. The capacitance of capacitors C744 and C745 changed from 27pF to 24pF. The capacitance of capacitors C324 and C325 changed from 16pF to 30pF.	C638-C666, C693-C733 C269,C273 - - U61	GCM155R71A104KA55 GRM155R61A475MEAA - - 5P49V5923A	C638-C666, C693-C733 C269,C273 - U42,U44 U61 R792 C745,C745 C324,C325	GCM155R71H102KA37 GCM155C71A105KE38 - - 5P49V5923B MCR01MZPJ330	-	-	
1/20 1/22 2/4 4/4	P22: DEBUG SCIF/LED/TactSW P03: R-CarH3 DU/LBSC P03: R-CarH3_DU/LBSC P03: R-CarH3_DU/LBSC P11: Serial ATA P02: R-CarH3_SD/QSPI	changed components changed Connection changed Component replaced Component added Components replaced Components replaced Pin name changed	"mounted". EXPAD(pin29) added to the component library of CP2102 and connected to Type number changed: 5P49V5923A -> 5P49V5923B A resistor added so that Versaclock5(U61) OUT2 can be input to DU_DOTCLKIN2 of the H3SiP(U1). This is to make analog RGB output software-compatible between R-CarH3 and M3. The capacitance of capacitors C744 and C745 changed from 27pF to 24pF. The capacitance of capacitors C324 and C325 changed from 16pF to 30pF. TANS deleted from pin H39 of U1	C638-C666, C693-C733 C269,C273 - - U61 - C744,C745	GCM155R71A104KA55 GRM155R61A475MEAA - - - 5P49V5923A - GCM1555C1H270JA16	C638-C666, C693-C733 C269,C273 - U42,U44 U61 R792 C745,C745 C324,C325 -	GCM155R71H102KA37 GCM155C71A105KE38 - - - 5P49V5923B MCR01MZPJ330 GCM1555C1H240JA16	-	-	
1/20 1/22 2/4 4/4	P22: DEBUG SCIF/LED/TactSW P03: R-CarH3_DU/LBSC P03: R-CarH3_DU/LBSC P03: R-CarH3_DU/LBSC P11: Serial ATA	changed components changed Connection changed Component replaced Component added Components replaced Components replaced Components replaced	"mounted". EXPAD(pin29) added to the component library of CP2102 and connected to Type number changed: 5P49V5923A -> 5P49V5923B A resistor added so that Versaclock5(U61) OUT2 can be input to DU_DOTCLKIN2 of the H3SIP(U1). This is to make analog RGB output software-compatible between R-CarH3 and M3. The capacitance of capacitors C744 and C745 changed from 27pF to 24pF. The capacitance of capacitors C324 and C325 changed from 16pF to 30pF. TANS deleted from pin H39 of U1 TANS deleted from pin G39 of U1	C638-C666, C693-C733 C269,C273 - - U61 - C744,C745	GCM155R71A104KA55 GRM155R61A475MEAA - - - 5P49V5923A - GCM1555C1H270JA16	C638-C666, C693-C733 C269,C273 - U42,U44 U61 R792 C745,C745 C324,C325	GCM155R71H102KA37 GCM155C71A105KE38 - - - 5P49V5923B MCR01MZPJ330 GCM1555C1H240JA16	-	-	
1/20 1/22 2/4 4/4	P22: DEBUG SCIF/LED/TactSW P03: R-CarH3 DU/LBSC P03: R-CarH3_DU/LBSC P03: R-CarH3_DU/LBSC P11: Serial ATA P02: R-CarH3_SD/QSPI P03: R-CarH3_DU/LBSC	changed components changed Connection changed Component replaced Component added Components replaced Components replaced Pin name changed Pin name changed	"mounted". EXPAD(pin29) added to the component library of CP2102 and connected to Type number changed: 5P49V5923A -> 5P49V5923B A resistor added so that Versaclock5(U61) OUT2 can be input to DU_DOTCLKIN2 of the H3SiP(U1). This is to make analog RGB output software-compatible between R-CarH3 and M3. The capacitance of capacitors C744 and C745 changed from 27pF to 24pF. The capacitance of capacitors C324 and C325 changed from 16pF to 30pF. TANS deleted from pin G39 of U1 TANS deleted from pin G39 of U1 A20 to A25 deleted from the U1 symbol	C638-C666, C693-C733 C269,C273 - - U61 - C744,C745	GCM155R71A104KA55 GRM155R61A475MEAA - - - 5P49V5923A - GCM1555C1H270JA16	C638-C666, C693-C733 C269,C273 - U42,U44 U61 R792 C745,C745 C324,C325 -	GCM155R71H102KA37 GCM155C71A105KE38 - - - 5P49V5923B MCR01MZPJ330 GCM1555C1H240JA16	- - - - - - - - -	-	
1/20 1/22 2/4 4/4	P22: DEBUG SCIF/LED/TactSW P03: R-CarH3 DU/LBSC P03: R-CarH3 DU/LBSC P03: R-CarH3 DU/LBSC P11: Serial ATA P02: R-CarH3 SD/QSPI P03: R-CarH3 DU/LBSC P18: LVDS0	changed components changed Connection changed Component replaced Component added Components replaced Components replaced Pin name changed Pin name changed Commont(s) changed	"mounted". EXPAD(pin29) added to the component library of CP2102 and connected to Type number changed: 5P49V5923A -> 5P49V5923B A resistor added so that Versaclock5(U61) OUT2 can be input to DU_DOTCLKIN2 of the H3SIP(U1). This is to make analog RGB output software-compatible between R-CarH3 and M3. The capacitance of capacitors C744 and C745 changed from 27pF to 24pF. The capacitance of capacitors C324 and C325 changed from 16pF to 30pF. TANS deleted from pin H39 of U1 TANS deleted from pin G39 of U1	C638-C666, C693-C733 C269,C273 - - U61 - C744,C745	GCM155R71A104KA55 GRM155R61A475MEAA - - - 5P49V5923A - GCM1555C1H270JA16	C638-C666, C693-C733 C269,C273 - U42,U44 U61 R792 C745,C745 C324,C325	GCM155R71H102KA37 GCM155C71A105KE38 - - - 5P49V5923B MCR01MZPJ330 GCM1555C1H240JA16	- - - - - - - - - -		
1/20 1/22 2/4 4/4	P22: DEBUG SCIF/LED/TactSW P03: R-CarH3 DU/LBSC P03: R-CarH3_DU/LBSC P03: R-CarH3_DU/LBSC P11: Serial ATA P02: R-CarH3_SD/QSPI P03: R-CarH3_DU/LBSC	changed components changed Connection changed Component replaced Component added Components replaced Components replaced Pin name changed Pin name changed Commont(s) changed	"mounted". EXPAD(pin29) added to the component library of CP2102 and connected to Type number changed: 5P49V5923A -> 5P49V5923B A resistor added so that Versaclock5(U61) OUT2 can be input to DU_DOTCLKIN2 of the H3SiP(U1). This is to make analog RGB output software-compatible between R-CarH3 and M3. The capacitance of capacitors C744 and C745 changed from 27pF to 24pF. The capacitance of capacitors C324 and C325 changed from 16pF to 30pF. TANS deleted from pin H39 of U1 TANS deleted from the U1 symbol A21 deleted from the U1 symbol	C638-C666, C693-C733 C269,C273 - - U61 - C744,C745	GCM155R71A104KA55 GRM155R61A475MEAA - - - 5P49V5923A - GCM1555C1H270JA16	C638-C666, C693-C733 C269,C273 - U42,U44 U61 R792 C745,C745 C324,C325	GCM155R71H102KA37 GCM155C71A105KE38 - - - 5P49V5923B MCR01MZPJ330 GCM1555C1H240JA16	- - - - - - - - - - - -	-	
1/20 1/22 2/4 4/4	P22: DEBUG SCIF/LED/TactSW P03: R-CarH3 DU/LBSC P03: R-CarH3 DU/LBSC P03: R-CarH3 DU/LBSC P11: Serial ATA P02: R-CarH3 SD/QSPI P03: R-CarH3 DU/LBSC P18: LVDS0 P20: EtherAVB(GbPHY, PHY CN) P03: R-CarH3 DU/LBSC	changed components changed Connection changed Component replaced Component added Components replaced Components replaced Pin name changed Pin name changed Comment(s) changed Comment(s) changed Pin name changed	"mounted". EXPAD(pin29) added to the component library of CP2102 and connected to Type number changed: 5P49V5923A -> 5P49V5923B A resistor added so that Versaclock5(U61) OUT2 can be input to DU_DOTCLKIN2 of the H3SiP(U1). This is to make analog RGB output software-compatible between R-CarH3 and M3. The capacitance of capacitors C744 and C745 changed from 27pF to 24pF. The capacitance of capacitors C324 and C325 changed from 16pF to 30pF. TANS deleted from pin H39 of U1 TANS deleted from pin G39 of U1 A20 to A25 deleted from the U1 symbol A21 deleted from the comment A22 deleted from the comment A26 deleted from the F4 pin of U1 MD24 deleted from the M1 pin of U1	C638-C666, C693-C733 C269,C273 - - U61 - C744,C745	GCM155R71A104KA55 GRM155R61A475MEAA - - - 5P49V5923A - GCM1555C1H270JA16	C638-C666, C693-C733 C269,C273 - U42,U44 U61 R792 C745,C745 C324,C325	GCM155R71H102KA37 GCM155C71A105KE38 - - - 5P49V5923B MCR01MZPJ330 GCM1555C1H240JA16	- - - - - - - - - - - - - - - -		
1/20 1/22 2/4 4/4 4/27	P22: DEBUG SCIF/LED/TactSW P03: R-CarH3 DU/LBSC P03: R-CarH3 DU/LBSC P03: R-CarH3 DU/LBSC P11: Serial ATA P02: R-CarH3 SD/QSPI P03: R-CarH3 DU/LBSC P18: LVDS0 P20: EtherAVB(GbPHY, PHY CN) P03: R-CarH3 DU/LBSC P06: R-CarH3 NEW_POW2	changed components changed Connection changed Component replaced Component added Components replaced Components replaced Pin name changed Pin name changed Comment(s) changed Comment(s) changed Pin name changed Pin name changed Pin name changed Pin name changed	"mounted". EXPAD(pin29) added to the component library of CP2102 and connected to Type number changed: 5P49V5923A -> 5P49V5923B A resistor added so that Versaclock5(U61) OUT2 can be input to DU_DOTCLKIN2 of the H3SiP(U1). This is to make analog RGB output software-compatible between R-CarH3 and M3. The capacitance of capacitors C324 and C325 changed from 27pF to 24pF. The capacitance of capacitors C324 and C325 changed from 16pF to 30pF. TANS deleted from pin H39 of U1 TANS deleted from pin G39 of U1 A20 to A25 deleted from the U1 symbol A21 deleted from the comment A22 deleted from the comment A26 deleted from the F4 pin of U1 MD24 deleted from the M1 pin of U1 The AL6, AM6, and AK6 pins of U1 changed to VSS	C638-C666, C693-C733 C269,C273 - - U61 - C744,C745	GCM155R71A104KA55 GRM155R61A475MEAA - - - 5P49V5923A - GCM1555C1H270JA16	C638-C666, C693-C733 C269,C273 - U42,U44 U61 R792 C745,C745 C324,C325	GCM155R71H102KA37 GCM155C71A105KE38 - - - 5P49V5923B MCR01MZPJ330 GCM1555C1H240JA16 GCM1555C1H300JA16 - -			
1/20 1/22 2/4 4/4 4/27	P22: DEBUG SCIF/LED/TactSW P03: R-CarH3 DU/LBSC P03: R-CarH3 DU/LBSC P03: R-CarH3 DU/LBSC P11: Serial ATA P02: R-CarH3 SD/QSPI P03: R-CarH3 DU/LBSC P18: LVDS0 P20: EtherAVB(GbPHY, PHY CN) P03: R-CarH3_DU/LBSC P18: LVDS0 P20: EtherAVB(GbPHY, PHY CN) P03: R-CarH3_DU/LBSC P06: R-CarH3_NEW_POW2 P08: MODE_SW	changed components changed Connection changed Component replaced Component added Components replaced Components replaced Pin name changed Pin name changed Comment(s) changed Pin name changed Pin name changed Pin name changed Pin name changed Comment(s) changed Comment(s) changed Comment(s) changed Comment(s) changed	"mounted". EXPAD(pin29) added to the component library of CP2102 and connected to Type number changed: 5P49V5923A -> 5P49V5923B A resistor added so that Versaclock5(U61) OUT2 can be input to DU_DOTCLKIN2 of the H3SiP(U1). This is to make analog RGB output software-compatible between R-CarH3 and M3. The capacitance of capacitors C744 and C745 changed from 27pF to 24pF. The capacitance of capacitors C324 and C325 changed from 16pF to 30pF. TANS deleted from pin G39 of U1 TANS deleted from pin G39 of U1 A20 to A25 deleted from the U1 symbol A21 deleted from the comment A22 deleted from the comment A26 deleted from the F4 pin of U1 MD24 deleted from the M1 pin of U1 The AL6, AM6, and AK6 pins of U1 changed to VSS MD24 deleted from the comment	C638-C666, C693-C733 C269,C273	GCM155R71A104KA55 GRM155R61A475MEAA - - - 5P49V5923A - GCM1555C1H270JA16 GCM1555C1H160JA16 - - - - -	C638-C666, C693-C733 C269,C273 - U42,U44 U61 R792 C745,C745 C324,C325	GCM155R71H102KA37 GCM155C71A105KE38 - - - 5P49V5923B MCR01MZPJ330 GCM1555C1H240JA16 GCM1555C1H300JA16 - - -			
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	Page	Description	Before	the changes		ts information sed or Addition		Delete	Notes
4	. 250	Doddipadii	REF#	Part Number	REF#	Part Number	REF#	Part Number	
	mber changed to RTP0RC7795SIPB0012								
P02: R-CarH3_SD/QSPI	Components replaced Component replaced	62Ω added to all the signals of MMC0 22Ω for the MMC0 CLK signal changed to 62Ω	- R9	- MCR01MZPJ220	R795-R804 R9	MCR01MZPJ620 MCR01MZPJ620	-		
	Component replaced Comment(s) changed	MMC0 signals: 50-Ω impedance	-	-	-	- WICKU WIZF 3020	-		
P03: R-CarH3 DU/LBSC	Component replaced	The crystal resonator X3 changed to 16.6600 MHz	V2	NX2016SA-16.6666MHz-	V2	NX2016SA-16.66MHz-CHP-			
F03. R-Cairi3_D0/LB3C		The dystal resolution A3 changed to 10.0000 Minz	۸۵	CHP-CZS-39	۸۵	CZS-41	-	-	
	Component replaced	The crystal oscillator X4 changed to 16.6600 MHz	X4	NZ2520SB-16.6666MHz-	X4	NZ2016SH-16.66MHz- NSC5021A	-	-	
P04: R-CarH3 USB/HDMI	Connection changed	CSI2-CH3 deleted, so U1 connection signals and comment deleted		RNAxxxx		NSC5021A			
T 64. TO Garrio_GGB/TIDIMI	Commodion onlyinged	CSI3 DATAP0(AK4), CSI3 DATAN0(AK3), CSI3 CLKP(AL4),	-	-	-	-	-	-	
	Component replaced	The components connected to U1 deleted due to deletion of CSI2-CH3,					Dos	MODO4M7DE4004	
		R65(U1.AR6)	-	-	-	-	R65	MCR01MZPF4021	
	Connection changed								
		SATA deleted, so U1 connection signals and comment deleted							
		SATA_CLK_P(AW20), SATA_CLK_M(AW21), SATA_RX_P(AW23),	-	-	-	-	-	-	
		SATA_RX_M(AW22), SATA_TX_P(AW24), SATA_TX_M(AW25)							
	Components replaced	The components connected to U1 deleted due to deletion of SATA					R58	MCR01MZPF2000	
		R58(U1.AN27), C33(U1.AW24), C35(U1.AW25)	-	-			C33, C35	GCM155R71E103KA37	+
	Connection changed		-	-			030, 030	GCW135K7 TE 105KA57	†
	g	USB3.0-CH1 deleted, so U1 connection signals deleted and comment changed USB3S1 CLK P(AT36), USB3S1 CLK M(AU36), USB3S1 RX P(AV39),	_				_	_	
		USB3S1 RX M(AU39), USB3S1 TX P(AW36), USB3S1 TX M(AW37)							
	Components replaced	The following components connected to U1 deleted due to deletion of USB3.0-							
		CH1	<u> -</u>	-	<u> -</u>	<u> </u>	C30, C31	GCM155R71A104KA55	<u> </u>
	Connection changed	UT connection signals changed due to deletion of USB3.0-CHT							
		USB23_DP(AU34), USB23_DM(AT34), USB23_ID(AP34),							<u> </u>
	Component replaced	SW31 added to branch the GP6_30 and GP6_31 signals to CSI-VIN and	-	_	SW31	CHS-08A	_		
DOE: D. OIIO. MEM. T	O company time of	USB2.0-CH3			57751	OTTO OUT			1
P05: R-CarH3_NEW_POW1	Connection changed	The following deleted functions are disconnected from the power supply pins The components left changed because they are to be used for other pins.							
		CSI2-CH3: VDDQ18_CSI3, VDDQ09_CSI3 USB3.0-CH1: VDD09_USB31	-	-	-	-	-	-	
	Components replaced	SATA and USB3.0-CH1 connection to the power supply pins and components					C38, C94, C800	GCM155R71A104KA55	
		deleted						GCM155R71E103KA37	
			-	-	-		C52 C50	GCM155K7 TE 103KA37 GCM188C71A225KE02	
			-	-	-	-	L3	BLM15AX601SZ1D	
P08: MODE_SW	Connection changed	MD28 connected to 8-pin SW12 and R117 deleted	-	-	-	-	R117	MCR03EZPJ103	
P10: PCI-E ch0/ch1	Component replaced	CP18 deleted	-	-	-	-	CP18	-	
	Signal name changed	The signal names of SATA and PCIE1 changed because they are to be							
		connected to the switching circuit.							
		IO_EX7 -> PCIE1_SATA_SEL_18							
		PCIE1_RX_M -> PCIE1_CN_RX_M PCIE1_RX_P -> PCIE1_CN_RX_P	-	-	-	-	-	-	
		PCIE1 TX M -> PCIE1 CN TX M							
		PCIE1_TX_P -> PCIE1_CN_TX_P							
P11: Serial ATA	Connection changed	U11 connection signals deleted due to deletion of SATA and USB3-CH1							
	-	SATA_CLK_P(15pin), SATA_CLK_M(16pin), USB3S1_CLK_P(44pin),	-	-	-	-	-	-	
		USB3S1_CLK_M(45pin)							
	Components replaced	The pull-up resistors for vOE0# and vOE7# of U11 added, and comment			D700 D704	MODOMAZDIAGO			
			<u> </u> -	-	R793, R794	MCR01MZPJ103	-	-	
		changed due to deletion of SATA and USB3-CH1, R793-R794(10k)							
	Components replaced	changed due to deletion of SATA and USB3-CH1, R793-R794(10k) SATA/PCIET switching circuit added, and the control signal changed based on	-	-	U97	MAX4888BETI+	-	-	
	Components replaced	SATA/PCIET switching circuit added, and the control signal changed based on Kriek	-	-	U97	MAX488BETI+	-	-	+
	Components replaced	• , ,	-	-	U98	HD74LV1GT08A	-	-	
		SATAPCIET switching circuit added, and the control signal changed based on Kriek The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added	-	-			-	-	
P13: USB3.0	Components replaced Connection changed	SATAPCIET switching circuit added, and the control signal changed based on Kriek The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1	-	-	U98	HD74LV1GT08A	-	- - -	
P13: USB3.0	Connection changed	SATAPCIET switching circuit added, and the control signal changed based on Kriek The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS	-	-	U98	HD74LV1GT08A	-	- - -	
P13: USB3.0		SATAPCIET switching circuit added, and the control signal changed based on Kriek. The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added. The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_D, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed	-	-	U98	HD74LV1GT08A	- - - - -	- - - - TCE1608G-900-4P	
P13: USB3.0	Connection changed	SATAPCIET switching circuit added, and the control signal changed based on Kriek. The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added. The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1	-	-	U98	HD74LV1GT08A	- - - - L66	- - - - TCE1608G-900-4P	
P13: USB3.0	Connection changed	SATAPCIET switching circuit added, and the control signal changed based on Kriek. The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added. The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped	- - - - CN12		U98 C972-C977 -	HD74LV1GT08A	- - - - L66	- - - - TCE1608G-900-4P	
P13: USB3.0	Connection changed Component replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections	- - - - CN12	- - - - - CMS1821-010010	U98 C972-C977 CN37	HD74LV1GT08A GCM155R71A104KA55 ZX62D-AB-5P8	- - - - L66	- - - - TCE1608G-900-4P	
P13: USB3.0	Connection changed	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3	CN12 -		U98 C972-C977 CN37 U96	HD74LV1GT08A GCM155R71A104KA55 ZX62D-AB-5P8 BD82065FVJ	- - - - L66	- - - - TCE1608G-900-4P	
P13: USB3.0	Connection changed Component replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections			U98 C972-C977 CN37	HD74LV1GT08A GCM155R71A104KA55 ZX62D-AB-5P8	- - - - L66	- - - - TCE1608G-900-4P - -	
P13: USB3.0	Connection changed Component replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, Ch12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals			U98 C972-C977 CN37 U96 C971 R805	HD74LV1GT08A GCM155R71A104KA55 - - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJ472	- - - - L66	TCE1608G-900-4P	
P13: USB3.0	Connection changed Component replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, Ch12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals			U98 C972-C977 CN37 U96 C971 R805 R806, R807	HD74LV1GT08A GCM155R71A104KA55 - - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJ472 MCR01MZPJ000		TCE1608G-900-4P	
	Connection changed Component replaced Components replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriek. The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN.	- - - -	-	U98 C972-C977 CN37 U96 C971 R805 R806, R807 R808	HD74LV1GT08A GCM155R71A104KA55 - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJ472 MCR01MZPJ472 MCR01MZPJ2000 MCR01MZPJ202			
	Connection changed Component replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, Ch12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals	- CN12		U98 C972-C977 CN37 U96 C971 R805 R806, R807	HD74LV1GT08A GCM155R71A104KA55 - - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJ472 MCR01MZPJ000			
	Connection changed Component replaced Components replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN. An HDMI1.4 device is replaced with three HDMI2.0 devices, for each HDMI The utility pin connected to NC pin The HDMI companion chip and peripheral components changed, 10kΩ x2	- - - -	-	U98 C972-C977 CN37 U96 C971 R805 R806, R807 R808 U87, U88, U90, U91	HD74LV1GT08A GCM155R71A104KA55 - - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJ472 MCR01MZPJ400 MCR01MZPJ202 TPD4E05U06DQAR	-	- - - - - - - -	
	Connection changed Component replaced Components replaced Components replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN. An HDMI1.4 device is replaced with three HDMI2.0 devices, for each HDMI The utility pin connected to NC pin The HDMI companion chip and peripheral components changed, 10kΩ x2 (R245, R250) deleted	- - - - - - U27, U28	- - - - - TPD12S016PWR	U98 C972-C977 CN37 U96 C971 R805 R806, R807 R808 U87, U88, U90, U91 U89, U92 -	HD74LV1GT08A GCM155R71A104KA55 - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJ472 MCR01MZPJ000 MCR01MZPJ202 TPD4E05U06DQAR TPD5S116YFFR -	- L66		
P17: HDMI_OUT	Connection changed Component replaced Components replaced Components replaced Components replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN. An HDMI1.4 device is replaced with three HDMI2.0 devices, for each HDMI The utility pin connected to NC pin The HDMI companion chip and peripheral components changed, 10kΩ x2 (R245, R250) deleted 4.7UF x4 (C377,C378,C380,C381) -> 0.1uF x4 (C967-C970)	- - - - - - U27, U28 - C377, C378, C380, C381	- - - - - TPD12S016PWR - GCM21BC71E475KE36	U98 C972-C977 CN37 U96 C971 R805 R806, R807 R808 U87, U88, U90, U91 U89, U92 - C967-C970	HD74LV1GT08A GCM155R71A104KA55 - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJ472 MCR01MZPJ202 TPD4E05U06DQAR TPD5S116YFFR - GCM155R71A104KA55	-	- - - - - - - -	
P17: HDMI_OUT	Connection changed Component replaced Components replaced Components replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN. An HDMI1.4 device is replaced with three HDMI2.0 devices, for each HDMI The utility pin connected to NC pin The HDMI companion chip and peripheral components changed, 10kΩ x2 (R245, R250) deleted	- - - - - - U27, U28	- - - - - TPD12S016PWR	U98 C972-C977 CN37 U96 C971 R805 R806, R807 R808 U87, U88, U90, U91 U89, U92 -	HD74LV1GT08A GCM155R71A104KA55 - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJ472 MCR01MZPJ000 MCR01MZPJ202 TPD4E05U06DQAR TPD5S116YFFR -	-	- - - - - - - -	
P17: HDMI_OUT P19: MIPI CSI-2_VIN P21: Audio(AK4613VQ)	Connection changed Component replaced Components replaced Components replaced Components replaced Components replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN. An HDMI1.4 device is replaced with three HDMI2.0 devices, for each HDMI The utility pin connected to NC pin The HDMI companion chip and peripheral components changed, 10kΩ x2 (R245, R250) deleted 4.7-UF x4 (C377,C378,C380,C381) -> 0.1-UF x4 (C967-C970) The following components are replaced with the ones in use to support HDMI2.0: D22, D23, D25 -> U93-U95 A single two-stage audio connector is replaced with two one-stage connectors	- - - - - - - - - - - - - - - - - - -	- - - - - TPD12S016PWR - GCM21BC71E475KE36	U98 C972-C977 CN37 U96 C971 R805 R806, R807 R808 U87, U88, U90, U91 U89, U92 - C967-C970	HD74LV1GT08A GCM155R71A104KA55 - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJ472 MCR01MZPJ202 TPD4E05U06DQAR TPD5S116YFFR - GCM155R71A104KA55	-	- - - - - - - -	
P17: HDMI_OUT P19: MIPI CSI-2_VIN P21: Audio(AK4613VQ)	Connection changed Component replaced Components replaced Components replaced Components replaced Components replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN. An HDMI1.4 device is replaced with three HDMI2.0 devices, for each HDMI The utility pin connected to NC pin The HDMI companion chip and peripheral components changed, 10kΩ x2 (R245, R250) deleted 4.7UF x4 (C377,C378,C380,C381) -> 0.1uF x4 (C967-C970) The following components are replaced with the ones in use to support HDMI2.0: D22, D23, D25 -> U93-U95 A single two-stage audio connector is replaced with two one-stage connectors The signals connected to CN29 deleted because CSI2-CH3 deleted	- - - - - - - - - - - - - - - - - - -		U98 C972-C977 - - CN37 U96 C971 R805 R806, R807 R808 U87, U88, U90, U91 U89, U92 - C967-C970 U93-U95	HD74LV1GT08A GCM155R71A104KA55 - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJJ72 MCR01MZPJJ00 MCR01MZPJJ00 MCR01MZPJJ00 TPD4E05U06DQAR TPD5S116YFFR - GCM155R71A104KA55 TPD4E05U06DQAR	-	- - - - - - - -	
P17: HDMI_OUT P19: MIPI CSI-2_VIN P21: Audio(AK4613VQ)	Connection changed Component replaced Components replaced Components replaced Components replaced Components replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN. An HDMI1.4 device is replaced with three HDMI2.0 devices, for each HDMI The utility pin connected to NC pin The HDMI companion chip and peripheral components changed, 10kΩ x2 (R245, R250) deleted 4.7uF x4 (C377,C378,C380,C381) -> 0.1uF x4 (C967-C970) The following components are replaced with the ones in use to support HDMI2.0: D22, D23, D25 -> U93-U95 A single two-stage audio connector is replaced with two one-stage connectors The signals connected to CN29 deleted because CSI2-CH3 deleted CSI3_CLKN(33pin), CSI3_CLKR(35pin), CSI3_DATANN(39pin),	- - - - - - - - - - - - - - - - - - -		U98 C972-C977 - - CN37 U96 C971 R805 R806, R807 R808 U87, U88, U90, U91 U89, U92 - C967-C970 U93-U95	HD74LV1GT08A GCM155R71A104KA55 - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJJ72 MCR01MZPJJ00 MCR01MZPJJ00 MCR01MZPJJ00 TPD4E05U06DQAR TPD5S116YFFR - GCM155R71A104KA55 TPD4E05U06DQAR	-	- - - - - - - -	
P17: HDMI_OUT P19: MIPI CSI-2_VIN P21: Audio(AK4613VQ)	Connection changed Component replaced Components replaced Components replaced Components replaced Components replaced Components replaced Components replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN. An HDMI1.4 device is replaced with three HDMI2.0 devices, for each HDMI The utility pin connected to NC pin The HDMI companion chip and peripheral components changed, 10kΩ x2 (R245, R250) deleted 4.7uF x4 (C377,C378,C380,C381) -> 0.1uF x4 (C967-C970) The following components are replaced with the ones in use to support HDMI2.0: D22, D23, D25 -> U93-U95 A single two-stage audio connector is replaced with two one-stage connectors The signals connected to CN29 deleted because CSI2-CH3 deleted CSI3_CLKN(33pin), CSI3_CLKP(35pin), CSI3_DATANO(39pin), CSI3_DATAPO(41pin)	- - - - - - - - - - - - - - - - - - -		U98 C972-C977 - - CN37 U96 C971 R805 R806, R807 R808 U87, U88, U90, U91 U89, U92 - C967-C970 U93-U95	HD74LV1GT08A GCM155R71A104KA55 - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJJ72 MCR01MZPJJ00 MCR01MZPJJ00 MCR01MZPJJ00 TPD4E05U06DQAR TPD5S116YFFR - GCM155R71A104KA55 TPD4E05U06DQAR	-	- - - - - - - -	
P17: HDMI_OUT P19: MIPI CSI-2_VIN P21: Audio(AK4613VQ)	Connection changed Component replaced Components replaced Components replaced Components replaced Components replaced	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN. An HDMI1.4 device is replaced with three HDMI2.0 devices, for each HDMI The utility pin connected to NC pin The HDMI companion chip and peripheral components changed, 10kΩ x2 (R245, R250) deleted 4.7uF x4 (C377,C378,C380,C381) -> 0.1uF x4 (C967-C970) The following components are replaced with the ones in use to support HDMI2.0: D22, D23, D25 -> U93-U95 A single two-stage audio connector is replaced with two one-stage connectors The signals connected to CN29 deleted because CSI2-CH3 deleted CSI3_CLKN(33pin), CSI3_CLKR(35pin), CSI3_DATANN(39pin),	- - - - - - - - - - - - - - - - - - -		U98 C972-C977 - - CN37 U96 C971 R805 R806, R807 R808 U87, U88, U90, U91 U89, U92 - C967-C970 U93-U95	HD74LV1GT08A GCM155R71A104KA55 - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJJ72 MCR01MZPJJ00 MCR01MZPJJ00 MCR01MZPJJ00 TPD4E05U06DQAR TPD5S116YFFR - GCM155R71A104KA55 TPD4E05U06DQAR	-	- - - - - - - -	
P17: HDMI_OUT P19: MIPI CSI-2_VIN P21: Audio(AK4613VQ)	Connection changed Component replaced Components replaced Components replaced Components replaced Components replaced Components replaced Components replaced	SATAPCIET Switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN. An HDMI1.4 device is replaced with three HDMI2.0 devices, for each HDMI The utility pin connected to NC pin The HDMI companion chip and peripheral components changed, 10kΩ x2 (R245, R250) deleted 4.7uF x4 (C377,C378,C380,C381) -> 0.1uF x4 (C967-C970) The following components are replaced with the ones in use to support HDMI2.0: D22, D23, D25 -> U93-U95 A single two-stage audio connector is replaced with two one-stage connectors The signals connected to CN29 deleted because CSI2-CH3 deleted CSI3_CLKN(33pin), CSI3_CLKP(35pin), CSI3_DATAN0(39pin), CSI3_DATAPO(41pin) Name of the two interrupt signals from CSI-VIN changed because they are to be branched via SW GP6_30/INTRQ1 -> GP6_30 (CN29.58pin)	- - - - - - - - - - - - - - - - - - -		U98 C972-C977 - - CN37 U96 C971 R805 R806, R807 R808 U87, U88, U90, U91 U89, U92 - C967-C970 U93-U95	HD74LV1GT08A GCM155R71A104KA55 - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJJ72 MCR01MZPJJ00 MCR01MZPJJ00 MCR01MZPJJ00 TPD4E05U06DQAR TPD5S116YFFR - GCM155R71A104KA55 TPD4E05U06DQAR	-	- - - - - - - -	
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P13: USB3.0 P17: HDMI_OUT P19: MIPI CSI-2_VIN P21: Audio(AK4613VQ) P23: EXIO_CN/MIPI_SW	Connection changed Component replaced Components replaced Components replaced Components replaced Components replaced Components replaced Signal name changed	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN. An HDMI1.4 device is replaced with three HDMI2.0 devices, for each HDMI The utility pin connected to NC pin The HDMI companion chip and peripheral components changed, 10kΩ x2 (R245, R250) deleted 4.7uF x4 (C377,C378,C380,C381) -> 0.1uF x4 (C967-C970) The following components are replaced with the ones in use to support HDMI2.0: D22, D23, D25 -> U93-U95 A single two-stage audio connector is replaced with two one-stage connectors The signals connected to CN29 deleted because CSI2-CH3 deleted CSI3_CLKN(33pin), CSI3_CLKP(35pin), CSI3_DATAN0(39pin), CSI3_DATAP0(41pin) Name of the two interrupt signals from CSI-VIN changed because they are to be branched via SW GP6_30/INTRQ1 -> GP6_30 (CN29.58pin) GP6_31/INTRQ2 -> GP6_31 (CN29.58pin) CP68 to CP71 added to the unused pins of CN29	- - - - - - - - - - - - - - - - - - -		U98 C972-C977 - - CN37 U96 C971 R805 R806, R807 R808 U87, U88, U90, U91 U89, U92 - C967-C970 U93-U95 CN39, CN40 - CP68-CP71	HD74LV1GT08A GCM155R71A104KA55 - - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJ472 MCR01MZPJ472 MCR01MZPJ202 TPD4E05U06DQAR TPD5S116YFFR - GCM155R71A104KA55 TPD4E05U06DQAR SJ-3523-SMT -	-	- - - - - - - -	
P17: HDMI_OUT P19: MIPI CSI-2_VIN P21: Audio(AK4613VQ)	Connection changed Component replaced Components replaced Components replaced Components replaced Components replaced Components replaced Signal name changed	SATAPCIET switching circuit added, and the control signal changed based on Kriak The control signal connected to the I/O expander, 1.8V -> 3.3V switching circuit added The signal names of USB2.0 changed due to deletion of USB3.0-CH1 USB23_DP, USB23_DM, USB23_ID, USB23_VBUS The USB3-CH1 related components deleted, changed, and comment changed due to deletion of USB3-CH1 L66 deleted, CN12 changed to CN37 (USB2.0), L67 circuit diagram flipped vertically, no changes in connections BD82065FVJ and other peripheral components added to support USB2.0-CH3 Note that 0-Ω resistors are not mounted because the OVC and PWEN signals are use in HDMI-IN. An HDMI1.4 device is replaced with three HDMI2.0 devices, for each HDMI The utility pin connected to NC pin The HDMI companion chip and peripheral components changed, 10kΩ x2 (R245, R250) deleted 4.7uF x4 (C377,C378,C380,C381) -> 0.1uF x4 (C967-C970) The following components are replaced with the ones in use to support HDMI2.0: D22, D23, D25 -> U93-U95 A single two-stage audio connector is replaced with two one-stage connectors The signals connected to CN29 deleted because CSI2-CH3 deleted CSI3_CLKN(33pin), CSI3_CLKP(35pin), CSI3_DATANO(39pin), CSI3_DATAPO(41pin) Name of the two interrupt signals from CSI-VIN changed because they are to be branched via SW GP6_30/INTRQ1 -> GP6_30 (CN29.58pin) GP6_31/INTRQ1 -> GP6_31 (CN29.58pin)	- - - - - - - - - - - - - - - - - - -		U98 C972-C977 - - CN37 U96 C971 R805 R806, R807 R808 U87, U88, U90, U91 U89, U92 - C967-C970 U93-U95 CN39, CN40 -	HD74LV1GT08A GCM155R71A104KA55 - ZX62D-AB-5P8 BD82065FVJ GCM155R71A104KA55 MCR01MZPJJ72 MCR01MZPJJ00 MCR01MZPJJ00 MCR01MZPJJ00 TPD4E05U06DQAR TPD5S116YFFR - GCM155R71A104KA55 TPD4E05U06DQAR	-	- - - - - - - -	

е						s information			
e	Page	Description	Before REF#	the changes Part Number	Revis	sed or Addition Part Number	REF#	Delete Part Number	Notes
P02: R-CarH3_SD/QSPI	Comment(s) changed	Comment added to the EtherAVB signal: impedance 50Ω	-	-	-	-	-	-	
P20: EtherAVB(GbPHY, PHY CN) P02: R-CarH3 SD/QSPI	Components replaced	The resistors (R724 to R729) for the TX signal of the EtherAVB changed to	R724-R729	MCR01MZPF1000	R724-R729	MCR01MZPF24R9		_	
P04: R-CarH3 USB/HDMI	Component replaced	SW31 changed from CHS-08A to CHS-06A	SW31	CHS-08A	SW31	CHS-06A	-	-	
P10: PCI-E ch0/ch1	Connection changed	The power supply for SN74AVC4T245 (U7 ,U8, and U9) changed from D1.8V	_	_	_	_	_	_	
	Connection changed	to D1.8V PERI EN of U37 should be controlled by the PRESETn signal because D3.3V1.2V							
P20: EtherAVB(GbPHY, PHY_CN)	Connection changed	sequence corrected	-	-	-	-	-	-	
P24: POWER 5V/VSYS	Components deleted	Layout patterns for resistors (not mounted) deleted	<u>-</u>	-	-	-	R466-R469	MCR01MZPJ000	
	Connection changed	Two figures of a single point connection on the upper right changed to the	_	_	GND1,GND2	GND	-	-	
P25: POWER PMIC	Components replaced	Eagle specification Capacitors near PMIC changed from 100uF to 47uF	C533-C537 C5/3 C9/2-C9	45 GRT32ER60J107ME13	*	945 GCM32ER70J476KE19			
1 20.1 OWERT IMIO	Connection changed	Two figures of a single point connection on the upper right changed to the	0000-0001,0040,0042-004	43 GITT32ETT003T07WET3			-	-	
		Eagle specification	-	-	GND3,GND4	GND		-	
P26: POWER PMIC2	Components deleted	Layout patterns for resistors (not mounted) deleted	<u>-</u>	-	-	-	R470,R471,R716,R717	MCR03EZPJ000	
	Components deleted	Power supply measurement circuit deleted	-	_		-	J1,J2 TH6.TH7	82_MMCX-50-0-18/111_OE 0.6-1.1-1.2	
			-	-	-	-	R719,R721	MCR01MZPJ121	
			-	-	-	-	C835,C836	GCM155R71H682KA55	
	Components mayod	Capacitors for EMI and GND through holes moved from Page 24 to 26	-	-	-	-	R720,R722	MCR01MZPF51R0	
P24: POWER 5V/VSYS	Components moved	Capacitors for Einit and GND through holes moved from Page 24 to 20	C901-C912	GCM155R71H104KE02	_	-	-	_	
P26: POWER PMIC2		0	TH3,TH4	0.6-1.1-1.2	-	-	-	-	
P02: R-CarH3_SD/QSPI	Comment(s) changed	Comment added to the SDHI0 and SDHI3 signals: Impedance 50Ω	-	-	-	-	-	-	
P14: SDHI0/SDHI3 P25: POWER PMIC	Component replaced	10-k pull-down resistor (R809) added to the PWM2 signal	-	-	R809	MCR01MZPJ103	-	=	
P03: R-CarH3_DU/LBSC	Connection changed	only.							
		I2C is 5-V tolerant so D1.8V power supply creates no problems.	-	-	-	-	-	-	
	Comment(s) changed	"I2C is 3.3V signal." -> "I2C inputs are 5V tolerant."	+	-	-	-		-	
	Components replaced	D3.3V side input circuit components deleted following the change of the power	+				L74	BLM15BB121SH1D	
	,	suuply above.		-	-	-		_	
D40: DCLE = 50/=54	Connection shared	L74, C746 The power supply of SN74AVC4T245 (LIZ LIR and LIQ) changed from	 -	-	-	-	C746	GCM21BC71C106KE36	
P10: PCI-E ch0/ch1	Connection changed	The power supply of SN74AVC4T245 (U7, U8, and U9) changed from D1.8V PERI to D1.8V because PMIC changed from D3.3V to D1.8V and PERI	. -	_	_	_	_	_	
		is not required for the sequence.			-		[
P15: MMC0	Comment(s) changed	Comment under U22A (eMMC) changed	-	-	-	-	-	-	
P19: MIPI CSI-2_VIN	Connection changed	The power supply source of ADV7482W changed from D1.8V_PERI to D1.8V							
		because PMIC changed from D3.3V to D1.8V and PERI is not required for the	-	-	-	-	-	-	
	Components replaced	sequence. The generation circuit deleted because the circuit using D1.8V PERI deleted		_		_	U80	MIC94091YC6	
	Components replaced	U80, C898, C899	-	-	-	-	C898	GCM155R71A104KA55	
			-	-	-	-	C899	GCM155C71A105KE38	
P25: POWER PMIC	Symbol changed	Pin names and comments on BD9571MWV-M(U72) corrected							
		Comment on PD for pins 70 and 71 deleted (because pull-down resistors will be deleted due to update of PMIC)	3						
		Pin 51 name changed to POFFB/MRB	-	-	-	-	-	-	
		Pin 65 name changed to BKUP REQ							
		Comment on pins 53 and 54 corrected							
P26: POWER PMIC2 P25: POWER PMIC	Components replaced Components moved	EMI capacitors (C924 to C926) deleted because D1.8V PERI deleted Current measurement circuit for U58 and U59 moved from page 25 to 26.	+	-	-	-	C924-C926	GCM155R71A104KA55	
P26: POWER PMIC2	Components moved	The new page number added in each page.	-	-	-	-	-	-	
B P25: POWER PMIC	Error corrected	An error in tolerance existing at the layout pattern for 0.01uF (not mounted).			_	_		_	
		The type number and others have no errors. 16V->25V							
P01 to P26	Comment(s) changed	The cover title and circuit diagram name on the lower right of each page changed. (For common use of the board with H3 and M3)							
		Before correction: R-CarH3-SiP System Evaluation Board	-	-	_	-	_	_	
		After correction: R-CarH3-SiP/M3-SiP System Evaluation Board							
P2 to P7	Comment(s) changed	Comment on the difference of the symbol between H3 and M3 added to the symbolb U1.	<u> </u>	_	_	_	_	-	
		(For common use of the board with H3 and M3)							
P04: R-CarH3_USB/HDMI	Comment(s) changed	Comment on the difference in resistance value between H3 and M3 added to							
		CSIn_REXT. The list at the lower left of the page also changed.	-	-	-	-	-	=	
7 P25: POWER PMIC	Components replaced	(For common use of the board with H3 and M3) C934 and C936 changed to the majority capacitors on the board	+						
	Somponenta repiaced	GCM188R71C104KA37(0.1uF 1608 16V) -> GCM155R71A104KA55(0.1uF	C934,C936	GCM188R71C104KA37	C934,C936	GCM155R71A104KA55	-	-	
		1005 10V)							
DOS: D CarH2 NEW DOWS	Components replaced	An error in the tolerance corrected (C559, C563, C568, and C572): 16V→25V		- GCM188C71A225KE02	- C977	- CCASEAVEDO LAZENT	-	-	
P06: R-CarH3_NEW_POW2	Component replaced Components replaced	Capacitance of the capacitor for VDD power changed Capacitance of capacitors for DVFS power changed as follows.	C877 -	- GUNITOOU/TAZZSKEUZ	C877	CGA3E1X5R0J475KT	-	-	
	Sompononia repiaced	0.22uF->0.01uF	- C195,C198,C865,C866	GCM155R71C224KE02	C195,C198,C865,C866	GCM155R71E103KA37	-	=	
		0.022uF->0.01uF	C211,C213,C214	GCM155R71E223KA55	C211,C213,C214	GCM155R71E103KA37	-	=	
		0.047uF->0.01uF	C216	GCM155R71E473KA55	C216	GCM155R71E103KA37	-	-	
		0.047uF->0.015uF 0.047uF->0.022uF	C867,C868 C200,C870	GCM155C71A474KE36 GCM155R71E473KA55	C867,C868 C200,C870	GCM155R71E153KA55 GCM155R71E223KA55		-	
		0.047uF->0.022uF 0.01uF->0.022uF	C200,C870 C206,C210	GCM155R71E103KA37	C200,C870 C206,C210	GCM155R71E223KA55	-	-	
		0.047uF->0.033uF	C201	GCM155R71E473KA55	C201	GCM155R71E333KA55	-	-	
		0.22uF->0.033uF	C864	GCM155R71C224KE02	C864	GCM155R71E333KA55	-	-	
		0.01uF->0.047uF	C203,C209	GCM155R71E103KA37	C203,C209	GCM155R71E473KA55	-	-	
		0.01uF->0.068uF 0.01uF->0.1uF	C205 C204	GCM155R71E103KA37 GCM155R71E103KA37	C205 C204	GCM155R71C683KA55 GCM155R71A104KA55	-	-	
		0.47uF->0.1uF	C869	GCM155C71A474KE36	C869	GCM155R71A104KA55	<u> </u>	<u>-</u>	
		0.047uF->0.22uF	C871	GCM155R71E473KA55	C871	GCM155R71C224KE02	-	-	
i .		0.022uF->0.47uF	C872,C873	GCM155R71E223KA55	C872,C873	GCM155C71A474KE36	-	-	
	Mounting specification of componen	nts C966 47uF mounted	-	-	-	-	-	-	
	alanaman and an anaman and a street and a street	1	D0 D705 D004	MCR01MZPJ620	R9,R795-R804	MCR01MZPJ330	-	-	
P02: R-CarH3 SD/OSPI	changed components changed Components replaced	eMMC dumping resistors changed: 620 -> 330	IR9.R795-R804				1	į.	+
P02: R-CarH3_SD/QSPI	Components replaced	eMMC dumping resistors changed: 62Ω -> 33Ω Line impedance changed from 50ohm to "within 75ohm"	R9,R795-R804	INIOROTINIZI 3020	710,711 00 1100 1				
P02: R-CarH3_SD/QSPI		Line impedance changed from 50ohm to "within 75ohm' Comment added: The distance between SiP and resistor(s) should be withn	- -	-	-	-	-	-	
	Components replaced Comment(s) changed	Line impedance changed from 50ohm to "within 75ohm' Comment added: The distance between SiP and resistor(s) should be withn 20mm.	-	-	-	-	-	-	
P02: R-CarH3_SD/QSPI P01 to P26	Components replaced	Line impedance changed from 50ohm to "within 75ohm' Comment added: The distance between SiP and resistor(s) should be withn	- -	-	-	-	-	-	

P25: POWER	Components replaced Components replaced Components replaced Comment(s) changed Comment(s) changed Components replaced Connection changed Component replaced Symbol modified Component replaced Connection changed Component replaced Component replaced Component replaced Connection changed Connection changed Connection changed Connection changed Connection changed	Description An error in the pull-up power supply of the Versaclock5 SD/OE pin corrected. from D3.3V to D1.8V) Type numbers of crystal oscillator/resonator changed (16.66MHz -> 16.64MHz) The type number of coils (L53 and L54) for DVFS and VDD changed. Ref numbers also changed. (L110, L111) Target: DVFS0.8V, VDD0.8V TYP changed from 0.8125V to 0.82V, 0.600V-1.100V deleted. Target: D1.8V, D3.3V, VLD02.5V Comment on PMIC voltage range deleted An error in dimension of connectors in the location figure corrected from 1.2mm to 12mm. A vertical line deleted. The type number of coils (L55 to L58) for DDR0_1.1V, DDR1_1.1V, D1.8V, and D3.3V changed. Ref numbers remain unchanged. TX/RX connection of U97(PCIE signal SW) swapped in accord with the PCB The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the result of the PI simulation.	REF# - X3 X4 L53 L54	Part Number	REF# - X3 X4 L110 L111 L55, L56 L57, L58 - X3 - U61	r Addition Part Number - NX2016SA-16.64MHz-CHP- CZS-41 NZ2016SH-16.64MHz- RNA5013C SPM6545VT-R15M-D - - TFM201610ALMAR24MTAA TFM201610ALMAR33MTAA - NX2016SA-16.64MHz-CHP- CZS-53 -	REF#	Part Number	Notes
7 P25: POWER P21: Audio(AK4613VQ) 1/1 P25: POWER 1/7 P11: Serial ATA 1/1 P03: R-CarH3_DU/LBSC 7 All pages P03: R-CarH3_DU/LBSC	Components replaced Components replaced Comment(s) changed Comment(s) changed Components replaced Connection changed Component replaced Symbol modified Component replaced Connection changed Connection changed Connection changed Connection changed Connection changed	from D3.3V to D1.8V) Type numbers of crystal oscillator/resonator changed (16.66MHz -> 16.64MHz) The type number of coils (L53 and L54) for DVFS and VDD changed. Ref numbers also changed. (L110, L111) Target: DVFS0.8V, VDD0.8V TYP changed from 0.8125V to 0.82V, 0.600V-1.100V deleted. Target: D1.8V, D3.3V, VLD02.5V Comment on PMIC voltage range deleted An error in dimension of connectors in the location figure corrected from 1.2mm to 12mm. A vertical line deleted. The type number of coils (L55 to L58) for DDR0_1.1V, DDR1_1.1V, D1.8V, and D3.3V changed. Ref numbers remain unchanged. TX/RX connection of U97(PCIE signal SW) swapped in accord with the PCB The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	X4 L53 L54 L55, L56 L57, L58 - X3	CZS-41 NZ2016SH-16.64MHz- RNA5013C DDFCUL0630-H-R12M SPM3020T-R15M-CA02 DFE201612PD-R24M DFE201612PD-R33M - NX2016SA-16.64MHz-CHP- CZS-41	X4 L110 L111 L55, L56 L57, L58 - X3	CZS-41 NZ2016SH-16.64MHz- RNA5013C SPM6545VT-R15M-D SPM5030VT-R15M-D TFM201610ALMAR24MTAA TFM201610ALMAR33MTAA - NX2016SA-16.64MHz-CHP-			
P25: POWER P21: Audio(AK4613VQ) 1/1 P25: POWER 1/7 P11: Serial ATA 1/1 P03: R-CarH3_DU/LBSC 7 All pages P03: R-CarH3_DU/LBSC	Components replaced Comment(s) changed Comment(s) changed Components replaced Connection changed Component replaced Symbol modified Component replaced Connection changed Connection changed Connection changed Connection changed Connection changed Connection changed	Type numbers of crystal oscillator/resonator changed (16.66MHz -> 16.64MHz) The type number of coils (L53 and L54) for DVFS and VDD changed. Ref numbers also changed. (L110, L111) Target: DVFS0.8V, VDD0.8V TYP changed from 0.8125V to 0.82V, 0.600V-1.100V deleted. Target: D1.8V, D3.3V, VLD02.5V Comment on PMIC voltage range deleted An error in dimension of connectors in the location figure corrected from 1.2mm to 12mm. A vertical line deleted. The type number of coils (L55 to L58) for DDR0_1.1V, DDR1_1.1V, D1.8V, and D3.3V changed. Ref numbers remain unchanged. TX/RX connection of U97(PCIE signal SW) swapped in accord with the PCB The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	X4 L53 L54 L55, L56 L57, L58 - X3	CZS-41 NZ2016SH-16.64MHz- RNA5013C DDFCUL0630-H-R12M SPM3020T-R15M-CA02 DFE201612PD-R24M DFE201612PD-R33M - NX2016SA-16.64MHz-CHP- CZS-41	X4 L110 L111 L55, L56 L57, L58 - X3	CZS-41 NZ2016SH-16.64MHz- RNA5013C SPM6545VT-R15M-D SPM5030VT-R15M-D TFM201610ALMAR24MTAA TFM201610ALMAR33MTAA - NX2016SA-16.64MHz-CHP-	-		
P25: POWER P21: Audio(AK4613VQ) 1/1 P25: POWER 1/7 P11: Serial ATA 1/1 P03: R-CarH3_DU/LBSC 7 All pages P03: R-CarH3_DU/LBSC	Comment(s) changed Comment(s) changed Components replaced Connection changed Component replaced Symbol modified Component replaced Connection changed Connection changed Connection changed Connection changed	The type number of coils (L53 and L54) for DVFS and VDD changed. Ref numbers also changed. (L110, L111) Target: DVFS0.8V, VDD0.8V TYP changed from 0.8125V to 0.82V, 0.600V-1.100V deleted. Target: D1.8V, D3.3V, VLD02.5V Comment on PMIC voltage range deleted An error in dimension of connectors in the location figure corrected from 1.2mm to 12mm. A vertical line deleted. The type number of coils (L55 to L58) for DDR0_1.1V, DDR1_1.1V, D1.8V, and D3.3V changed. Ref numbers remain unchanged. TX/RX connection of U97(PCIE signal SW) swapped in accord with the PCB The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	X4 L53 L54 L55, L56 L57, L58 - X3	NZ2016SH-16.64MHz- RNA5013C DDFCUL0630-H-R12M SPM3020T-R15M-CA02 DFE201612PD-R24M DFE201612PD-R33M - NX2016SA-16.64MHz-CHP-CZS-41	L111 L55, L56 L57, L58 - X3	NZ2016SH-16.64MHz- RNA5013C SPM6545VT-R15M-D SPM5030VT-R15M-D - - - TFM201610ALMAR24MTAA TFM201610ALMAR33MTAA - NX2016SA-16.64MHz-CHP-	-		
P25: POWER P21: Audio(AK4613VQ) 1/1 P25: POWER 1/7 P11: Serial ATA 1/1 P03: R-CarH3_DU/LBSC 7 All pages P03: R-CarH3_DU/LBSC	Comment(s) changed Comment(s) changed Components replaced Connection changed Component replaced Symbol modified Component replaced Connection changed Connection changed Connection changed Connection changed	numbers also changed. (L110, L111) Target: DVFS0.8V, VDD0.8V TYP changed from 0.8125V to 0.82V, 0.600V-1.100V deleted. Target: D1.8V, D3.3V, VLD02.5V Comment on PMIC voltage range deleted An error in dimension of connectors in the location figure corrected from 1.2mm to 12mm. A vertical line deleted. The type number of coils (L55 to L58) for DDR0_1.1V, DDR1_1.1V, D1.8V, and D3.3V changed. Ref numbers remain unchanged. TX/RX connection of U97(PCIE signal SW) swapped in accord with the PCB The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU_DOTCLKIN2_18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	L54 L55, L56 L57, L58 - X3	DDFCUL0630-H-R12M SPM3020T-R15M-CA02 - - - DFE201612PD-R24M DFE201612PD-R33M - NX2016SA-16.64MHz-CHP- CZS-41	L111 L55, L56 L57, L58 - X3	SPM6545VT-R15M-D SPM5030VT-R15M-D TFM201610ALMAR24MTAA TFM201610ALMAR33MTAA - NX2016SA-16.64MHz-CHP-	-	-	
P25: POWER P21: Audio(AK4613VQ) 1/1 P25: POWER 1/7 P11: Serial ATA 1/1 P03: R-CarH3_DU/LBSC 7 All pages P03: R-CarH3_DU/LBSC	Comment(s) changed Comment(s) changed Components replaced Connection changed Component replaced Symbol modified Component replaced Connection changed Connection changed Connection changed Connection changed	numbers also changed. (L110, L111) Target: DVFS0.8V, VDD0.8V TYP changed from 0.8125V to 0.82V, 0.600V-1.100V deleted. Target: D1.8V, D3.3V, VLD02.5V Comment on PMIC voltage range deleted An error in dimension of connectors in the location figure corrected from 1.2mm to 12mm. A vertical line deleted. The type number of coils (L55 to L58) for DDR0_1.1V, DDR1_1.1V, D1.8V, and D3.3V changed. Ref numbers remain unchanged. TX/RX connection of U97(PCIE signal SW) swapped in accord with the PCB The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU_DOTCLKIN2_18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	L54 L55, L56 L57, L58 - X3	SPM3020T-R15M-CA02 DFE201612PD-R24M DFE201612PD-R33M - NX2016SA-16.64MHz-CHP-CZS-41	L111 L55, L56 L57, L58 - X3	SPM5030VT-R15M-D TFM201610ALMAR24MTAA TFM201610ALMAR33MTAA - NX2016SA-16.64MHz-CHP-	-		
P21: AUGIO(AK4613VQ)	Comment(s) changed Components replaced Connection changed Component replaced Symbol modified Component replaced Connection changed Connection changed Connection changed Comment(s) changed	Target: DVFS0.8V, VDD0.8V TYP changed from 0.8125V to 0.82V, 0.600V-1.100V deleted. Target: D1.8V, D3.3V, VLD02.5V Comment on PMIC voltage range deleted An error in dimension of connectors in the location figure corrected from 1.2mm to 12mm. A vertical line deleted. The type number of coils (L55 to L58) for DDR0_1.1V, DDR1_1.1V, D1.8V, and D3.3V changed. Ref numbers remain unchanged. TX/RX connection of U97(PCIE signal SW) swapped in accord with the PCB The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	- - L55, L56 L57, L58 - X3	- - DFE201612PD-R24M DFE201612PD-R33M - NX2016SA-16.64MHz-CHP- CZS-41	- - - L55, L56 L57, L58 - X3	TFM201610ALMAR24MTAA TFM201610ALMAR33MTAA NX2016SA-16.64MHz-CHP-	-	- - - - - -	
P21: AUGIO(AK4613VQ)	Comment(s) changed Components replaced Connection changed Component replaced Symbol modified Component replaced Connection changed Connection changed Connection changed Comment(s) changed	TYP changed from 0.8125V to 0.82V, 0.600V-1.100V deleted. Target: D1.8V, D3.3V, VLD02.5V Comment on PMIC voltage range deleted An error in dimension of connectors in the location figure corrected from 1.2mm to 12mm. A vertical line deleted. The type number of coils (L55 to L58) for DDR0_1.1V, DDR1_1.1V, D1.8V, and D3.3V changed. Ref numbers remain unchanged. TX/RX connection of U97(PCIE signal SW) swapped in accord with the PCB The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU_DOTCLKIN2_18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)"	L57, L58 - X3	DFE201612PD-R33M - NX2016SA-16.64MHz-CHP- CZS-41 -	L57, L58 - X3	TFM201610ALMAR33MTAA - NX2016SA-16.64MHz-CHP-	-	- - - - - -	
P21: AUGIO(AK4613VQ)	Components replaced Connection changed Component replaced Symbol modified Component replaced Connection changed Connection changed Connection changed Comment(s) changed	Target: D1.8V, D3.3V, VLDO2.5V Comment on PMIC voltage range deleted An error in dimension of connectors in the location figure corrected from 1.2mm to 12mm. A vertical line deleted. The type number of coils (L55 to L58) for DDR0_1.1V, DDR1_1.1V, D1.8V, and D3.3V changed. Ref numbers remain unchanged. TX/RX connection of U97(PCIE signal SW) swapped in accord with the PCB The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	L57, L58 - X3	DFE201612PD-R33M - NX2016SA-16.64MHz-CHP- CZS-41 -	L57, L58 - X3	TFM201610ALMAR33MTAA - NX2016SA-16.64MHz-CHP-	-	-	
P21: AUGIO(AK4613VQ)	Components replaced Connection changed Component replaced Symbol modified Component replaced Connection changed Connection changed Connection changed Comment(s) changed	An error in dimension of connectors in the location figure corrected from 1.2mm to 12mm. A vertical line deleted. The type number of coils (L55 to L58) for DDR0_1.1V, DDR1_1.1V, D1.8V, and D3.3V changed. Ref numbers remain unchanged. TX/RX connection of U97(PCIE signal SW) swapped in accord with the PCB The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	L57, L58 - X3	DFE201612PD-R33M - NX2016SA-16.64MHz-CHP- CZS-41 -	L57, L58 - X3	TFM201610ALMAR33MTAA - NX2016SA-16.64MHz-CHP-	-	-	
1/1 P25: POWER 1/7 P11: Serial ATA 1/1 P03: R-CarH3_DU/LBSC 7 All pages P03: R-CarH3_DU/LBSC	Connection changed Component replaced Symbol modified Component replaced Connection changed Connection changed Connection changed	The type number of coils (L55 to L58) for DDR0_1.1V, DDR1_1.1V, D1.8V, and D3.3V changed. Ref numbers remain unchanged. TX/RX connection of U97(PCIE signal SW) swapped in accord with the PCB The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	L57, L58 - X3	DFE201612PD-R33M - NX2016SA-16.64MHz-CHP- CZS-41 -	L57, L58 - X3	TFM201610ALMAR33MTAA - NX2016SA-16.64MHz-CHP-	-	-	
1/7 P11: Serial ATA 1/1 P03: R-CarH3_DU/LBSC 7 All pages P03: R-CarH3_DU/LBSC	Connection changed Component replaced Symbol modified Component replaced Connection changed Connection changed Connection changed	D3.3v changed. Ref numbers remain unchanged. TX/RX connection of U97(PCIE signal SW) swapped in accord with the PCB The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	L57, L58 - X3	DFE201612PD-R33M - NX2016SA-16.64MHz-CHP- CZS-41 -	L57, L58 - X3	TFM201610ALMAR33MTAA - NX2016SA-16.64MHz-CHP-	-		
1/1 P03: R-CarH3_DU/LBSC 7 All pages P03: R-CarH3_DU/LBSC	Component replaced Symbol modified Component replaced Connection changed Connection changed Comment(s) changed	The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	- X3	- NX2016SA-16.64MHz-CHP- CZS-41	- X3	- NX2016SA-16.64MHz-CHP-	-	-	
1/1 P03: R-CarH3_DU/LBSC 7 All pages P03: R-CarH3_DU/LBSC	Component replaced Symbol modified Component replaced Connection changed Connection changed Comment(s) changed	The type number of a crystal resonator (X3, not mounted) changed The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	-	CZS-41 -	-		-	-	
P03: R-CarH3_DU/LBSC	Component replaced Connection changed Connection changed Comment(s) changed	The attribute of the NC pins (symbol U1) changed from "Power" to "Passive" because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	-	-	-	CZS-53 -	_	-	
P03: R-CarH3_DU/LBSC	Component replaced Connection changed Connection changed Comment(s) changed	because all NC pins are not connected. U61 changed from Versaclock5 to Versaclock6. Changed from two outputs to four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	- U61 -	- 5P49V5923B -	-	-	_	_	
PUS: R-Carh3_DU/LBSC	Connection changed Connection changed Comment(s) changed	four outputs. Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	U61 - -	5P49V5923B -	1164				
All pages	Connection changed Comment(s) changed	Two capacitors added to the power circuit. The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	-	-	001	5P49V6901A	-	-	
All pages	Comment(s) changed	The layout pattern for R792 (not mounted) deleted, connection not in use between U61.17pin and DU DOTCLKIN2 18 deleted. Comment added to U1: "PRESET#: Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	-		C978,C979	GCM155R71A104KA55	-	-	+
All pages		Comment added to U1: "PRESET# : Internal pull-down. (100k typ.)" Cpacitance of the capacitors for VDD and DVFS changed according to the	_	-	-	-	R792	MCR01MZPJ330	
All pages				-	-	-	-	-	+
All pages		result of the PI simulation.							
	_	D 010 F00 0:	-	-	-	-	-	-	
		R-CarH3_ES2_Sipコア電源インピーダンス最終結果_161110.xlsx	C474 C470	GCM155R71H472KA37	C171 C170	COMMEED 741 14001/AFF			+
		4700pF -> 0.01uF* (not mounted) 0.01uF -> 0.1uF	C171,C172 C198	GCM155R71H472KA37 GCM155R71E103KA37	C171,C172 C198	GCM155R71H103KA55 GCM155R71H104KE02	-	-	+
		0.047uF -> 0.01uF	C203,C209 C205	GCM155R71E473KA55	C203,C209	GCM155R71H103KA55 GCM155R71H473KE02	-	-	
		0.068uF -> 0.047uF 0.022uF -> 0.068uF	C206	GCM155R71C683KA55 GCM155R71E223KA55	C205 C206	GCM155R71H473KE02 GCM155R71H683KE02	-	-	+
		0.022uF -> 0.01uF	C210	GCM155R71E223KA55	C210	GCM155R71H103KA55	-	-	
i		0.01uF -> 0.015uF 0.01uF -> 0.022uF	C211,C215,C865 C212,C214	GCM155R71E103KA37 GCM155R71E103KA37	C211,C215,C865 C212.C214	GCM155R71H153KA55 GCM155R71H223KA55	-	-	+
i		0.01uF -> 0.22uF	C213	GCM155R71E103KA37	C213	GCM155R71C224KE02	-	-	
		0.01uF -> 0.047uF 0.015uF -> 0.01uF	C216 C867	GCM155R71E103KA37 GCM155R71E153KA55	C216 C867	GCM155R71H473KE02 GCM155R71H103KA55	-	-	+
ļ		0.1uF -> 0.01uF	C869	GCM155R71A104KA55	C869	GCM155R71H103KA55	-	-	
ļ		0.22uF -> 0.01uF 0.01uF -> 0.01uF* (not mounted)	C871 C880,C882,C885,C886	GCM155R71C224KE02 GCM155R71E103KA37	C871 C880,C882,C885,C886	GCM155R71H103KA55 GCM155R71H103KA55	-	-	+
	Components replaced	Capacitors for VDD and DVFS changed to the ones used in the PI simulation	-	-	-	-	-	-	
ļ		R-CarH3 ES2 Sipコア電源インピーダンス最終結果 161110.xlsx	C173,C174,C175,C177,C180,		C173,C174,C175,C177,C180,				+
ļ			0100,0000,0000				-	-	
ļ		0.015uF	C868 C167,C200,C870,C888,C890,	GCM155R71E153KA55	C868 C167,C200,C870,C888,C890,	GCM155R71H153KA55	-	-	+
ļ		0.022uF	C891,C892	GCM155R71E223KA55	C891,C892	GCM155R71H223KA55	-	-	
ļ		0.033uF 0.047uF	C201,C864 C162,C884,C889,C893	GCM155R71E333KA55 GCM155R71E473KA55	C201,C864 C162,C884,C889,C893	GCM155R71C333KA37 GCM155R71H473KE02	-	-	-
ļ		0.1uF	C204,C929,C930	GCM155R71A104KA55	C204,C929,C930	GCM155R71H104KE02	-	-	
ļ		2.2uF	C862,C863,C876 C534,C535,C536,C537,C543,	GCM188C71A225KE02	C862,C863,C876 C534,C535,C536,C537,C543,	GCM188R70J225KE22	-	-	+
ļ		47uF	C546,C547,C548,C549,C550,	GCM32ER70.I476KE19	C546,C547,C548,C549,C550,	GCM32ER70.I476ME19			
ļ		77.41	C900,C942,C943,C944,C945, C964,C965,C966	COMOZZI (10041 OKZ 10	C900,C942,C943,C944,C945, C964,C965,C966	COMOZEI (1004) CIME 10			
ļ		47uF* (not mounted)	C946	GCM32ER70J476KE19		GCM32ER70J476ME19	-	-	
ļ	Components replaced	Capacitors not in the list changed to the ones used in the PI simulation R-CarH3 FS2 Sinコア電源インピーダンス最終結果 161110 xlsx	-	-	-	-	-	-	
<u>'</u>		0.01uF* (not mounted)	C559,C563,C568,C572	GCM155R71E103KA37	C559,C563,C568,C572	GCM155R71H103KA55	-	-	<u> </u>
ļ		0.022uF 0.047uF	C545 C285.C286	GCM155R71E223KA55 GCM155R71E473KA55	C545 C285.C286	GCM155R71H223KA55 GCM155R71H473KE02	-	-	
<u>'</u>		0.1uF* (not mounted)	C558,C562,C932,C933	GCM155R71A104KA55	C558,C562,C932,C933	GCM155R71H473KE02 GCM155R71H104KE02	-	-	
ļ			C533,C560,C564,C573,C837, C838,C839,C840,C841,C842,		C533,C560,C564,C573,C837, C838,C839,C840,C841,C842,				
<u>'</u>		47uF	C843,C844,C928,C959,C960,	GCM32ER70J476KE19	C843,C844,C928,C959,C960,	GCM32ER70J476ME19	-	-	
1/2	Commont(s) showed	The layout pattern for the capacitors framed with dotted lines, and "Do not stuff"	C961,C962		C961,C962				
1/2 2 P06: R-CarH3_NEW_POW2	Comment(s) changed	added to it.	-		-	<u> </u>	-	-	
	Components replaced	The capacitance of capacitors C171 and C172 (not mounted) changed from 0.01uF to 4700pF. Remain not mounted.	C171,C172	GCM155R71H103KA55	C171,C172	GCM155R71H472KA37	-	-	
1/2 5 P06: R-CarH3_NEW_POW2	Mounting specification of components	U.UTUF to 4700pF. Remain not mounted.	C171,C172	GCM155R71H472KA37		_	_	_	+
5 FUO. N-CAIRS_NEW_PUW2	changed	The following six VDD power cacacitors mounted.			-	-	-	-	+
2/7 P25: POWER	Components replaced	C171, C172(4700pF)/C880, C882, C885, C886(0.01uF) The capacitance of the SDx power output capacitor changed from 1uF to 10uF.	C880,C882,C885,C886 C579,C580,C581,C582	GCM155R71E103KA37	C570 C580 C591 C592	- GCM21BC71C106KE36	-	-	+
JI FZS. FOWER		Note that the dimension is larger.	UU18,UU0U,UU0 1,UU0Z	GCM155C71A105KE38	C579,C580,C581,C582	GUIVIZ IDU/ IU IUOKE30	-	-	+
<u> </u>	Mounting specification of components changed	R768 not mounted, R770 mounted	-	-	-	<u>-</u>	-	-	
	Component added	A 0-Ω resistor (1005) added to V5AIN of PMIC SBD added to the location between SW_DVFS and PGND_DVFS, and	-	-	R810	MCR01MZPJ000	-	-	
	Components added	between SW VD09 and PGND VD09. (Not mounted)		-	D45,D46	RSX501L-20	-	-	
2/1 P21: Audio(AK4613VQ)	Comment(s) changed	A figure indicating the connector locations changed. "Left AOUT" "Right MIC" => "Left MIC" "Right AOUT"	-	-	-	-	-	-	
P15: MMC0	Comment(s) changed	=> "Left MIC" "Right AOUT" The size of R616 (1608) made visible.			-		-	-	<u>+</u>
P19: MIPI CSI-2_VIN	Components replaced	47-k network resistors changed from size 1608 to 1005 for easier wiring of	NR14, NR15	MNR14E0APJ473	R811-R818	MCD04M7D 1472	-		
<u>'</u>		eMMC and unification of the size	R223, R224, R236, R263 R262, R624 (非実装)	MCR03EZPJ473 MCR03EZPJ473	R819-R821, R824 R822, R823 (非実装)	MCR01MZPJ473	-	-	+
2/1 P26: POWER PMIC2	Components added	Fight screw holes added to the circuit diagram	-	-	FH1-FH8	HOLE_E_4.0_6.0_8.0	-	-	
/19 P25: POWER	Components replaced	The capacitance of the following capacitors changed from 47uF to 100uF: C533, C534, C535, C536, C537, C543, C942, C943, C944, and C945	C533,C534,C535,C536,C537, C543,C942,C943,C944,C945	GCM32ER70J476ME19	C533,C534,C535,C536,C537, C543,C942,C943,C944,C945	GRT32ER60J107ME13	-	-	PMIC 4th回路→ 2nd回路に戻すための暫定対
		R426 changed from 10k to 240	R426	MCR01MZPF1002	R426	MCR01MZPF2400	-	-	PMIC 4th回路→ 2nd回路に戻すための暫定対
<u>'</u>		R436 changed from 160 to 240 The capacitance of the capacitor for SDx power output changed from 10uF to	R436 C579.C580.C581.C582	MCR01MZPF1600 GCM21BC71C106KE36	R436 C579,C580,C581,C582	MCR01MZPF2400 GCM219R71C105KA37	-	-	PMIC 4th回路→ 2nd回路に戻すための暫定対 PMIC 4th回路→ 2nd回路に戻すための暫定対
į į		Comment added: This Schematics is common by R-CarH3 and R-CarM3.	-	D 01 10 10 01 L 00				+	一

Da	e		Description							
Rev c		Page		Before the changes		Revised or Addition		Delete		Notes
Iss	ie			REF#	Part Number	REF#	Part Number	REF#	Part Number	
2.01 4/2	4 P04: R-CarH3_USB/HDMI	Comment(s) changed	bits7,8 deleted from the comment of SW31	-	-	-	-	-	-	
	P23: EXIO_CN/MIPI_SW	Comment(s) changed	A26 deleted from the comment of CN28	-	-	-	-	-	-	
	P02: R-CarH3_SD/QSPI	Signal name changed	SSI_WS01239 -> SSI_WS0129	-	-	-	-	-	-	
	P21: Audio(AK4613VQ)		SSI_SCK01239 -> SSI_SCK0129	-	-	-	-	-	-	
	P23: EXIO CN/MIPI SW			-	-	-	-	-	-	
2.02 5/	P25: POWER	Components replaced	The capacitance of the following capacitors changed from 100uF to 47uF:	C533,C534,C535,C536,C537,	GRT32ER60J107ME13	C533,C534,C535,C536,C537,	CCM32EP70 I476ME10			
2.02 3/	Z F25. FOWER		C533, C534, C535, C536, C537, C543, C942, C943, C944, and C945	C543,C942,C943,C944,C945		C543,C942,C943,C944,C945	GCW32EI\703470WE19	-	-	
		(PMIC 5th)	R426 changed from 240 to 10k and not mounted		MCR01MZPF1002	-	-	-	-	
			R436 changed from 240 to 160 and not mounted		MCR01MZPF1600	-	-	-	-	
			The capacitance of the SDx power output capacitor changed from 10uF to 1uF.		GCM219R71C105KA37		GCM21BC71C106KE36	-	-	
			R425 changed from 470 to 820	R425	MCR01MZPF4700	R425	MCR01MZPF8200	-	-	
			R434 changed from 1k to 510	R434	MCR01MZPF1001	R434	MCR01MZPF5100	-	-	
			R418 changed from 1k to 2.7k	R418	MCR01MZPF1001	R418	MCR01MZPF2701	-	-	
			The capacitance of the following capacitors changed from 0.022uF to	C545	GCM155R71H223KA55	C545	GCM155R71H104KE02	-	-	
	P11: Serial ATA	Comment(s) changed	Differential Impedance : 90(+/-5)ohm → 100ohm	-	-	-	-	-	-	
2.03 7/	4 P25: POWER	Type no. of a component changed	U72 changed from BD9571MWV-M to BD9571MWF-M		BD9571MWV-M	U72	BD9571MWF-M	-	-	
			D45 and D46 changed from RSX501L-20 to RSX501LAM20	D45,D46	RSX501L-20	D45,D46	RSX501LAM20	-	-	
2 04 10	17 P2 to P7	Comment(s) changed	Comment on the difference of the symbol M3N added to the symbolb U1.							
2.04 10	17 FZ 10 F7	_	(For common use of the board with H3,M3 and M3N)	_	[-	_	-	