

<p>Conn to Power</p> <p>5-5</p> <p>主電源2 (+5V)</p>	<p>5V生成</p> <p>5V</p> <p>主電源2 (+5V)</p>	<p>電源ダイオード/3.3VSYS</p> <p>3.3VSYS</p> <p>主電源2 (+5V)</p>	<p>インフラ+LED</p> <p>5V</p> <p>主電源2 (+5V)</p>
<p>電源回路: 3.3V+</p> <p>3.3V+</p> <p>主電源2 (+5V)</p>	<p>3.3V生成</p> <p>3.3V</p> <p>主電源2 (+5V)</p>	<p>電源ダイオード/STLIn</p> <p>3.3VSYS</p> <p>主電源2 (+5V)</p>	<p>Capacitor</p> <p>5V</p> <p>主電源2 (+5V)</p>

The schematic diagram illustrates the power and reset connections for the STM32C031K4 microcontroller. The board is organized into four quadrants by dashed lines:

- Top-Left Quadrant:** The NRST pin is connected to a 5V SW-SBST button. A 10k pull-up resistor connects the button to the NRST pin. A 1uF capacitor connects the NRST pin to GND.
- Bottom-Left Quadrant:** A crystal (H2) is mounted on a 9MHz\_Pad. The crystal is connected to the OSC\_IN and OSC\_OUT pins of the microcontroller.
- Top-Right Quadrant:** The +3.3V supply is connected to the VDD/VDDA and VSS/VSSA pins of the microcontroller.
- Bottom-Right Quadrant:** The +3.3V supply is connected to a 1uF capacitor (C1) and GND.

The central component is the STM32C031K4 microcontroller, with pins 1 through 28 labeled on the left and right sides. The pins are connected as follows:

- Pin 1:** NRST
- Pin 2:** OSC\_IN
- Pin 3:** OSC\_OUT
- Pin 4:** VDD/VDDA
- Pin 5:** VSS/VSSA
- Pin 6:** PA0
- Pin 7:** PA1
- Pin 8:** PA2
- Pin 9:** PA3
- Pin 10:** PA4
- Pin 11:** PA5
- Pin 12:** PA6
- Pin 13:** PA7
- Pin 14:** PA8
- Pin 15:** PA9
- Pin 16:** PA10
- Pin 17:** PA11
- Pin 18:** PA12
- Pin 19:** PA13
- Pin 20:** PA14
- Pin 21:** PA15
- Pin 22:** PA16
- Pin 23:** PA17
- Pin 24:** PA18
- Pin 25:** PA19
- Pin 26:** PA20
- Pin 27:** PA21
- Pin 28:** PA22

H4 MountingHole

H6 MountingHole

H8 MountingHole

H5 MountingHole

H7 MountingHole

H9 MountingHole

[illegible]