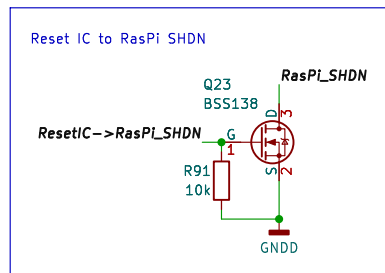
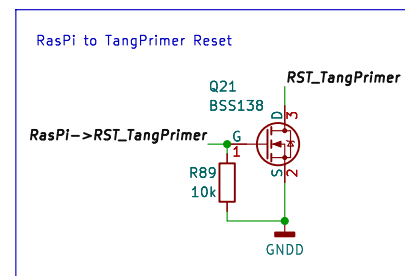
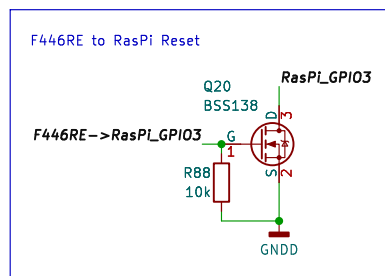
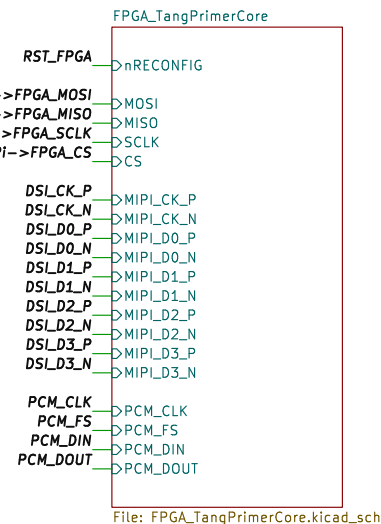
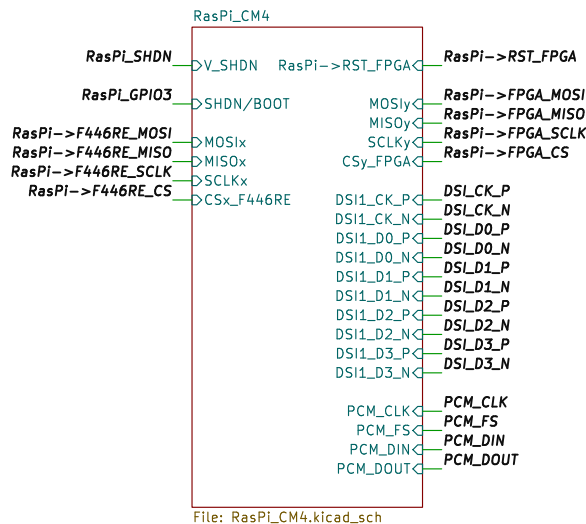
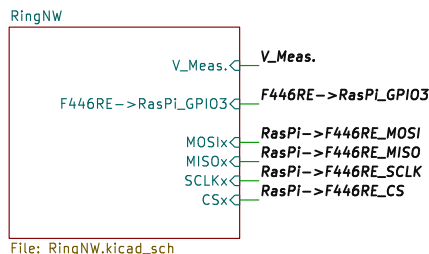


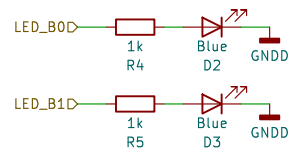
GLOBAL_EN
SHDN/BOOT(GPIO3)
V_SHDN(電圧監視SHDN)



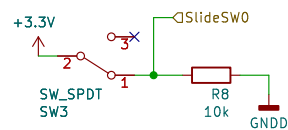
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Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.11		Id: 1/14

I/O

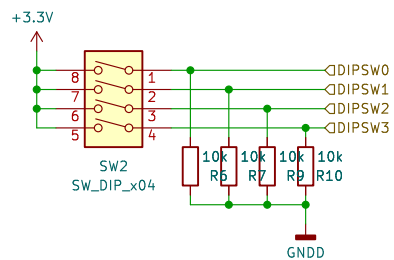
LED



スライドスイッチ



DIPスイッチ

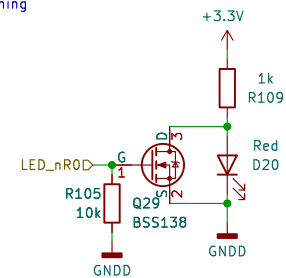


Indicator

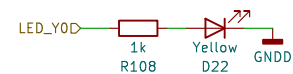
Running



not Running



Error



Sheet: /RingNW/UI_RingNW/
File: UI_RingNW.kicad_sch

Title:

Size: A4

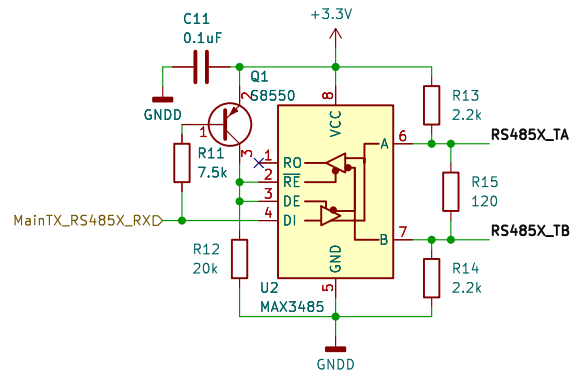
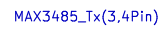
Date:

KiCad E.D.A. kicad 7.0.11

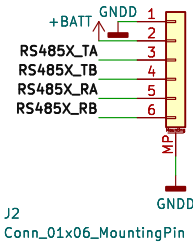
Rev:

Id: 2/14

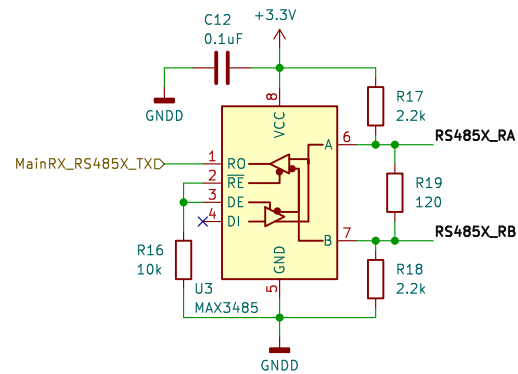
RS485_RingNW_X



Conn

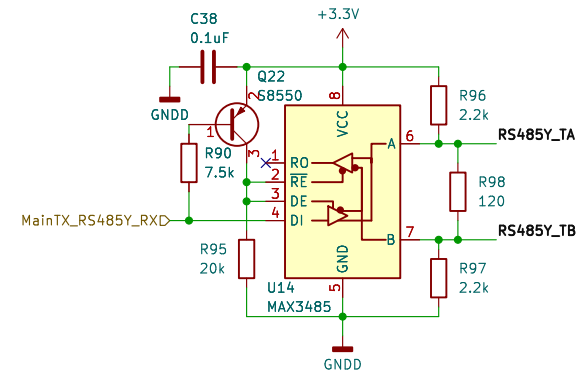
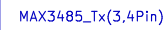


MAX3485_Rx(5,6Pin)

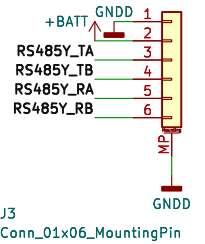


J2 GND
Conn_01x06_MountingPin

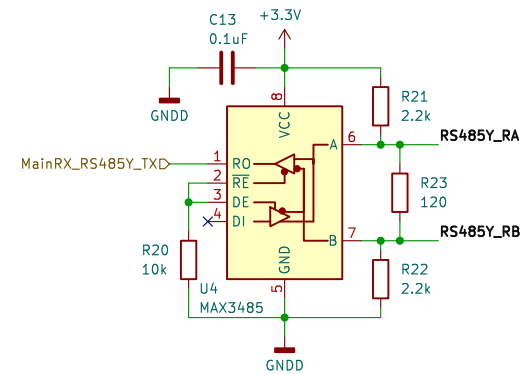
RS485_RingNW_Y



Conn



MAX3485_Rx(5,6Pin)



J3
Conn_01x06_MountingPin

Sheet: /RingNW/RS485_RingNW/
File: RS485_RingNW.kicad_sch

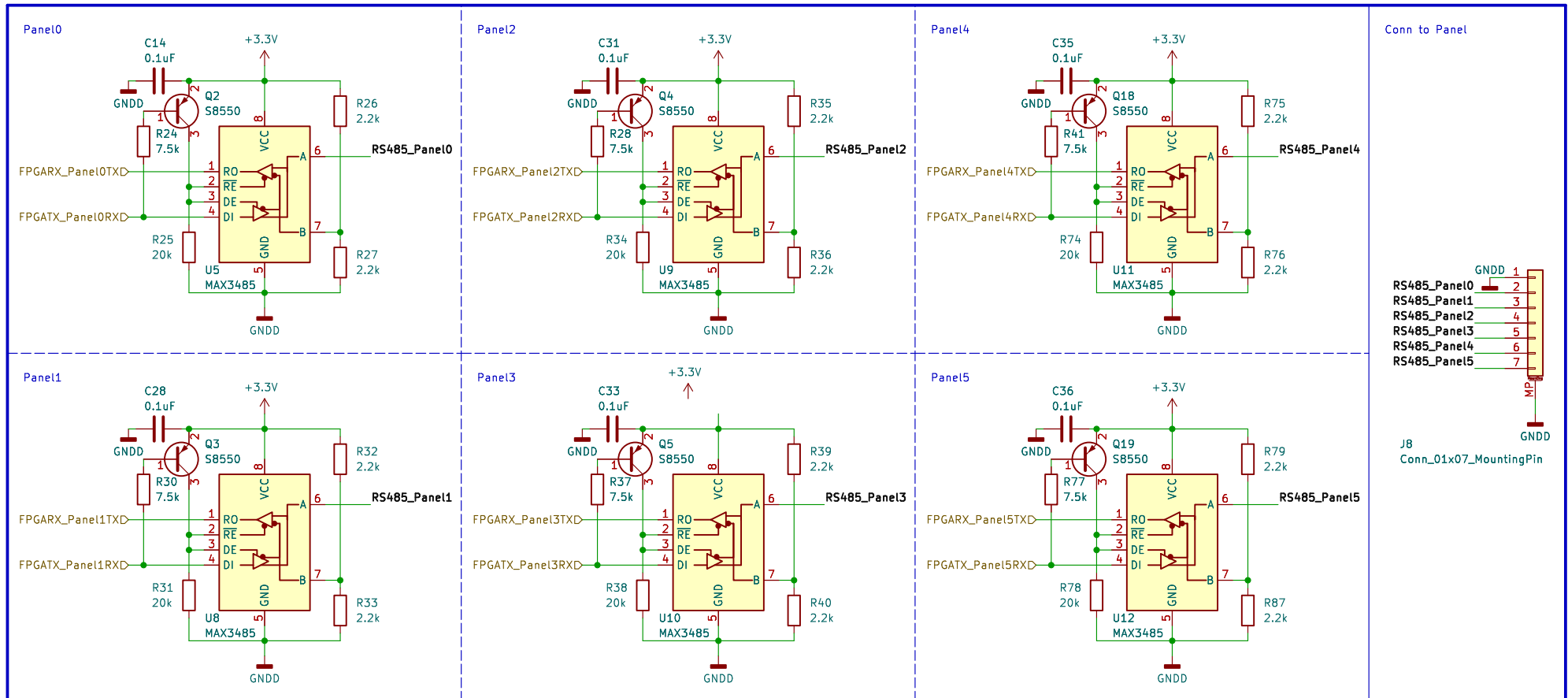
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Size: A4	Date:
KiCad E.D.A.	kicad 7.0.11

Date:

Rev:
Id: 3/14

Trans to RS485



Sheet: /FPGA_TangPrimerCore/RS485_Panel/
File: RS485_Panel.kicad_sch

Title:

Size: A4

Date:

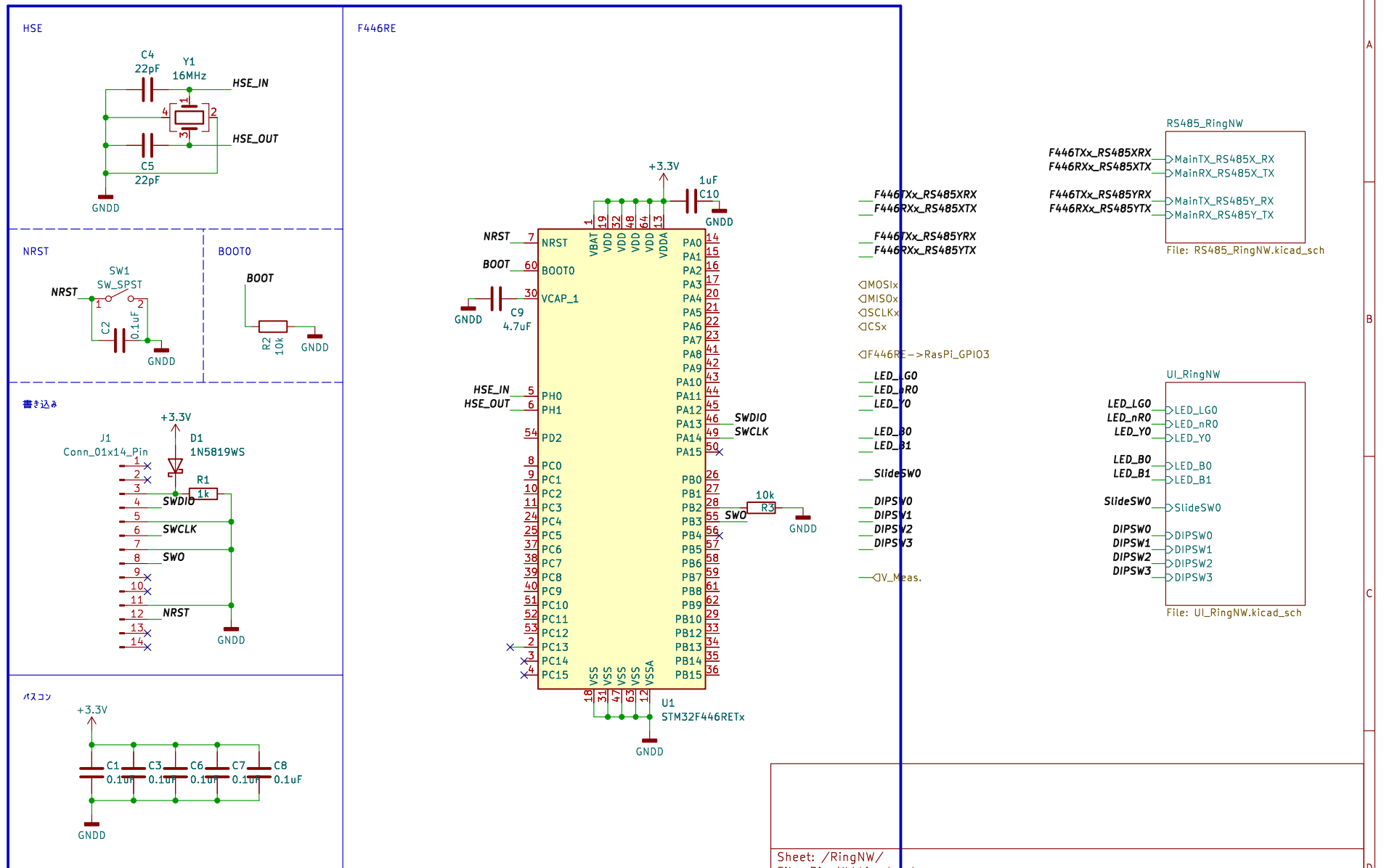
KiCad E.D.A. kicad 7.0.11

Rev:

Id: 4/14

いつもはマスターだけど今回はスレーブ H723VGにする

ネットワークマイコン



Sheet: /RingNW/
File: RingNW.kicad_sch

Title:

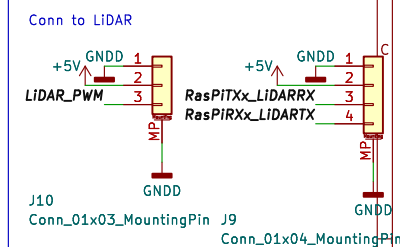
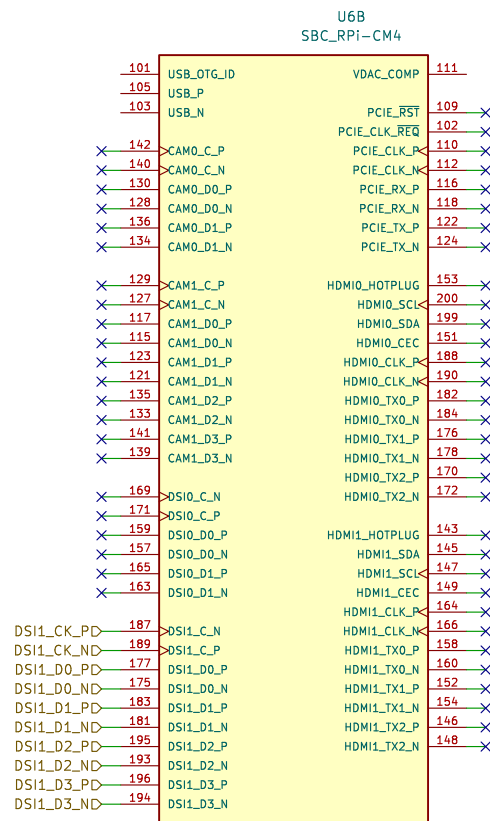
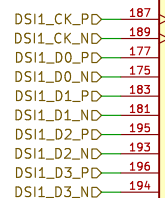
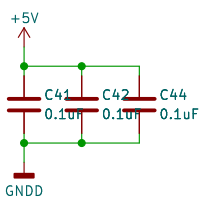
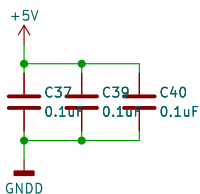
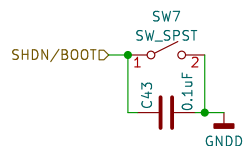
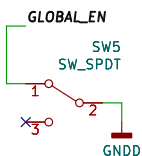
Size: A4

Date:

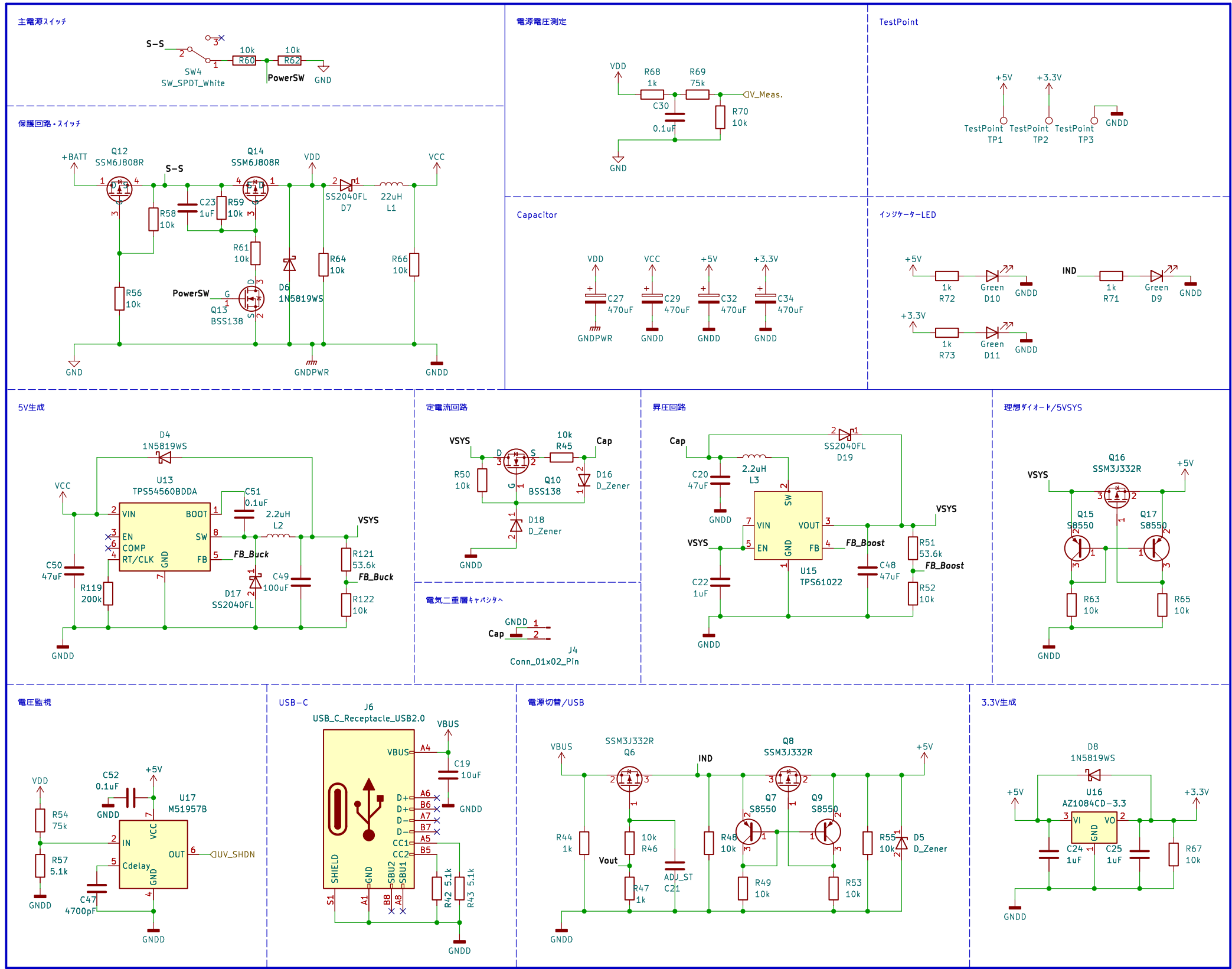
KiCad E.D.A. kicad 7.0.11

Rev:

Id: 5/14



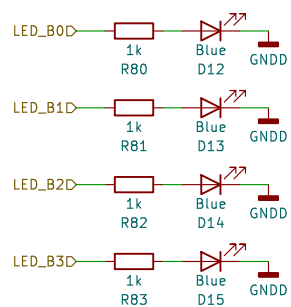
PowerModule



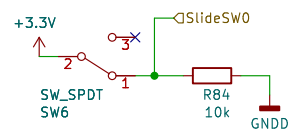
電源スイッチ 耐圧大丈夫？
三端子を新しく選定したのにする

I/O

LED



スライドスイッチ



Sheet: /FPGA_TangPrimerCore/UI_TangPrimer/
File: UI_TangPrimer.kicad_sch

Title:

Size: A4

Date:

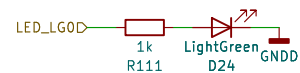
KiCad E.D.A. kicad 7.0.11

Rev:

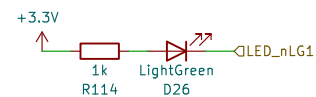
Id: 8/14

Indicator

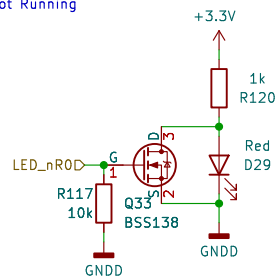
Running



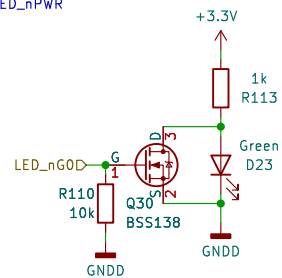
LED_nACT



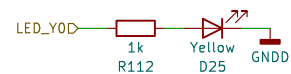
not Running



LED_nPWR



Error



Sheet: /RasPi_CM4/UI_RasPi/
File: UI_RasPi.kicad_sch

Title:

Size: A4

Date:

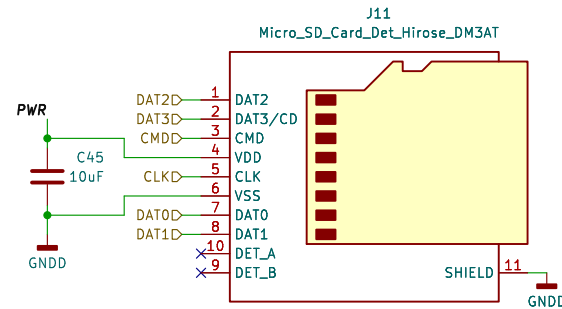
KiCad E.D.A. kicad 7.0.11

Rev:

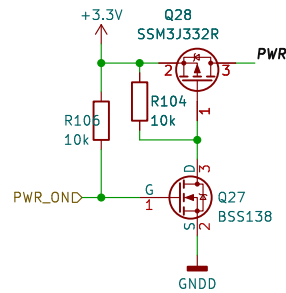
Id: 9/14

SD Card

SD Card



SD Card Power



Sheet: /RasPi_CM4/SD_Card/
File: SD_Card.kicad_sch

Title:

Size: A4

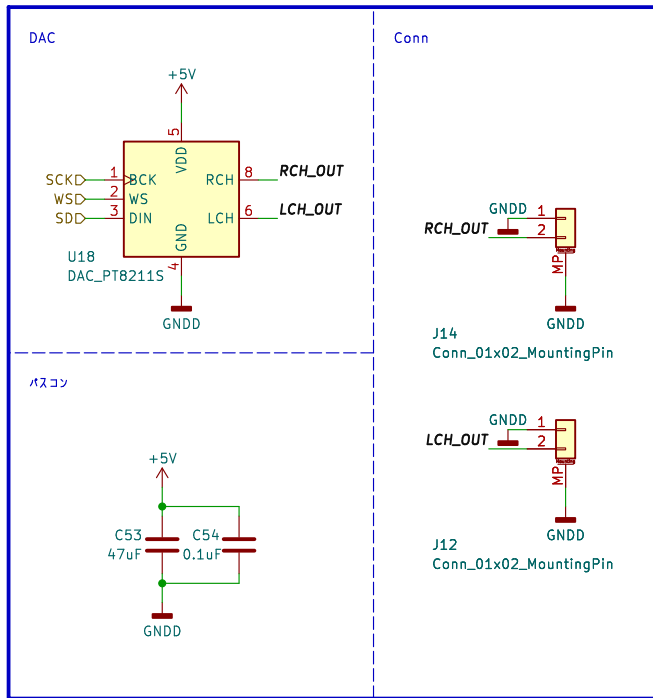
Date:

KiCad E.D.A. kicad 7.0.11

Rev:

Id: 10/14

DAC



Sheet: /FPGA_TangPrimerCore/DAC/
File: DAC.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. kicad 7.0.11

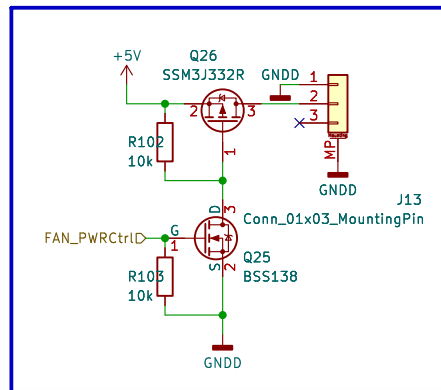
Rev:

Id: 11/14

The schematic shows the internal components of the RJ45 module. It includes two transformer-like structures labeled XMIT and RCV. The XMIT side has pins TD+, TCT-, TD-, RD+, RCT-, and RD-. The RCV side has pins TD+, TCT-, TD-, RD+, RCT-, and RD-. A capacitor C46 (0.1uF) is connected between TD_PD and TD_ND. A capacitor C1 (1000pF) is connected between the RCV output pins. Two LEDs are shown: LEDY (yellow) and LEDG (green), both connected to +3.3V and ground.

Title:		
Size: A4	Date:	Rev:
KiCad E.D.A. kicad 7.0.11		Id: 12/14

FAN



Sheet: /RasPi_CM4/FAN/
File: FAN.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. kicad 7.0.11

Rev:

Id: 13/14