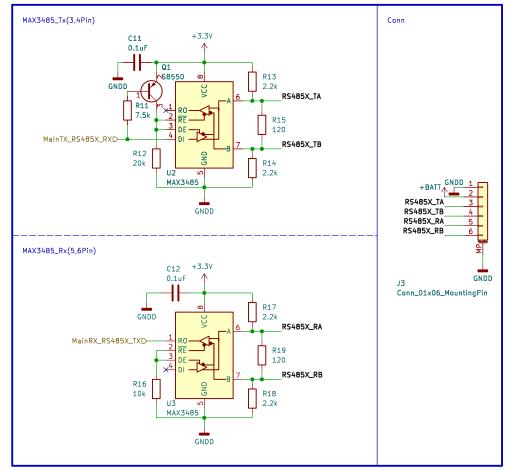
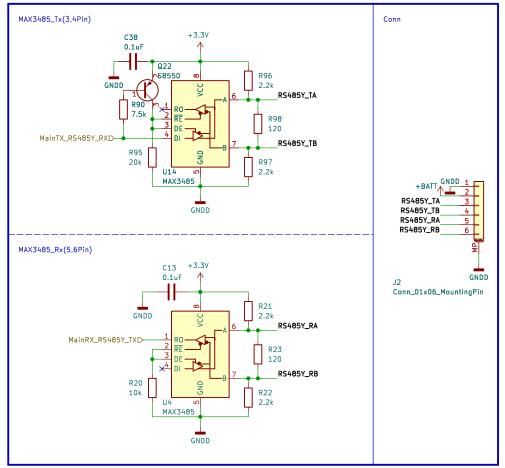


## RS485\_RingNW\_X



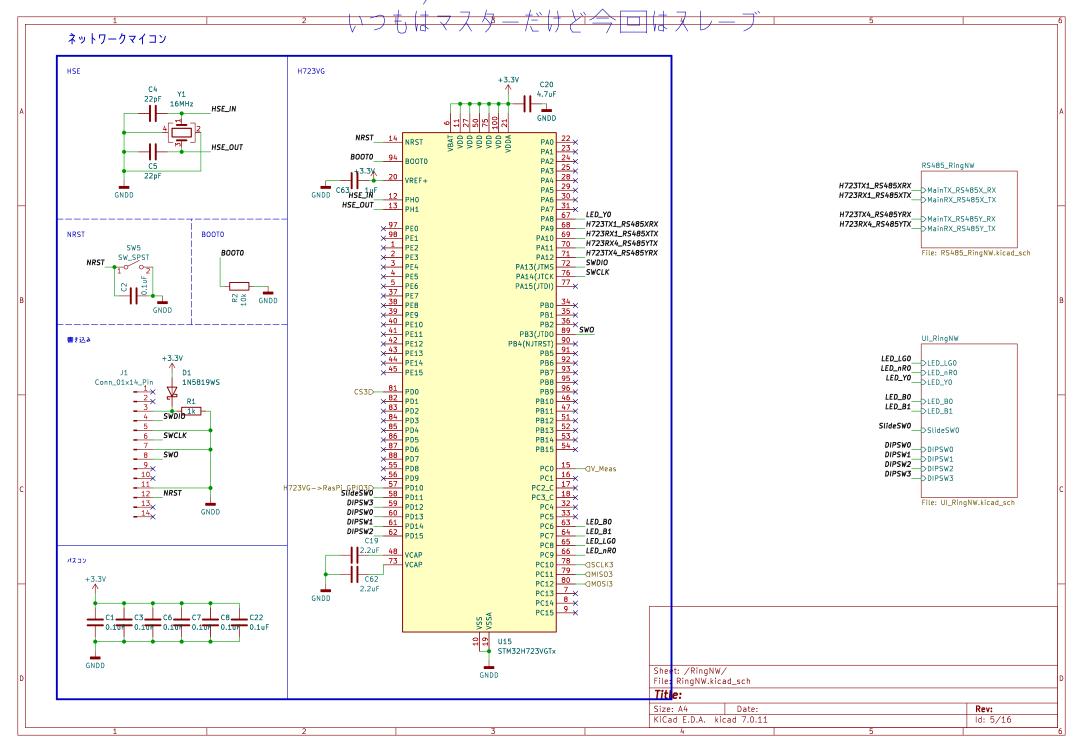
## RS485\_RingNW\_Y

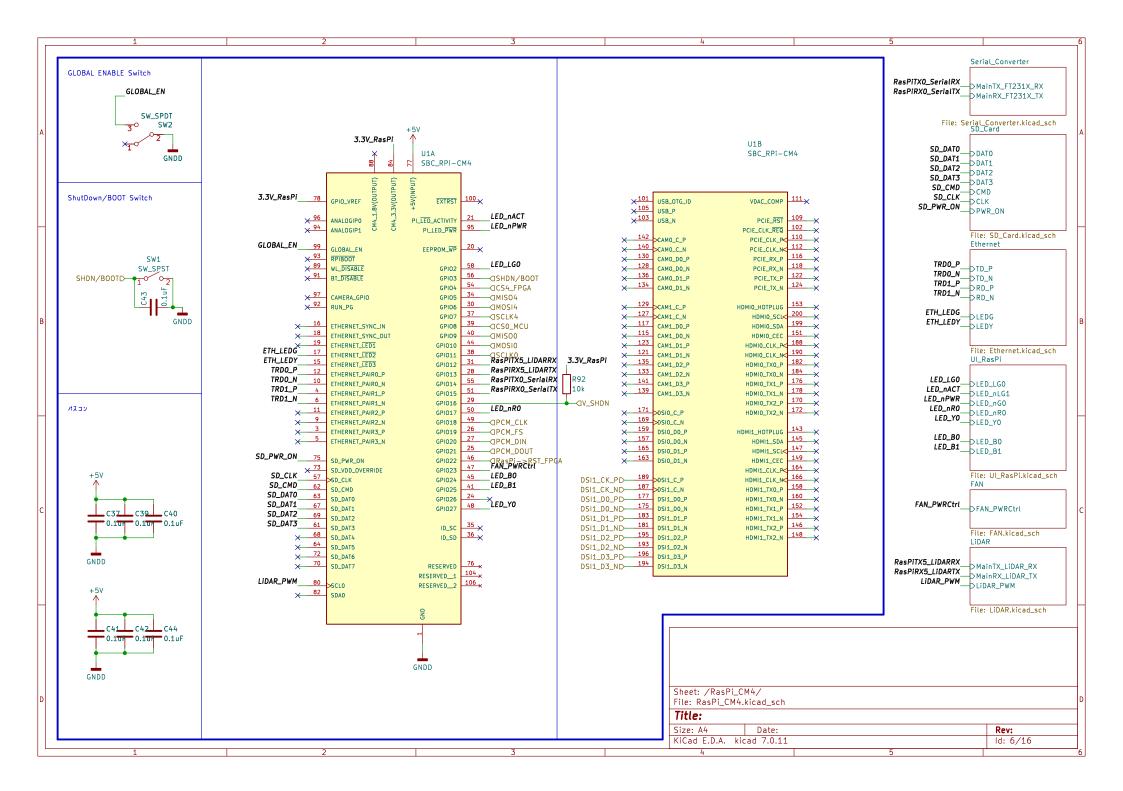


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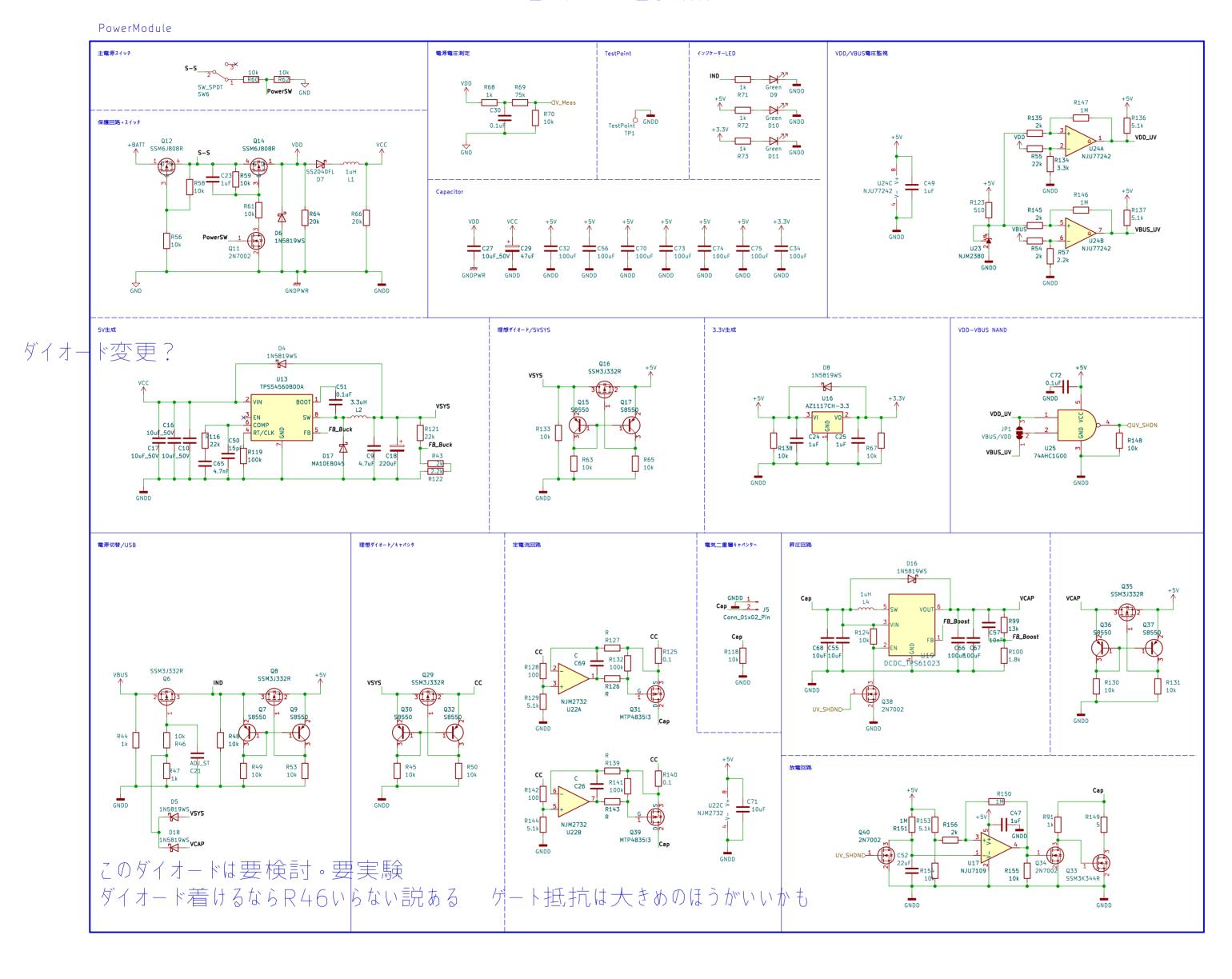
Trans to RS485 Panel0 Panel2 Panel4 Conn to Panel +3.3V +3.37 +3.37 C14 C35 0.1uF 0.1uF 0.1uF R75 2.2k \$8550 S8550 2.2k 2.2k RS485\_Panel0 RS485\_Panel2 RS485\_Panel4 FPGARX\_PanelOTXD FPGARX\_Panel2TXD-FPGARX\_Panel4TXD-FPGATX\_PanelORXD-FPGATX\_Panel2RXD-FPGATX\_Panel4RXD-R25 20k R74 20k R27 20k R36 2.2k 2.2k 2.2k U11 MAX3485 MAX3485 MAX3485 RS485\_PanelO 🚣 RS485\_Panel1 RS485\_Panel2 GNDD GNDD GNDD RS485\_Panel3 Conn\_01x08\_Mountin RS485\_Panel4 RS485\_Panel5 Panel1 Panel3 Panel5 Panel\_PWRCtrlD +3.3V +3.3V +3.3V C28 C33 C36 0.1uF 0.1uF 0.1uF GNDD GNDD Q3 Q5 Q19 GNDD R32 2.2k R39 2.2k R79 2.2k S8550 S8550 S8550 RS485\_Panel1 RS485\_Panel3 RS485\_Panel5 FPGARX\_Panel1TXD-FPGARX\_Panel3TXD— FPGARX\_Panel5TXD-FPGATX\_Panel1RXD-FPGATX\_Panel3RXD-FPGATX\_Panel5RXD— R31 R38 R78 20k R33 20k R40 20k R87 2.2k 2.2k U12 2.2k MAX3485 MAX3485 MAX3485 GNDD GNDD GNDD Sheet: /FPGA\_TangPrimerCore/RS485\_Panel/ File: RS485\_Panel.kicad\_sch Title: Size: A4 Date: Rev: KiCad E.D.A. kicad 7.0.11 ld: 4/16

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## 24V系には20k!!!!



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