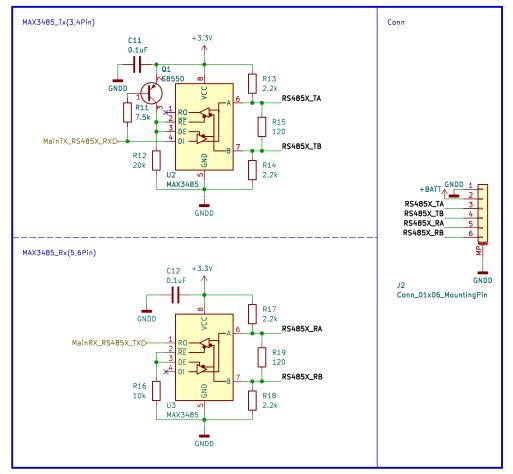
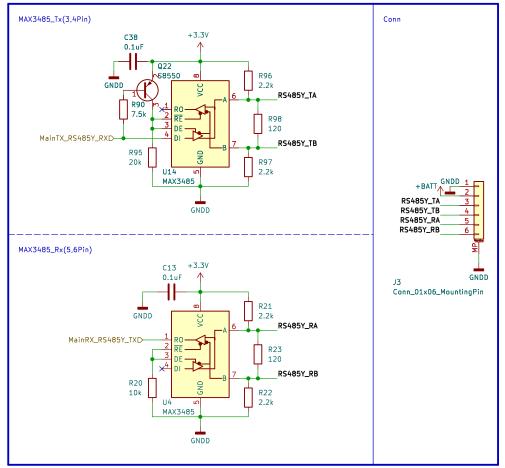


RS485_RingNW_X



RS485_RingNW_Y



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Trans to RS485 Panel0 Panel2 Panel4 Conn to Panel +3.3V +3.37 +3.37 C14 C35 0.1uF 0.1uF 0.1uF R75 2.2k \$8550 S8550 2.2k 2.2k RS485_Panel0 RS485_Panel2 RS485_Panel4 FPGARX_PanelOTXD FPGARX_Panel2TXD-FPGARX_Panel4TXD-FPGATX_PanelORXD-FPGATX_Panel2RXD-FPGATX_Panel4RXD-R25 20k R74 20k R27 20k R36 2.2k 2.2k 2.2k U11 MAX3485 MAX3485 MAX3485 RS485_PanelO 🚣 RS485_Panel1 RS485_Panel2 GNDD GNDD GNDD RS485_Panel3 Conn_01x08_Mountin RS485_Panel4 RS485_Panel5 Panel1 Panel3 Panel5 Panel_PWRCtrlD +3.3V +3.3V +3.3V C28 C33 C36 0.1uF 0.1uF 0.1uF GNDD GNDD Q3 Q5 Q19 GNDD R32 2.2k R39 2.2k R79 2.2k S8550 S8550 S8550 RS485_Panel1 RS485_Panel3 RS485_Panel5 FPGARX_Panel1TXD-FPGARX_Panel3TXD— FPGARX_Panel5TXD-FPGATX_Panel1RXD-FPGATX_Panel3RXD-FPGATX_Panel5RXD— R31 R38 R78 20k R33 20k R40 20k R87 2.2k 2.2k U12 2.2k MAX3485 MAX3485 MAX3485 GNDD GNDD GNDD Sheet: /FPGA_TangPrimerCore/RS485_Panel/ File: RS485_Panel.kicad_sch Title: Size: A4 Date: Rev: KiCad E.D.A. kicad 7.0.11 ld: 4/16

10SI/MISOはいつものとおんなじていいのか。

