



Lab Report- 03

Experiment Name: X-OR and O-NOR Gate Validation and De Morgan's Law Proved using NAND Gates

Course Code: ICT-2104

Course Name: Digital Logic Design Lab

Submitted to:

Dr. Md. Sazzadur Rahman

Professor

Institute of Information Technology

Jahangirnagar University

Submitted By:

Tonmoy Roy

Roll:1980

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Institute of Information Technology

Jahangirnagar University

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