Instruction	0	1	2	3	4	5	6	7	#	Num	Info
NOPO	0	0	0	0	0	0	0	0		0	Does no operation
LDIA	1	1	0	0	X	X	X	X		1	Loads a value from input (4-Bit) into Reg A
LDIB	1	0	1	0	X	X	X	X		2	Loads a value from input (4-Bit) into Reg B
ADDC	0	1	0	0	0	0	0	0		3	Adds values from Reg A and B into Reg C
HALT	0	1	1	0	0	0	0	0		4	Halts computation and stops the clock
SUBC	0	0	0	1	0	0	0	0		5	Subtracts values from Reg A and B into Reg C
OUTC	1	0	0	1	0	0	0	0		6	Outputs the value in Reg C to BUS
STRC	1	0	1	1	X	X	X	X		7	Stores the value in Reg C to RAM address
										8	
										9	
										10	
										11	
										12	
										13	
										14	
										15	

Instruction	0	1	2	3	4	5	6	7	#	Num	Info
1 - On											
0 - Off											
X - Value / Input											