Instruction	0	1	2	3	4	5	6	7	#	Num		Info
NOPO	0	0	0	0	0	0	0	0			0	Does no operation
LDIA	0	0	0	1	Χ	Χ	Χ	Х			1	Loads a value from input (4-Bit) into Reg A
LDIB	0	0	1	0	X	Χ	Χ	Χ			2	Loads a value from input (4-Bit) into Reg B
ADDC	0	1	0	1	0	0	0	0			3	Adds values from Reg A and B into Reg C
HALT	0	1	1	0	0	0	0	0			4	Halts computation and stops the clock.
SUBC	0	1	1	1	0	0	0	0			5	Subtracts values from Reg A and B into Reg C
OUTC	1	0	0	0	0	0	0	0			6	Outputs the value in Reg C to BUS.
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